Field Programmable Blank Oscillator

Series CPPL

- ***Programmed with the PG-2000P, PG-3000 field oscillator programming instrument within seconds***
- ***Can be programmed twice***
- ***Provides a sealed finished custom oscillator***
- ***Standard Package Options***
- ***Ultra low jitter @ 1 million samples***
- ***Power down and Tri-State options***

RoHS COMPLIANT 2002 / 95 / EC

Part Numbering Example: CPPL C 1 L Z - A5 B6 - XX.XXXX TS

CPPL	C	1	L	Z	A5	B6	XX.XXXX	TS
SERIES	OUTPUT	PACKAGE STYLE	VOLTAGE	ADDED FEATURES	OPERATING TEMP.	STABILITY	FREQUENCY	TRI-STATE
CPPL	C = CMOS T = TTL	1 = Full Size 4 = Half Size 5 = 3.2X5 Ceramic 7 = 5X7 Ceramic 8 = PLASTIC SMD 8B = PLASTIC SMD	Blank = 5V L = 3.3 V	Blank = Bulk T = Tube Z = Tape and Reel	Blank = 0°C +70°C A5 = -20°C +70°C A7 = -40°C +85°C	B6 = ±100 ppm BP = ±50 ppm BR = ±25 ppm	1.000~133.000 MHz	TS = Tri-State PD=PowerDwn

Specifications:

Description	Min	Typ	Max	Unit
Frequency Range: Programmable to Any Discrete Frequency	1.000		133.000	MHz
Available Stability Options:	-100		100	ppm
	-50		50	ppm
	-25		25	ppm
Programmable Input Voltage:				
(1–133 MHz)	4.5	5.0	5.5	V
(1–100 MHz)	3.0	3.3	3.6	V
Operating Temperature Range Options:	0		+70	°C
	-20		+70	°C
	-40		+85	°C
Storage Temperature:	-55		+125	°C
Aging (PPM/Year) Ta=25C, Vdd=5/3.3V			±5	
Programmable Output Level:	TTL/CMOS			
Packaging:	Tape and Reel (1K per Reel) Tube Bulk Shipping			

Operating Load Conditions:

	Description	Min	Max	Unit
Vdd	Supply Voltage	3.0	5.5	V
C_{TTL}	Max Capacitive Load on outputs for TTL levels			
	4.5V–5.5V Vdd ≤ 40 MHz		50	pF
	4.5V–5.5V Vdd > 40–133 MHz		25	pF
C_{CMOS}	Max Capacitive Load on outputs for CMOS levels			
	4.5V–5.5V Vdd, ≤ 66 MHz		50	pF
	4.5V–5.5V Vdd, >66–133 MHz		25	pF
	3.0V–3.6V Vdd, ≤ 40 MHz		30	pF
	3.0V–3.6V Vdd, >40–100 MHz		15	pF

Cardinal Components, Inc.
155 Route 46 West
Wayne, NJ 07470
Rev: C-090414-11

TEL: (973)785-1333
E-MAIL: sales@cardinalxtal.com
WEB: http://www.cardinalxtal.com