EF-21935-000 SHT I.I NOTES:  $-1,60\pm0,05$  $[.063\pm.002]$ 2,82±0,05 → ➤ A POSITIVE GOING VOLTAGE AT TERMINAL 2, [110+002]RELATIVE TO TERMINAL I, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET. ➤ LOCATED FROM TWO SURFACES FOR CUSTOMER  $7.87 \pm 0.05$ CONVENIENCE. ONLY APPLICABLE FROM ONE  $[.310 \pm .002]$ SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED TO  $\pm 0$ , |7 [.007].  $5,59\pm0,05 [.220\pm.002]$  $[.097 \pm .004]$  $1,41\pm0,04$  $[.0555\pm.0015]$ -TERMINAL 2 ERMINAL I-OUTER DIAMETER (POSITIVE) (NEGATIVE) 2,08±0,08 [.082±.003]  $4,01\pm0,05$  $[.158\pm.002]$  $1,99\pm0,08$ .0785±.0030]  $3,21\pm0,08$  $93\pm0.08$  $[.1265\pm.0030]$  $2,02\pm0,08$  $[.076 \pm .003]$ [.0795 + .00301]0.64  $0,80\pm0,08$ [.025]  $[.0315 \pm .0030]$ CENTERTAP-MAXIMUM TERMINAL SOLDER BUILDUP Revision C.O. # Implementation Date RELEASE LEVEL REVISION NOMINAL WEIGHT Released .66 GRAM DIMENSIONS IN MILLIMETERS [INCHES] C10103490 11-28-05 SCALE: 2:1 **KNOWLES ELECTRONICS** MMM 11-28-05 DO NOT SCALE DRAWING

TITLE:

RECEIVER

OUTLINE DRAWING

ITASCA, ILLINOIS U.S.A.

CK. BY

GJP

APP. BY

EF-21935-000

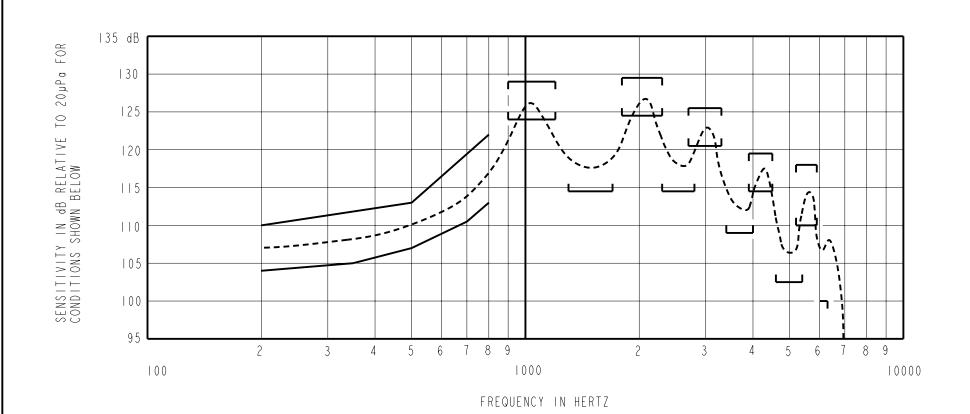
SHT I.I

DATE

12-1-05

DATE

12-1-05



## NOTES:

I. MEASUREMENTS MADE USING 8mm (.315") X 1mm (.039") 1D + 28mm (1.10") X 1.5mm (.059") 1D EAR HOOK SIMULATOR INTO 25mm (.984") OF 2mm (.079") ID TUBE + 18mm (.709") OF 3mm (.118") ID TUBE + 2cm<sup>3</sup> CAVITY (AS IEC126). (T-2607 AND B & K DB0138).

2. <u>SENSITIVITY</u>
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FREQUENCY	MIN.	MAX.
200	104.0	110.0
350	105.0	
500	107.0	113.0
700	110.5	
800	113.0	122.0
900-1200	124.0	129.0
1300-1700	114.5	
1800-2300	124.5	129.5
2300-2800	114.5	
2700-3300	120.5	125.5
3400-4000	109.0	
3900-4500	114.5	119.5
4600-5400	102.5	
5200-5900	110.0	118.0
<6300	100.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES IKHZ IMPEDANCE FOR TEST CONDITIONS SHOWN BELOW.
- 5. INDIVIDUAL SPECIFICATIONS.

PORT	IMPEDANCE OHMS ±15%		DCR @20°C	DISTORTION		ELECTRICAL TEST CONDITIONS	
LOCATION	IKHz	500Hz	OHMS ±10%	MAX. %	FREQ Hz	AC mA RMS	DC mA
128	327	128	48.5	10	500	1.56	0.00

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
		Released	l Released	ΙΔΙ
А	C10103490	11-28-05		/ \

**KNOWLES ELECTRONICS** ITASCA, ILLINOIS U.S.A.

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			DATE
			11-28-05
			DATE
TITLE: RECEIVER	EF-21935-000	GJP	12-1-05
NEOC I VEN	2. 2.000 000	APP. BY	DATE
PERFORMANCE SPECIFICATION	SHT 2.1	GJP	12-1-05

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