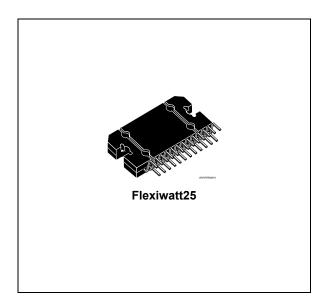




### **TDA7388**

## 4 x 45 W quad bridge car radio amplifier

#### **Datasheet - production data**



#### **Features**

- High output power capability:
  - $-4 \times 45 \text{ W} / 4 \Omega \text{ max}.$
  - 4 x 26 W / 4  $\Omega$  @ 14.4 V, 1 kHz, 10 %
- Low distortion
- · Low output noise
- Standby function
- Mute function
- Automute at min. supply voltage detection

- Low external component count:
  - Internally fixed gain (26 dB)
  - No external compensation
  - No bootstrap capacitors

#### **Protections:**

- Output short circuit to gnd, to V<sub>S</sub>, across the load
- Very inductive loads
- Overrating chip temperature with soft thermal limiter
- Load dump voltage
- Fortuitous open GND
- · Reversed battery
- ESD

### **Description**

The TDA7388 is an AB class audio power amplifier, packaged in Flexiwatt 25 and designed for high end car radio applications.

Based on a fully complementary PNP/NPN configuration, the TDA7388 allows a rail to rail output voltage swing with no need of bootstrap capacitors. The extremely reduced boundary components count allows very compact sets.

Table 1. Device summary

Order code	Package	Packing
TDA7388	Flexiwatt25	Tube



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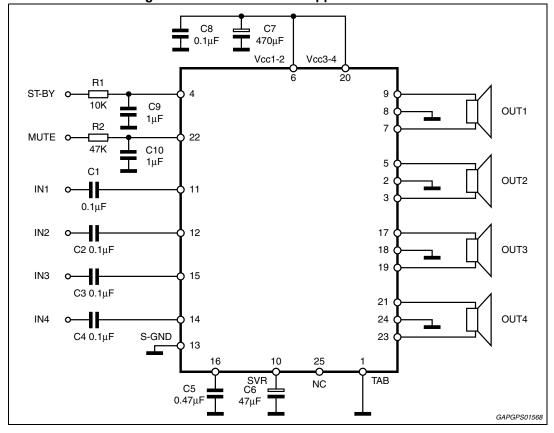


## Pin connection and test/application diagrams

25 4 N 8 N SVR AC-GND S-GND OUT3+ Z Z

Figure 1. Pin connection (top view)

Figure 2. Standard test and application circuit







# 2 Electrical specifications

### 2.1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V <sub>S</sub>	Operating supply voltage	18	V	
V <sub>S (DC)</sub>	DC supply voltage	28	V	
V <sub>S (pk)</sub>	Peak supply voltage (t = 50 ms)	50	V	
I <sub>O</sub>	Output peak current: Repetitive (duty cycle 10 % at f = 10 Hz) Non repetitive (t = 100 µs)	4.5 5.5	А	
P <sub>tot</sub>	Power dissipation, (T <sub>case</sub> = 70 °C)	80	W	
T <sub>j</sub>	Junction temperature	150	°C	
T <sub>stg</sub>	Storage temperature	– 55 to 150	°C	

### 2.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>th j-case</sub>	Thermal resistance junction-to-case max.	1	°C/W



### 2.3 Electrical characteristics

 $V_S$  = 14.4 V; f = 1 kHz;  $R_g$  = 600  $\Omega$ ;  $R_L$  = 4  $\Omega$ ;  $T_{amb}$  = 25 °C; Refer to the test and application diagram (*Figure 2*), unless otherwise specified.

**Table 4. Electrical characteristics** 

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
I <sub>q1</sub>	Quiescent current	R <sub>L</sub> = ∞	120	190	350	mA
V <sub>OS</sub>	Output offset voltage	Play mode	-	-	±100	mV
dV <sub>OS</sub>	During mute ON/OFF output offset voltage	ITU R-ARM weighted see Figure 12	-80	-	+80	mV
G <sub>v</sub>	Voltage gain	-	25	26	27	dB
Po	Output power	THD = 10 %; V <sub>S</sub> = 14.4 V	22	26	-	W
D	Max.output power <sup>(1)</sup>	V <sub>S</sub> = 14.4 V	37	41	-	W
P <sub>o max</sub>	Max.output power	V <sub>S</sub> = 15.2 V	-	45	-	
THD	Distortion	P <sub>o</sub> = 4 W	-	0.04	0.15	%
	Output noise	"A" Weighted	-	50	70	μV
e <sub>No</sub>	Output noise	Bw = 20 Hz to 20 kHz	-	70	100	μV
SVR	Supply voltage rejection	f = 100 Hz; V <sub>r</sub> = 1 Vrms	50	65	-	dB
f <sub>ch</sub>	High cut-off frequency	P <sub>o</sub> = 0.5 W	100	200	-	kHz
R <sub>i</sub>	Input Impedance	-	70	100	-	kΩ
C	Cross talk	f = 1 kHz; P <sub>o</sub> = 4 W	60	70	-	dB
C <sub>T</sub>	Cross talk	f = 10 kHz; P <sub>o</sub> = 4 W	-	60	-	dB
I <sub>SB</sub>	Standby current consumption	V <sub>St-by</sub> = 0V	-	-	20	μΑ
V <sub>SB out</sub>	Standby OUT threshold voltage	(Amp: ON)	3.5	-	-	V
V <sub>SB IN</sub>	Standby IN threshold voltage	(Amp: OFF)	-	-	1.5	V
A <sub>M</sub>	Mute attenuation	P <sub>Oref</sub> = 4 W	80	90	-	dB
V <sub>M out</sub>	Mute OUT threshold voltage	(Amp: play)	3.5	-	-	V
V <sub>M in</sub>	Mute IN threshold voltage	(Amp: mute)	-	-	1.5	V
$V_{AM\ in}$	V <sub>S</sub> automute threshold	(Amp: mute); Att. ≥ 80 dB; P <sub>Oref</sub> = 4 W (Amp: play); Att. < 0.1 dB; P <sub>O</sub> = 0.5 W	-	7.6	6.5 8.5	V
I <sub>pin22</sub>	Muting pin current	V <sub>MUTE</sub> = 1.2 V (Source current)	5	11	20	μA

<sup>1.</sup> Saturated square wave output.



#### 2.4 Electrical characteristic curves

Figure 3. Quiescent current vs. supply voltage

Figure 4. Output power vs. supply voltage (4 Ohm)

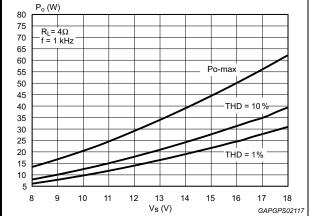
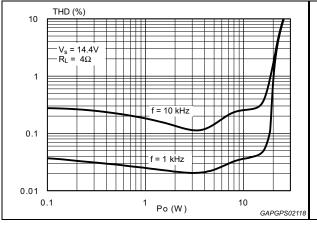


Figure 5. Distortion vs. output power (4 Ohm)

Figure 6. Distortion vs. frequency (4 Ohm)



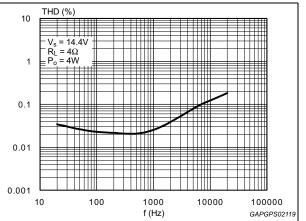
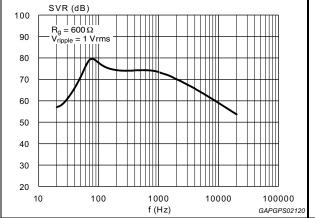
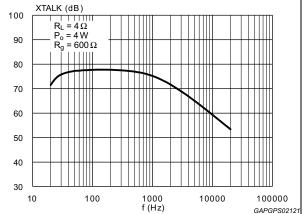


Figure 7. Supply voltage rejection vs. frequency

Figure 8. Crosstalk vs. frequency



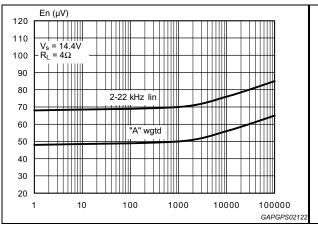


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Figure 9. Output noise vs. source resistance

Figure 10. Total power disipation & efficiency (4 Ohm, sine)



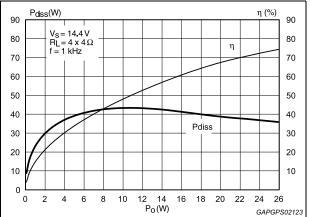
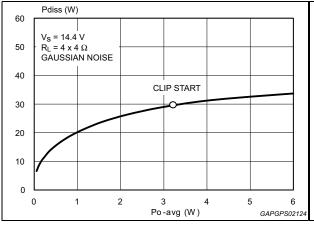
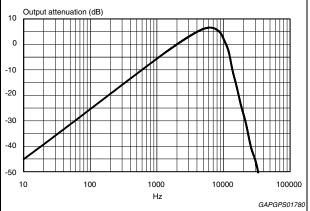


Figure 11. Power dissipation vs. average output power (4 Ohm, audio program simulation)

Figure 12. ITU R-ARM frequency response, weighting filter for transient pop







Application hints TDA7388

### 3 Application hints

Ref. to the circuit of Figure 2.

#### 3.1 SVR

Besides its contribution to the ripple rejection, the SVR capacitor governs the turn ON/OFF time sequence and, consequently, plays an essential role in the pop optimization during ON/OFF transients.

To conveniently serve both needs, its minimum recommended value is 10 µF.

### 3.2 Input stage

The TDA7388's inputs are ground-compatible and can stand very high input signals (±8 Vpk) without any performances degradation.

If the standard value for the input capacitors (0.1  $\mu$ F) is adopted, the low frequency cut-off amounts to 16 Hz.

### 3.3 Standby and muting

If standby and muting are not used, a straight connection to  $V_{\mbox{\scriptsize S}}$  of their respective pins would be admissible.

Conventional/low-power transistors can be employed to drive muting and standby pins in absence of true CMOS ports or microprocessors. R-C cells have always to be used in order to smooth down the transitions for preventing any audible transient noises.

Since a DC current of about 10  $\mu$ A normally flows out of pin 22, the maximum allowable muting-series resistance (R<sub>2</sub>) is 70 k $\Omega$ , which is sufficiently high to permit a muting capacitor reasonably small (about 1  $\mu$ F).

If  $R_2$  is higher than recommended, the involved risk is that the voltage at pin 22 may rises to above the 1.5 V threshold voltage and the device consequently fails to turn OFF when the mute line is brought down.

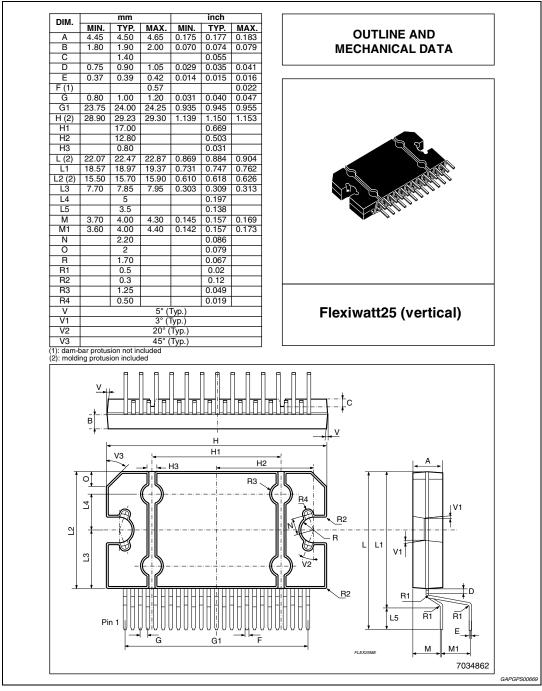
About the stand-by, the time constant to be assigned in order to obtain a virtually pop-free transition has to be slower than 2.5 V/ms.

#### **Package information** 4

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com.

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Figure 13. Flexiwatt25 mechanical data and package dimensions







Revision history TDA7388

# 5 Revision history

**Table 5. Document revision history** 

Date	Revision	Changes
06-Dec-2007	1	Initial release.
12-Jul-2010	2	Document status promoted from preliminary data to datasheet.
26-Apr-2012	3	Modified Features on page 1. Updated Table 4: Electrical characteristics on page 7.
20-Jun-2012	4	Updated Section 3.3: Standby and muting.
11-Mar-2013	5	Added Section 2.4: Electrical characteristic curves.
17-Sep-2013 6 Updated Disclaimer.		Updated Disclaimer.



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