DMS935E1

Silicon NPN epitaxial planar type (Tr) Silicon epitaxial planar type (CCD load device)

For CCD output circuits
DSC2G03 + CCD load device (Individual)

■ Features

- Two elements incorporated into one package (Tr + CCD load device)
- High transition frequency f_T
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

■ Marking Symbol: X0

■ Packaging

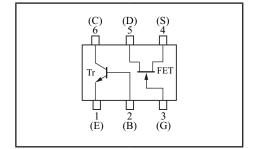
DMS935E10R Embossed type (Thermo-compression sealing): 8 000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit
Tr1	Collector-base voltage (Emitter open)	V _{CBO}	30	V
	Collector-emitter voltage (Base open)	V _{CEO}	20	V
	Emitter-base voltage (Collector open)	V _{EBO}	3	V
	Collector current	I _C	50	mA
CCD	Limiting element voltage	V _{max}	40	V
load device	Limiting element current	I _{max}	10	mA
Overall	Total power dissipation *1	P _T	125	mW
	Junction temperature	T _j	150	°C
	Operating ambient temperature	T _{opr}	-40 to +85	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Note) *1: Measuring on substrate at 17 mm \times 10 mm \times 1 mm

Unit: mm 1.6 0.2 0.13 (0.6)(0.5)(0.5)1: Emitter 4: Source 2: Base 5: Drain 3: Gate 6: Collector Panasonic SSMini6-F3-B **JEITA** SC-107C Code SOT-666



■ Electrical Characteristics $T_a = 25$ °C±3°C

• Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 100 \mu\text{A}, I_E = 0$	30			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \mu A, I_C = 0$	3			V
Base-emitter voltage	V _{BE}	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$		740		mV
Forward current transfer ratio	h_{FE}	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$	100		250	_
Transition frequency *1	f_T	$V_{CE} = 10 \text{ V}, I_{C} = 15 \text{ mA}$		1300		MHz

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

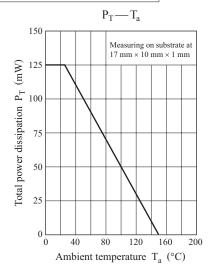
• CCD load device

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Pinchi off current	I_P	$V_{DS} = 10 \text{ V}, V_G = 0$	3.8		5.2	mA
Output impedance	Z _O	$V_{DS} = 10 \text{ V}, V_G = 0$		0.05		ΜΩ

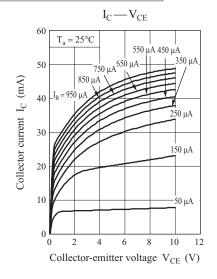
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

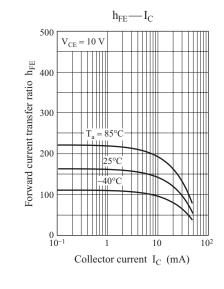
^{2. *1:} Pulse measurement

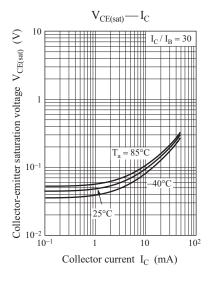
Common characteristics chart

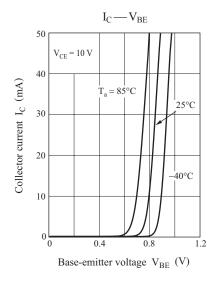


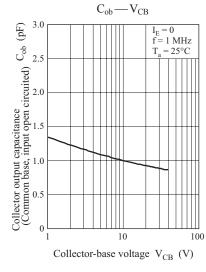
Characteristics charts of Tr

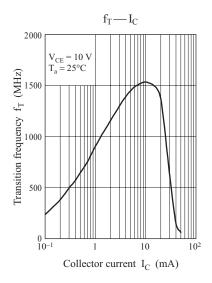




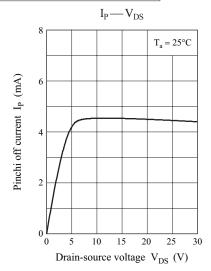






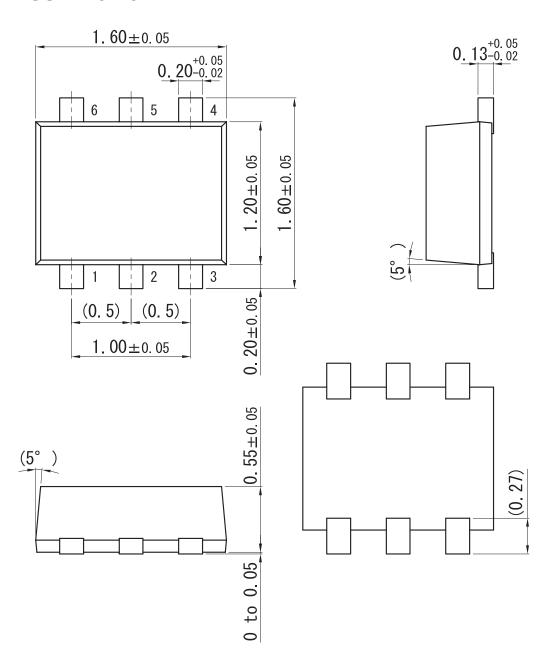


Characteristics charts of CCD

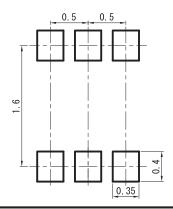


SSMini6-F3-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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