

NSVJ5908DSG5

Advance Information

N-Channel JFET

–15 V, 10 to 32 mA, 35 mS, Dual

Automotive JFET designed for compact and efficient designs and including high gain performance. AEC-Q101 qualified JFET and PPAP capable suitable for automotive applications.

Features

- Large |yfs|
- Small Ciss
- This small package enables sets to be smaller and thinner
- Ultralow noise figure
- Pb-Free, Halogen Free and RoHS compliance
- MCPH5 package is pin-compatible with SC-88AFL
- AEC-Q101 qualified and PPAP capable
- Composite type with 2 JFET contained in a MCPH5 package currently in use, improving the mounting efficiency greatly
- The NSVJ5908DSG5 is formed with two chips, being equivalent to the NSVJ3557SA3, placed in one package

Typical Applications

- AM Tuner RF Amplification
- Low Noise Amplifier

SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSX}	15	V
Gate-to-Drain Voltage	V _{GDS}	–15	V
Gate Current	I _G	10	mA
Drain Current	I _D	50	mA
Allowable Power Dissipation 1 unit	P _D	200	mW
Total Power Dissipation	P _T	300	mW
Operating Junction and Storage Temperature	T _J , T _{Stg}	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

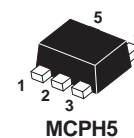
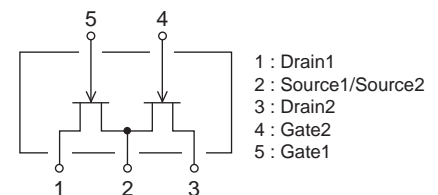
This document contains information on a new product. Specifications and information herein are subject to change without notice.



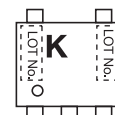
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ELECTRICAL CONNECTION N-Channel



MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet

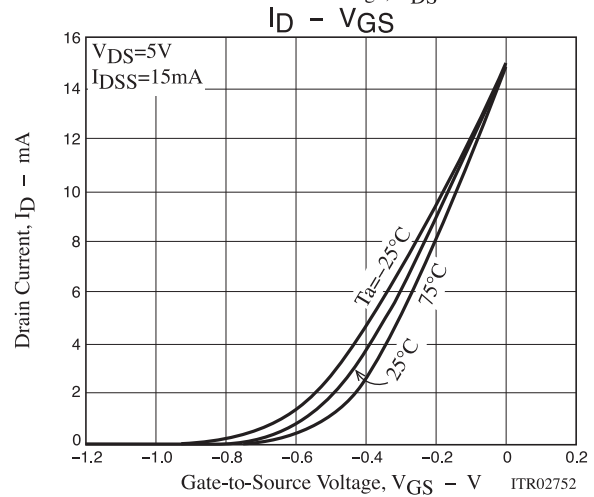
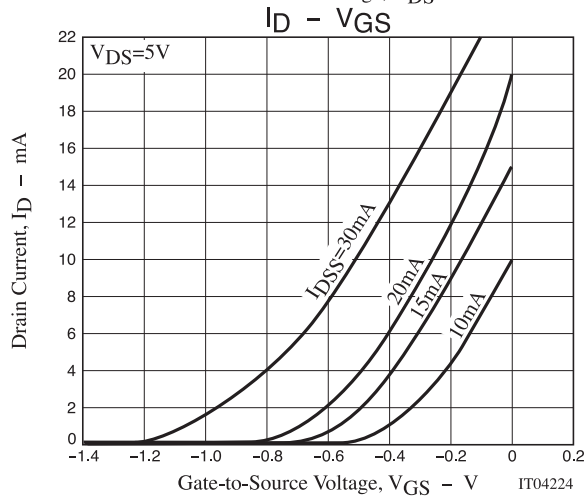
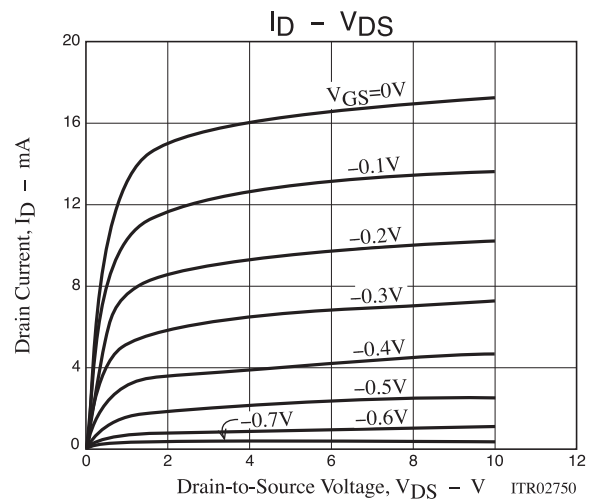
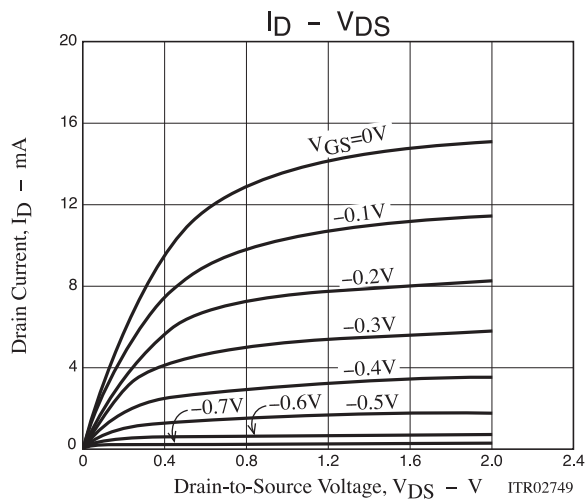
NSVJ5908DSG5

ELECTRICAL CHARACTERISTICS at Ta = 25°C (Notes 2,3)

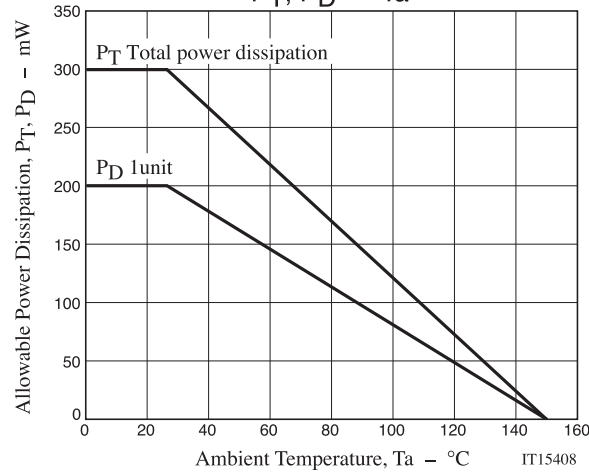
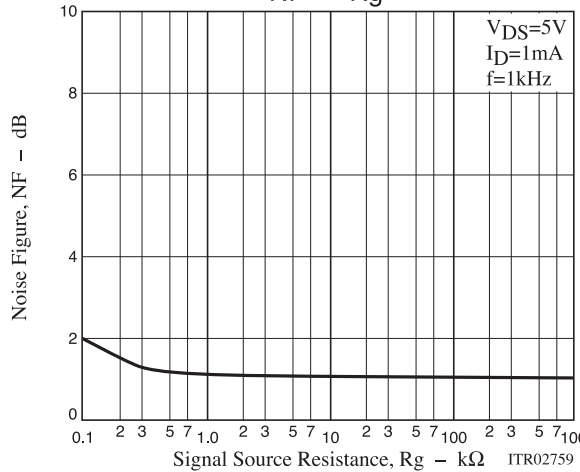
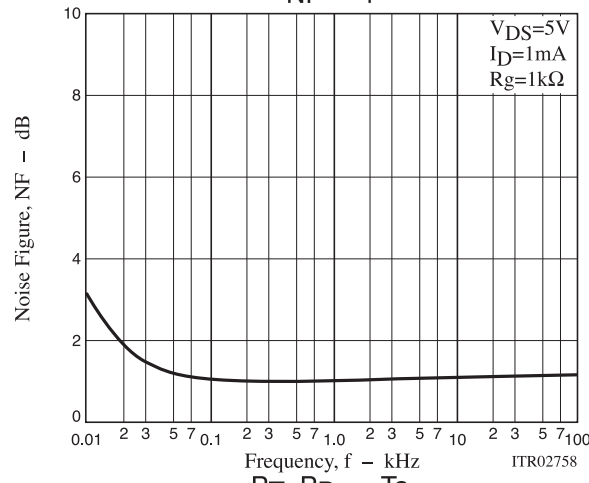
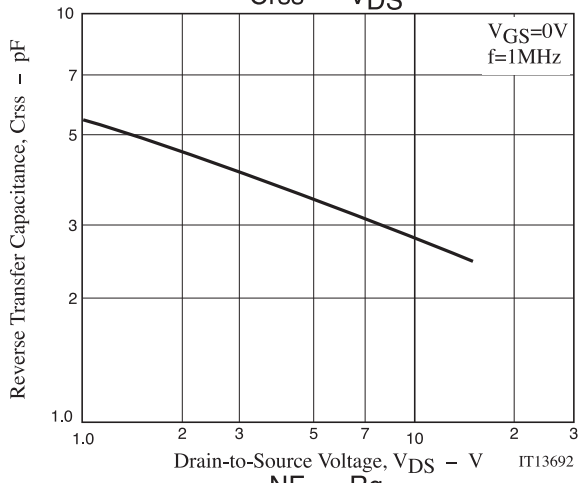
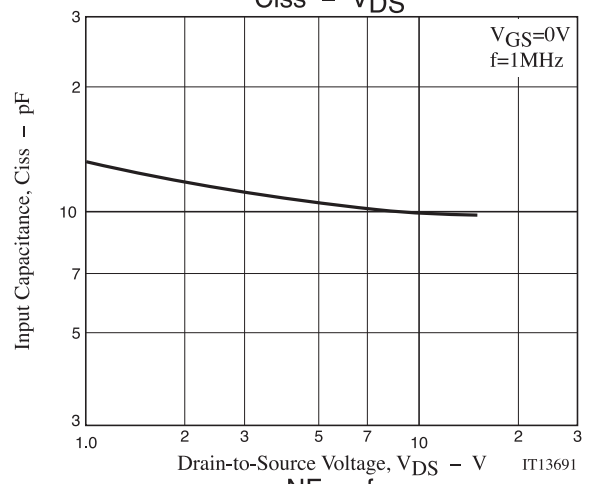
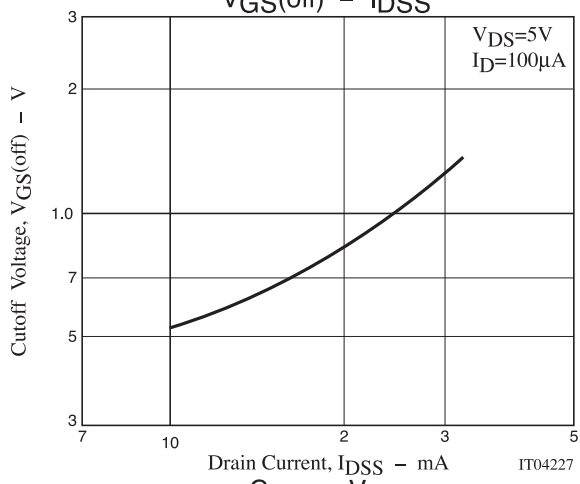
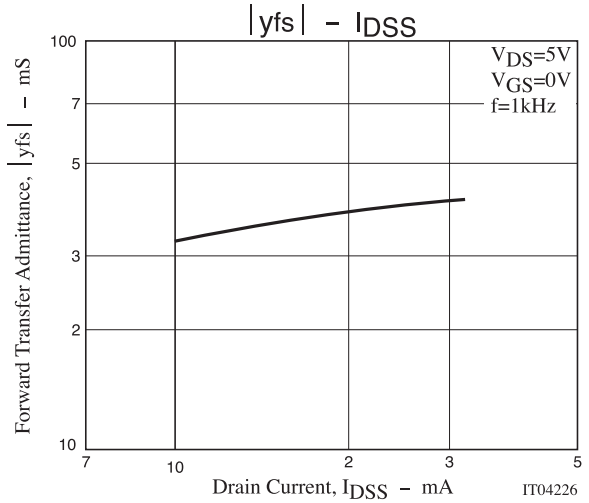
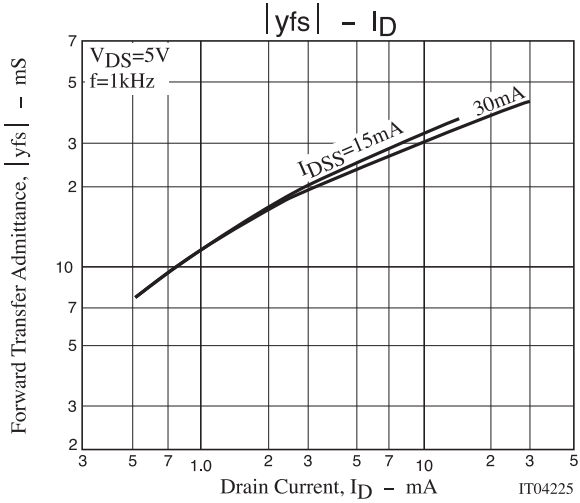
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Gate-to-Drain Breakdown Voltage	$V_{(BR)GDS}$	$I_G = -10 \mu A, V_{DS} = 0 V$	-15			V
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = -10 V, V_{DS} = 0 V$			-1.0	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = 5 V, I_D = 100 \mu A$	-0.3	-0.7	-1.5	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 5 V, V_{GS} = 0 V$	10		32	mA
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 5 V, V_{GS} = 0 V, f = 1 kHz$	24	35		mS
Input Capacitance	C_{iss}	$V_{DS} = 5 V, V_{GS} = 0 V, f = 1 MHz$		10.5		pF
Reverse Transfer Capacitance	C_{rss}				3.5	
Noise Figure	NF	$V_{DS} = 5 V, R_g = 1 k\Omega, I_D = 1 mA, f = 1 kHz$		1.0		dB

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Note 3 : The specifications shown above are for each individual JFET.



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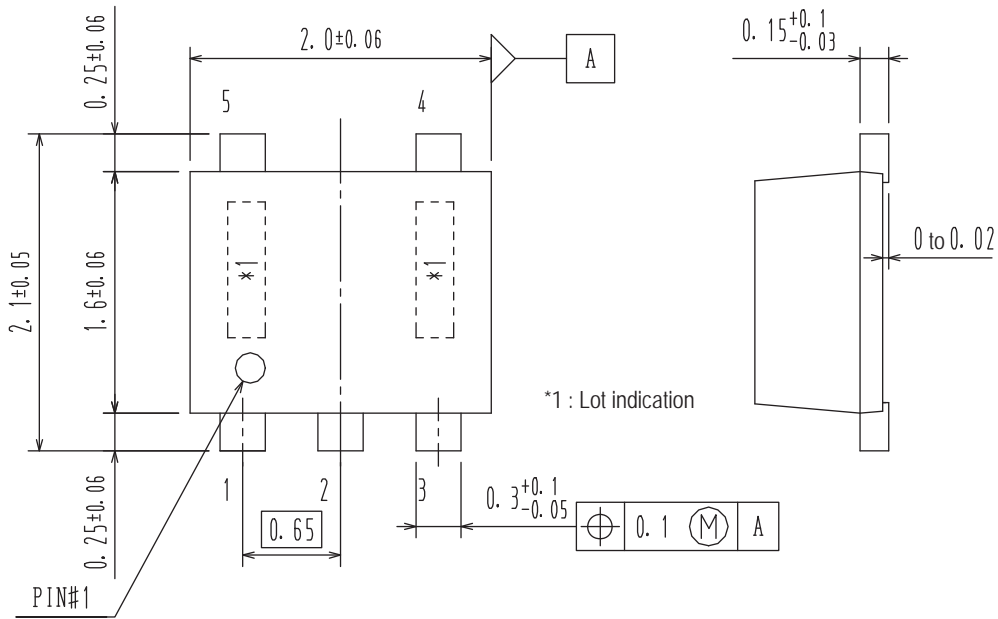
PACKAGE DIMENSIONS

unit : mm

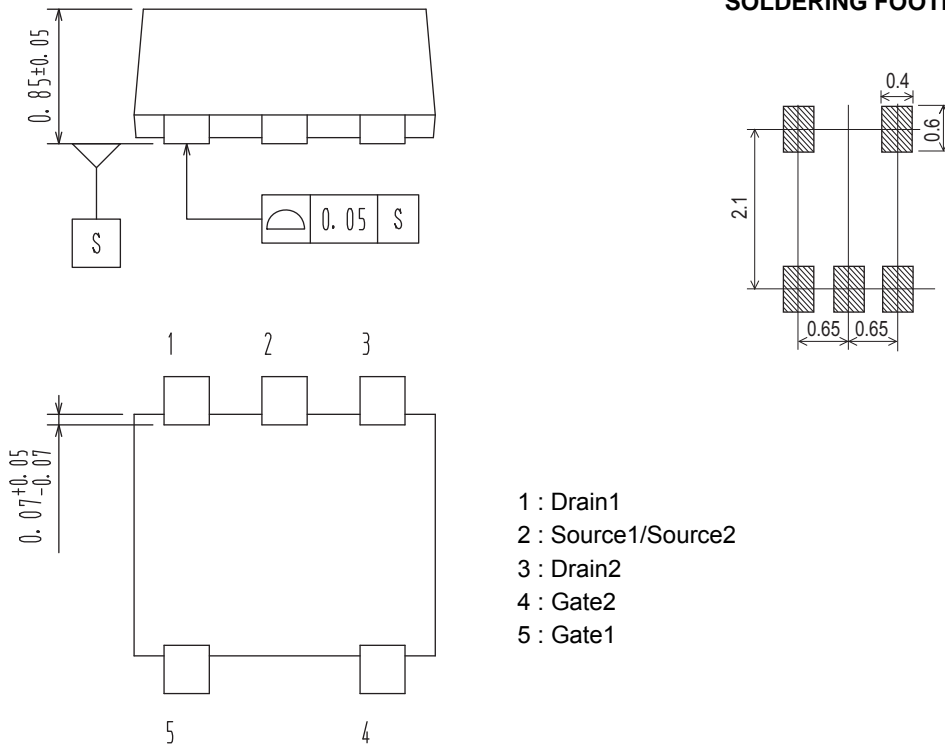
SC-88AFL / MCPH5

CASE 419AP

ISSUE 0



RECOMMENDED SOLDERING FOOTPRINT



NSVJ5908DSG5

ORDERING INFORMATION

Device	Marking	Package	Shipping
NSVJ5908DSG5T1G	K	SC-88AFL / MCPH5 (Pb-Free / Halogen Free)	3,000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

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