IGBT - Field Stop, Trench

650 V, 75 A

FGH75T65SHD

Description

Using novel field stop IGBT technology, ON Semiconductor's new series of field stop 3rd generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.

Features

- Maximum Junction Temperature: $T_J = 175$ °C
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.6 \text{ V (Typ.)} @ I_C = 75 \text{ A}$
- 100% of the Parts Tested for I_{LM} (Note 1)
- High Input Impedance
- Fast Switching
- Tighten Parameter Distribution
- This Device is Pb-Free and is RoHS Compliant

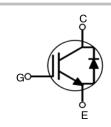
Applications

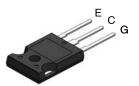
• Solar Inverter, UPS, Welder, PFC, Telecom, ESS, PFC



ON Semiconductor®

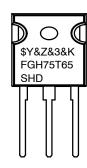
www.onsemi.com





TO-247-3LD CASE 340CH

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code &K = Lot Code

FGH75T65SHD = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS

Description		Symbol	FGH75T65SHD-F155	Unit
Collector to Emitter Voltage		V _{CES}	650	V
Gate to Emitter Voltage		V_{GES}	±20	V
Transient Gate to Emitter Voltage			±30	V
Collector Current	T _C = 25°C	I _C	150	Α
Collector Current	T _C = 100°C		75	Α
Pulsed Collector Current (Note 1)	T _C = 25°C	I _{LM}	225	Α
Pulsed Collector Current (Note 2)		I _{CM}	225	Α
Diode Forward Current	T _C = 25°C	I _F	75	Α
Diode Forward Current	T _C = 100°C		50	Α
Pulsed Diode Maximum Forward Current	(Note 2)	I _{FM}	225	Α
Maximum Power Dissipation	T _C = 25°C	P_{D}	455	W
Maximum Power Dissipation	T _C = 100°C		227	W
Operating Junction Temperature		TJ	-55 to +175	°C
Storage Temperature Range		T _{stg}	-55 to +175	°C
Maximum Lead Temp. for Soldering Purpo	oses, 1/8" from Case for 5 Seconds	T _L	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $V_{CC} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_{C} = 225 \text{ A}$, $R_{G} = 20 \Omega$, Inductive Load

2. Repetitive Rating: Pulse width limited by max. junction temperature.

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Case (IGBT)	$R_{ heta JC}$	0.33	°C/W
Thermal Resistance, Junction to Case (Diode)	$R_{ heta JC}$	0.65	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGH75T65SHD	FGH75T65SHD-F155	TO-247-3LD	ı	-	30

ELECTRICAL CHARACTERISTICS OF THE IGBT (T_C = 25°C unless otherwise noted)

Parameter	Symbol Test Conditions		Min	Тур	Max	Unit
OFF CHARACTERISTICS			•	•	-	
Collector to Emitter Breakdown Voltage	BV _{CES}	V _{GE} = 0 V, I _C = 1 mA	650	_	_	V
Temperature Coefficient of Breakdown Voltage	$\Delta BV_{CES}/\Delta T_{J}$	I _C = 1 mA, Referenced to 25°C		0.6		V/°C
Collector Cut-Off Current	I _{CES}	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μΑ
G-E Leakage Current	I _{GES}	V _{GE} = V _{GES} , V _{CE} = 0 V	_	_	±400	nA
ON CHARACTERISTICS						
G-E Threshold Voltage	V _{GE(th)}	I_C = 75 mA, V_{CE} = V_{GE}	4.0	5.5	7.5	V
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C = 75 A, V _{GE} = 15 V	-	1.6	2.1	V
		I _C = 75 A, V _{GE} = 15 V, T _C = 175°C	-	2.28	-	V

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25^{\circ}C$ unless otherwise noted) (continued)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS		•				
Input Capacitance	C _{ies}	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	-	3680	_	pF
Output Capacitance	C _{oes}		-	179	-	pF
Reverse Transfer Capacitance	C _{res}		-	43	-	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(on)}	$V_{CC} = 400 \text{ V}, I_{C} = 75 \text{ A},$	-	28	-	ns
Rise Time	t _r	$R_G = 3 \Omega$, $V_{GE} = 15 V$, Inductive Load, $T_C = 25^{\circ}C$	-	56	-	ns
Turn-Off Delay Time	t _{d(off)}		-	80	-	ns
Fall Time	t _f		-	14.4	-	ns
Turn-On Switching Loss	E _{on}		-	2.4	-	mJ
Turn-Off Switching Loss	E _{off}		-	0.72	_	mJ
Total Switching Loss	E _{ts}		-	3.12	_	mJ
Turn-On Delay Time	t _{d(on)}	$V_{CC} = 400 \text{ V, } I_{C} = 75 \text{ A,}$ $R_{G} = 3 \Omega, V_{GE} = 15 \text{ V,}$ Inductive Load, $T_{C} = 175^{\circ}\text{C}$	-	26.4	_	ns
Rise Time	t _r		-	58.4	_	ns
Turn-Off Delay Time	t _{d(off)}		-	86.4	_	ns
Fall Time	t _f		-	13.6	_	ns
Turn-On Switching Loss	E _{on}		-	3.7	_	mJ
Turn-Off Switching Loss	E _{off}		-	0.98	_	mJ
Total Switching Loss	E _{ts}		-	4.68	_	mJ
Total Gate Charge	Qg	V _{CC} = 400 V, I _C = 75 A,	-	123	-	nC
Gate to Emitter Charge	Q _{ge}	V _{GE} = 15 V	-	22.6	-	nC
Gate to Collector Charge	Q _{gc}	1	-	44.9	_	nC

ELECTRICAL CHARACTERISTICS OF THE DIODE ($T_J = 25$ °C unless otherwise noted)

Parameter	Symbol	Test Condi	tions	Min	Тур	Max	Unit
Diode Forward Voltage	V _{FM}	I _F = 50 A	T _C = 25°C	-	2.2	2.7	V
			T _C = 175°C	-	1.8	-	1
Reverse Recovery Energy	E _{rec}	I _F = 50 A,	T _C = 175°C	-	60	-	μJ
Diode Reverse Recovery Time	t _{rr}	dI _F /dt = 200 A/μs	T _C = 25°C	-	43.4	-	ns
			T _C = 175°C	-	207	-	
Diode Reverse Recovery Charge	Q _{rr}	1	T _C = 25°C	_	87.9	-	nC
			T _C = 175°C	-	1243	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CHARACTERISTICS

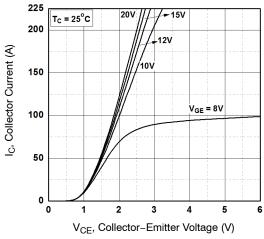


Figure 1. Typical Output Characteristics

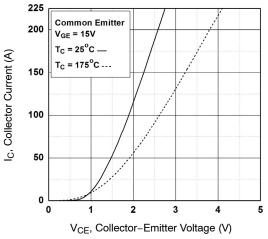


Figure 3. Typical Saturation Voltage Characteristics

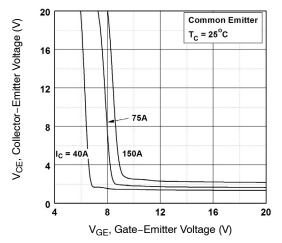


Figure 5. Saturation Voltage vs V_{GE}

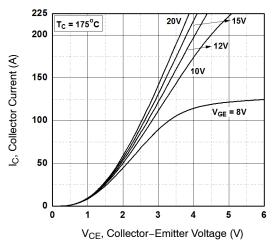


Figure 2. Typical Output Characteristics

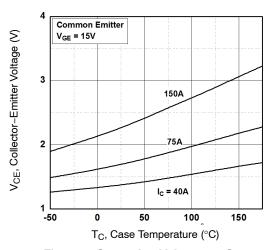


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

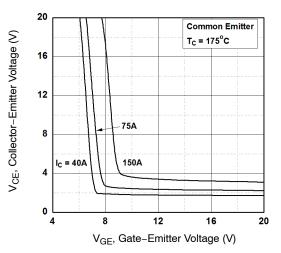


Figure 6. Saturation Voltage vs V_{GE}

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

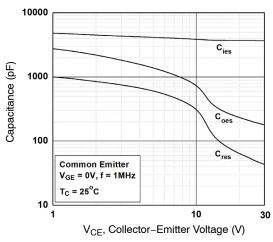
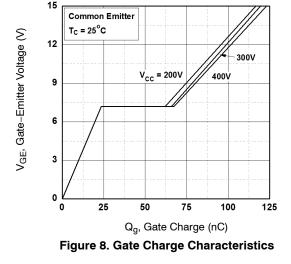


Figure 7. Capacitance Characteristics



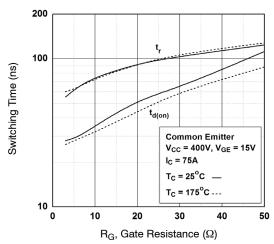


Figure 9. Turn-On Characteristics vs. Gate Resistance

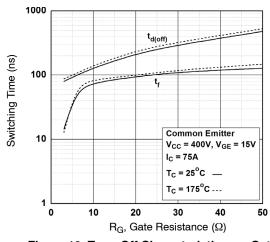


Figure 10. Turn-Off Characteristics vs. Gate Resistance

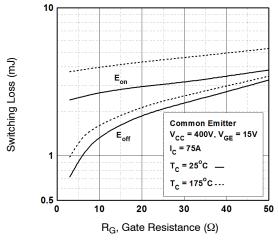


Figure 11. Switching Loss vs. Gate Resistance

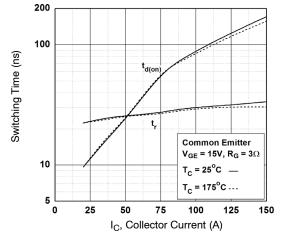


Figure 12. Turn-On Characteristics vs. Collector Current

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

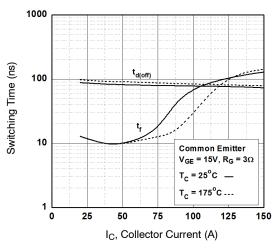


Figure 13. Turn-Off Characteristics vs. Collector Current

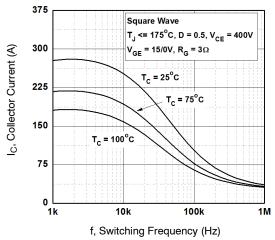


Figure 15. Load Current vs. Frequency

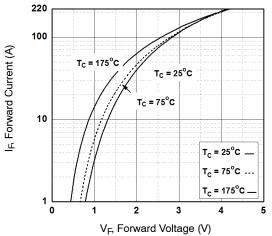


Figure 17. Forward Characteristics

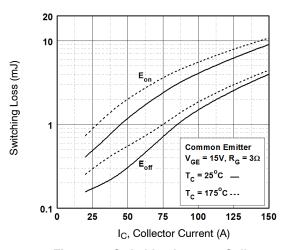


Figure 14. Switching Loss vs. Collector Current

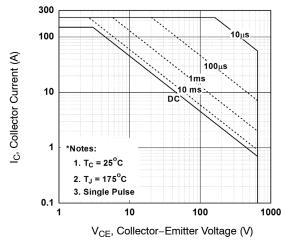


Figure 16. SOA Characteristics

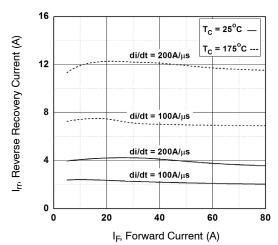


Figure 18. Reverse Recovery Current

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

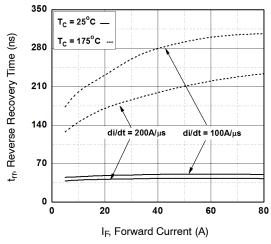


Figure 19. Reverse Recovery Time

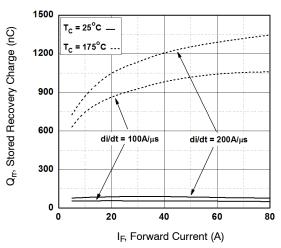


Figure 20. Stored Charge

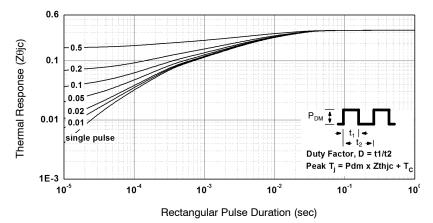


Figure 21. Transient Thermal Impedance of IGBT

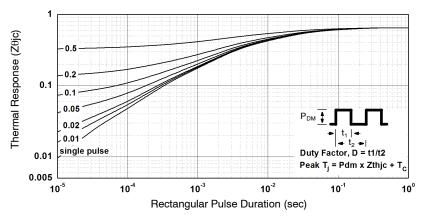
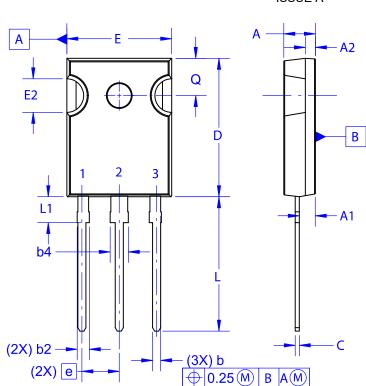


Figure 22. Transient Thermal Impedance of Diode

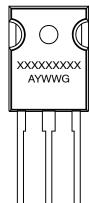
TO-247-3LD CASE 340CH **ISSUE A**





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC **MARKING DIAGRAM***



XXXX = Specific Device Code

= Assembly Location

WW = Work Week

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

	DATE 09 OCT 2019				
ØP —		,		Ø P1 D2	
S E1 —				D1	
21		2			
,				9	

DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A 1	2.29	2.475	2.66			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
ØP1	6.61	6.73	6.85			

DOCUMENT NUMBER:	98AON13853G	Electronic versions are uncontrolled except when accessed directly from the Document Repo Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	TO-247-3LD		PAGE 1 OF 1		

ON Semiconductor and unare trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMi., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer p

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative