

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended  
for new design

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N-CHANNEL MOS FIELD EFFECT TRANSISTOR  
 FOR SWITCHING

DESCRIPTION

The  $\mu$ PA622TT is a switching device which can be driven directly by a 4.0 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 4.0 V drive available
- Low on-state resistance
- $R_{DS(on)1} = 82 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 10 \text{ V}$ ,  $I_D = 1.5 \text{ A}$ )
- $R_{DS(on)2} = 120 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 4.5 \text{ V}$ ,  $I_D = 1.0 \text{ A}$ )
- $R_{DS(on)3} = 139 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 4.0 \text{ V}$ ,  $I_D = 1.0 \text{ A}$ )

★ ORDERING INFORMATION

PART NUMBER	PACKAGE
$\mu$ PA622TT-E1-A	6 pin WSOF (1620)
$\mu$ PA622TT-E2-A	

**Remark** "-A" indicates Pb-free (This product does not contain Pb in external electrode and other parts.).  
 "-E1" or "-E2" indicates the unit orientation.  
 (8 mm embossed carrier tape, 3000 pcs/reel)

Marking: WC

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

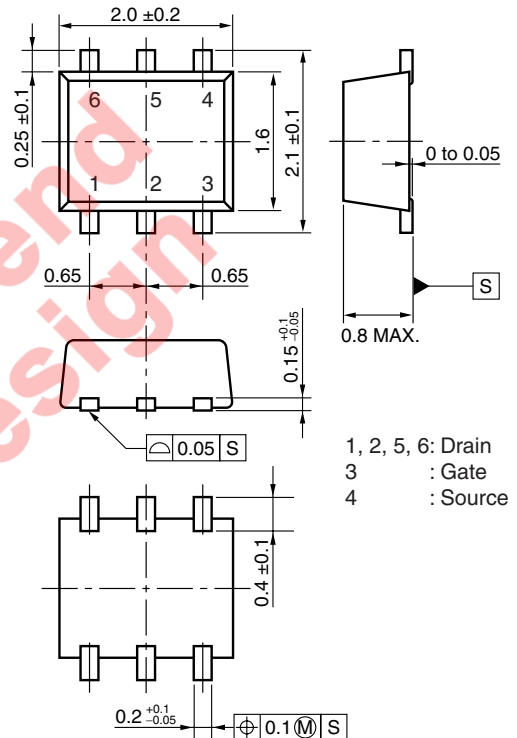
Drain to Source Voltage ( $V_{GS} = 0 \text{ V}$ )	$V_{DSS}$	30	V
Gate to Source Voltage ( $V_{DS} = 0 \text{ V}$ )	$V_{GSS}$	$\pm 20$	V
Drain Current (DC)	$I_{D(DC)}$	$\pm 3.0$	A
Drain Current (pulse)	$I_{D(pulse)}$	$\pm 12$	A
Total Power Dissipation	$P_{T1}$	0.2	W
Total Power Dissipation	$P_{T2}$	1.3	W
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

- Notes** 1. Mounted on FR-4 board of  $5000 \text{ mm}^2 \times 1.1 \text{ mm}$ ,  $t \leq 5 \text{ sec.}$   
 2.  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

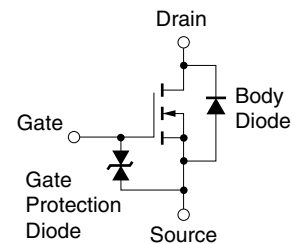
**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT

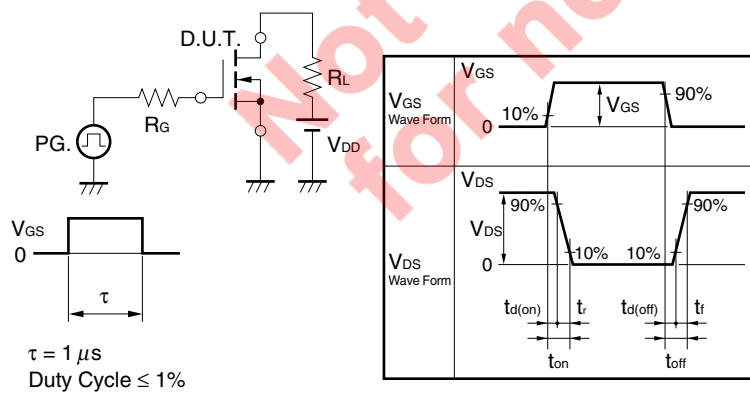


**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

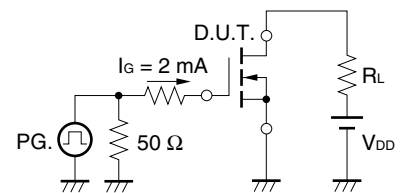
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			10	μA
★ Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V			±10	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 mA	1.5	2.0	2.5	V
Forward Transfer Admittance <sup>Note</sup>	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.5 A	0.5	2.1		S
Drain to Source On-state Resistance <sup>Note</sup>	R <sub>DS(on)1</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A		65	82	mΩ
	R <sub>DS(on)2</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.0 A		90	120	mΩ
	R <sub>DS(on)3</sub>	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 1.0 A		104	139	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V		155		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V		45		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0 MHz		27		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 1.5 A		10		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V		28		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 10 Ω		75		ns
Fall Time	t <sub>f</sub>			50		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 24 V		3.8		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V		0.7		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 3.0 A		1.3		nC
Body Diode Forward Voltage <sup>Note</sup>	V <sub>F(S-D)</sub>	I <sub>F</sub> = 3.0 A, V <sub>GS</sub> = 0 V		0.90		V

Note Pulsed

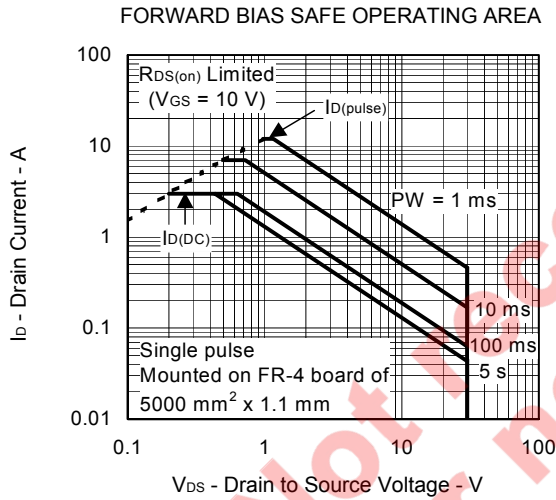
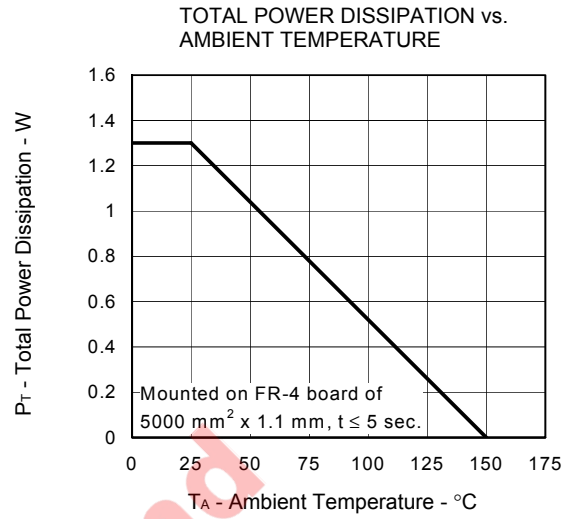
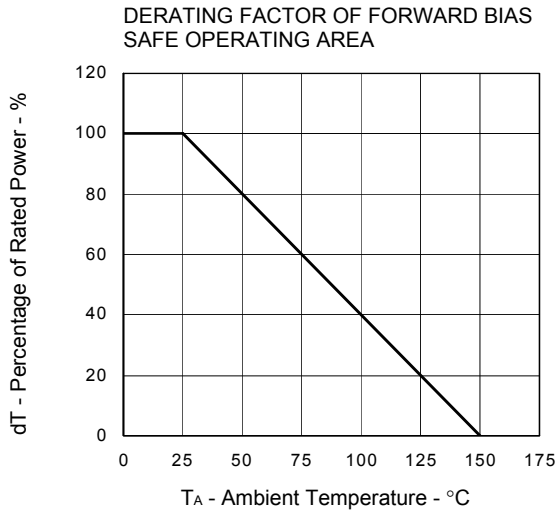
**TEST CIRCUIT 1 SWITCHING TIME**



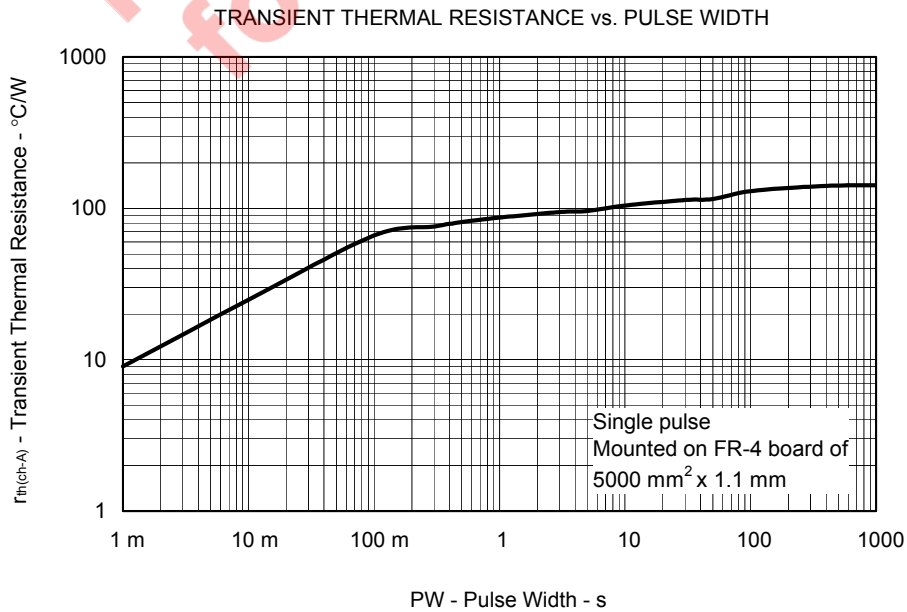
**TEST CIRCUIT 2 GATE CHARGE**



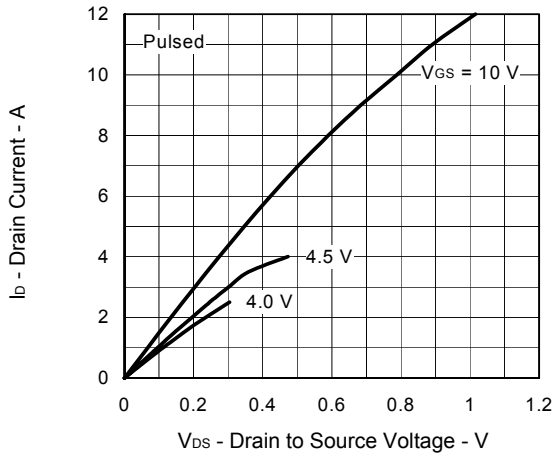
TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )



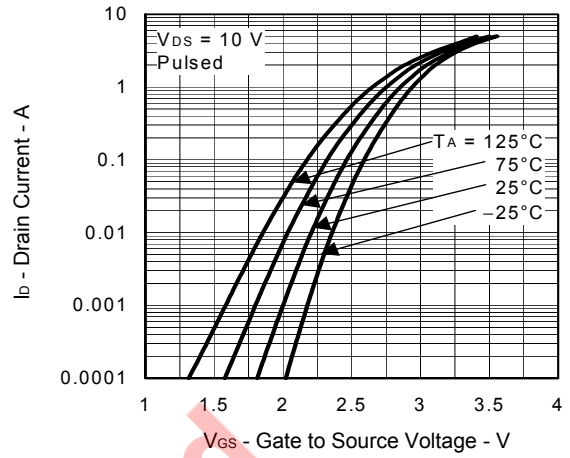
Not recommended for new design



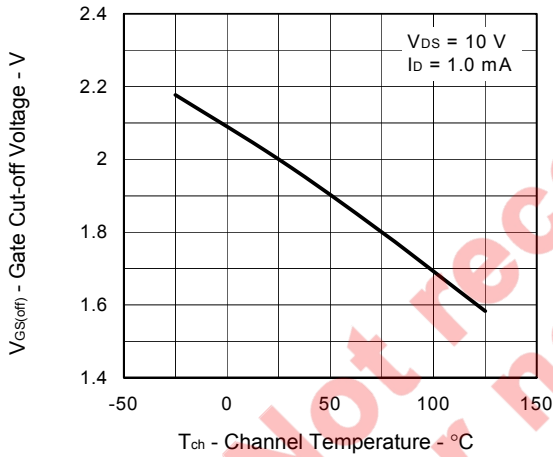
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



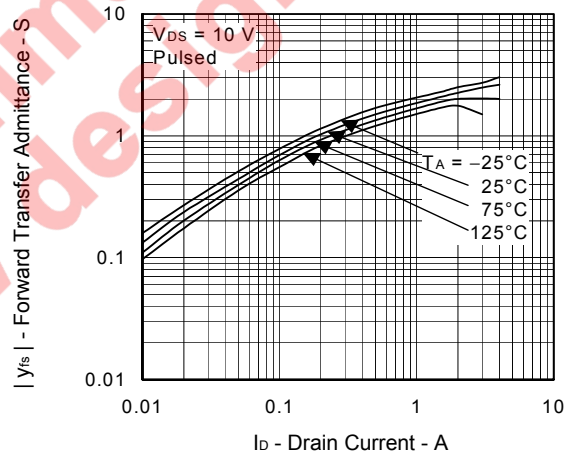
FORWARD TRANSFER CHARACTERISTICS



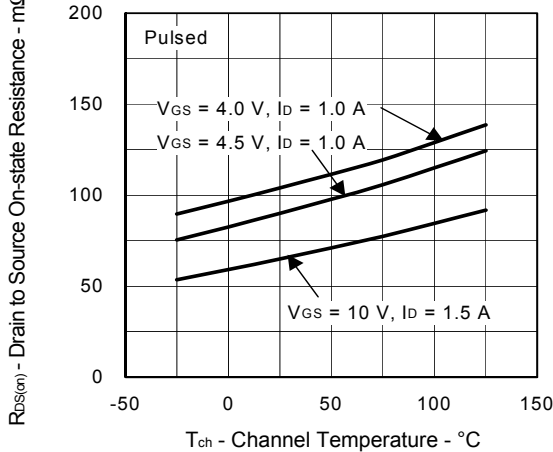
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



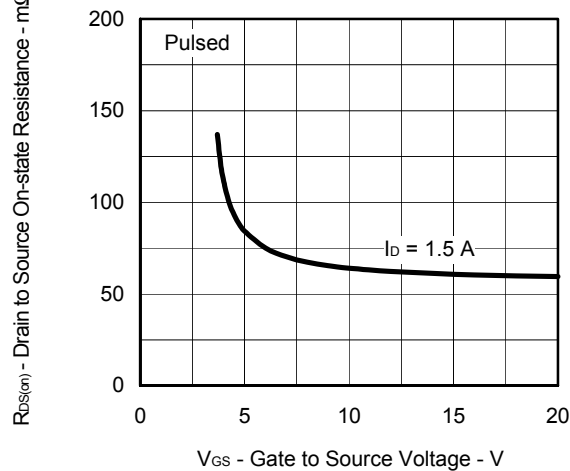
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

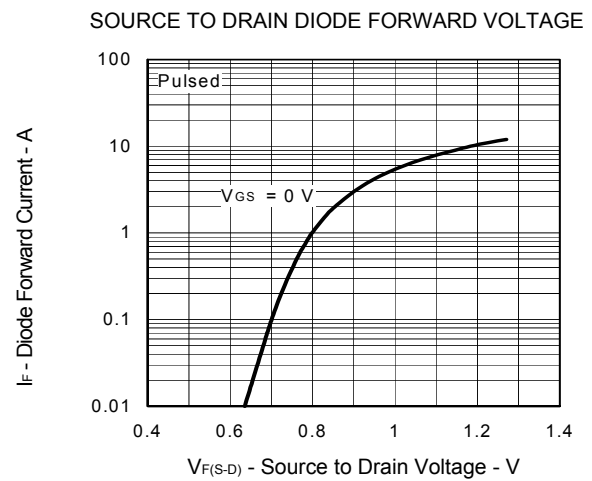
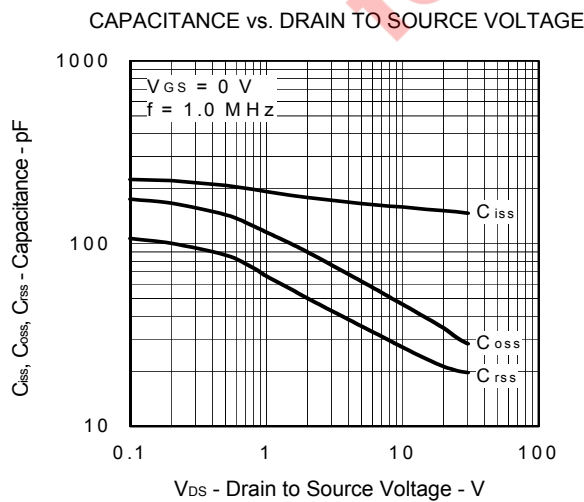
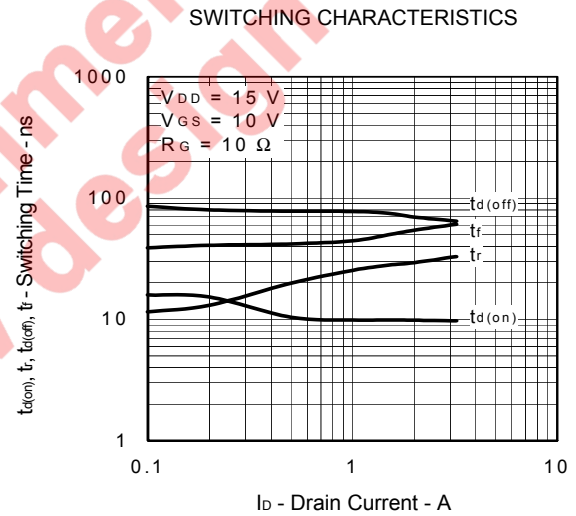
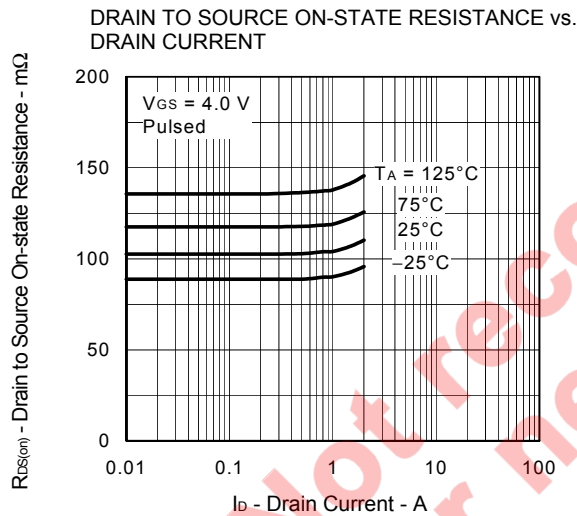
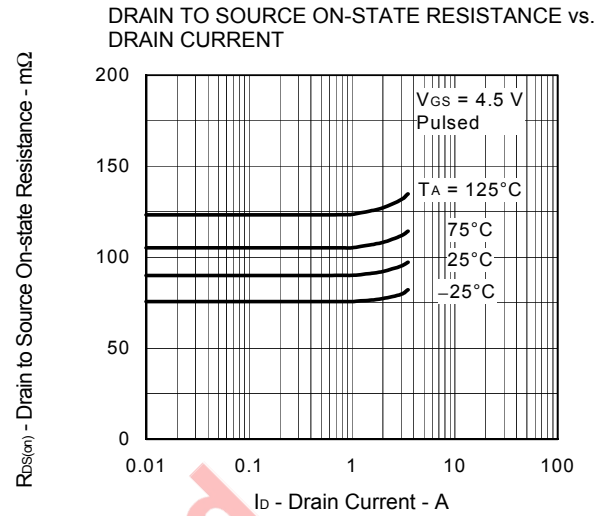
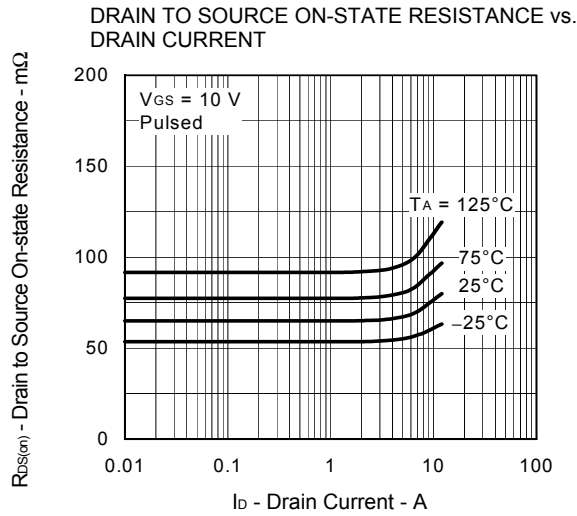


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

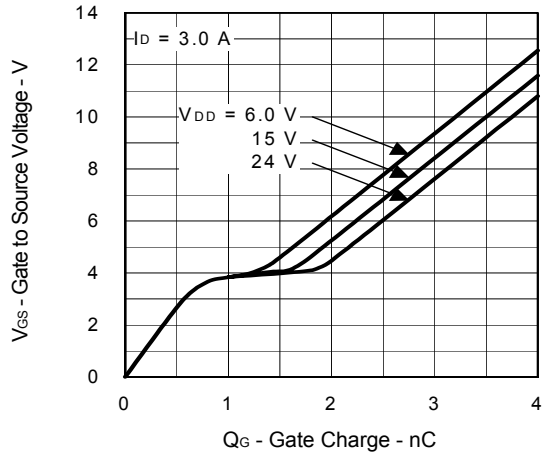


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE





DYNAMIC INPUT/OUTPUT CHARACTERISTICS



Not recommend  
for new design



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