

DM74AS34 Hex Non-Inverter

General Description

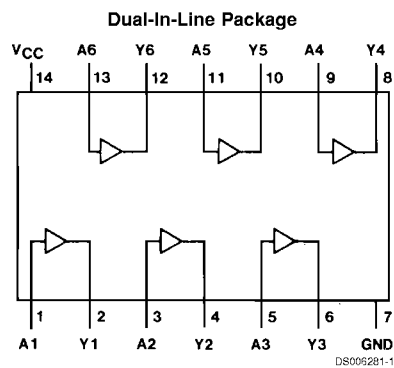
These devices contain six independent gates, each of which performs the logic identity function.

- Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process

Features

- Switching specifications at 50 pF

Connection Diagram



Order Number **DM74AS34N**
See Package Number **N14A** (Note 1)

Function Table

$$Y = \bar{A}$$

Input A	Output Y
H	H
L	L

H = High Logic Level
L = Low Logic Level

Note 1: Contact your local NSC representative about surface mount (M) package availability.

Absolute Maximum Ratings (Note 2)

Supply Voltage	7V	Range	0°C to +70°C
Input Voltage	7V	Storage Temperature Range	-65°C to +150°C
Operating Free Air Temperature		Typical θ_{JA}	
		N Package	84.5°C/W

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V_{CC}	Supply Voltage	4.5	5	5.5	V
V_{IH}	High Level Input Voltage	2			V
V_{IL}	Low Level Input Voltage			0.8	V
I_{OH}	High Level Output Current			-2	mA
I_{OL}	Low Level Output Current			20	mA
T_A	Free Air Operating Temperature	0		70	°C

Note 2: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^\circ C$.

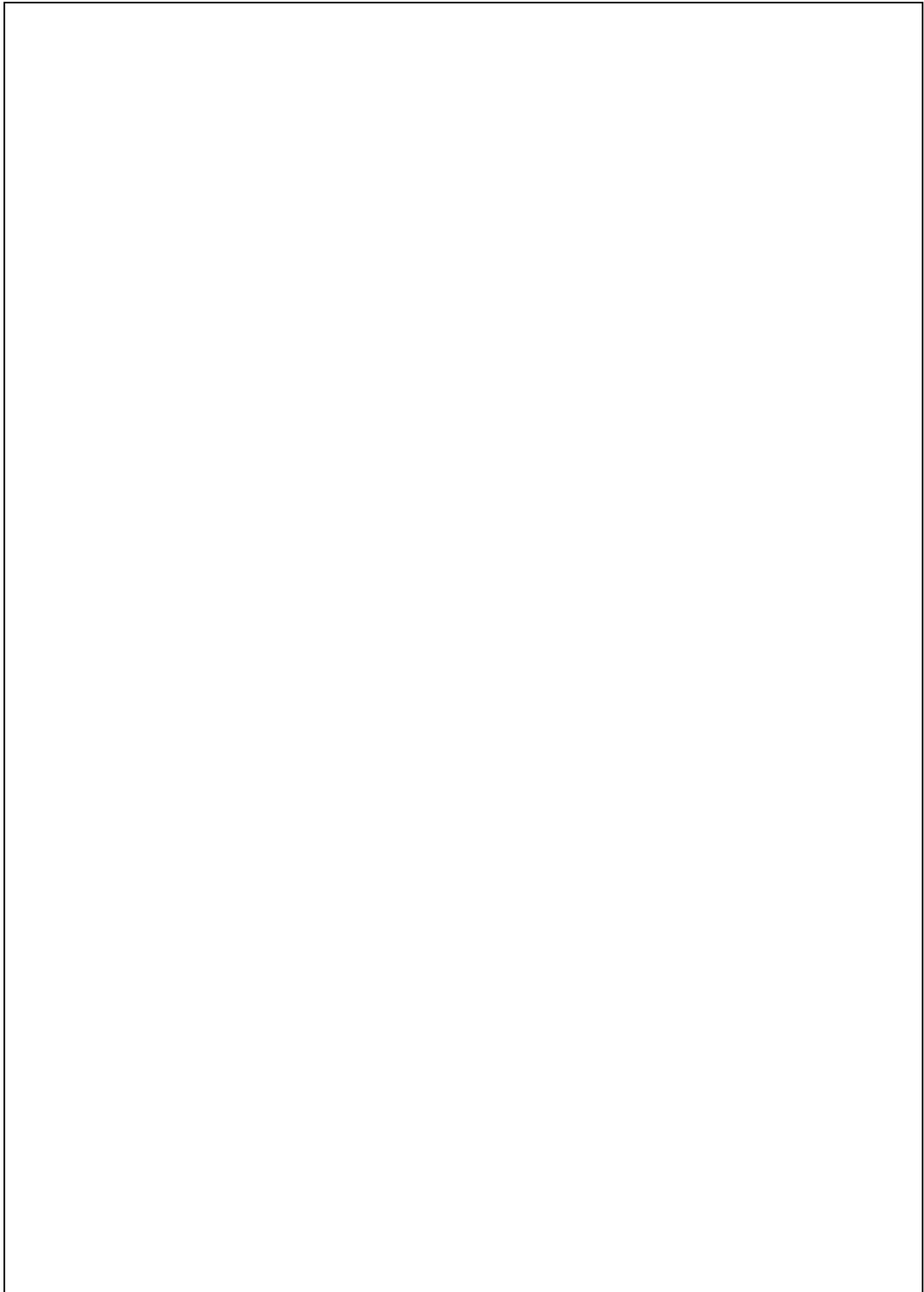
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_I = -18 mA$			-1.2	V
V_{OH}	High Level Output Voltage	$V_{CC} = 4.5V$ to $5.5V$ $I_{OH} = -2 mA$	$V_{CC} - 2$			V
V_{OL}	Low Level Output Voltage	$V_{CC} = 4.5V$, $I_{OL} = 20 mA$		0.35	0.5	V
I_I	Input Current @ Max Input Voltage	$V_{CC} = 5.5V$, $V_{IH} = 7V$			0.1	mA
I_{IH}	High Level Input Current	$V_{CC} = 5.5V$, $V_{IH} = 2.7V$			20	μA
I_{IL}	Low Level Input Current	$V_{CC} = 5.5V$, $V_{IL} = 0.4V$			-0.5	mA
I_O	Output Drive Current	$V_{CC} = 5.5V$, $V_O = 2.25V$	-30		-112	mA
I_{CC}	Supply Current	$V_{CC} = 5.5V$	Outputs High	7.4	12	mA
			Outputs Low	21.3	34.6	mA

Switching Characteristics

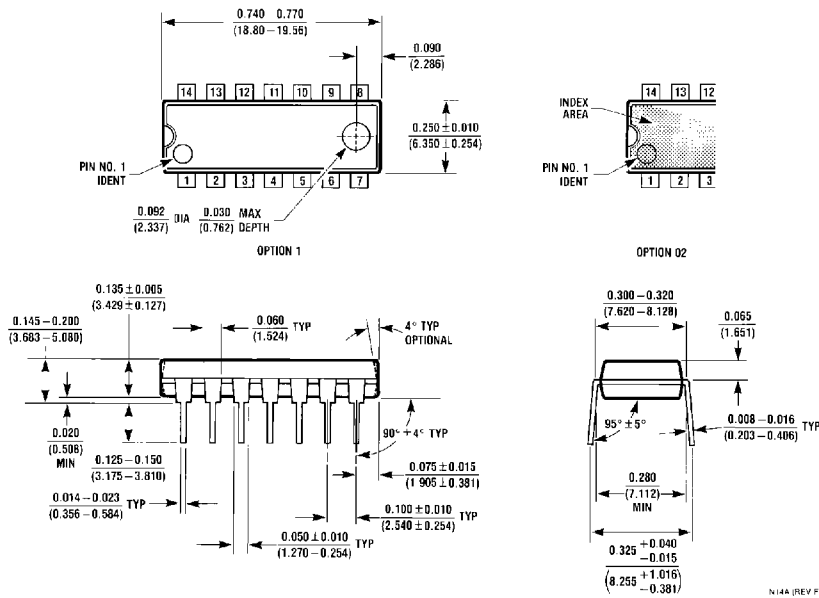
over recommended operating free air temperature range (Note 3)

Symbol	Parameter	Conditions	Min	Max	Units
t_{PLH}	Propagation Delay Time Low to High Level Output	$V_{CC} = 4.5V$ to $5.5V$ $R_L = 500\Omega$	1	5.5	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	$C_L = 50 pF$	1	6	ns

Note 3: See Section 1 for test waveforms and output load.



Physical Dimensions inches (millimeters) unless otherwise noted



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Fairchild Semiconductor Corporation
 Americas
 Customer Response Center
 Tel: 1-888-522-5372

Fairchild Semiconductor Europe
 Fax: +49 (0) 1 80-530 85 86
 Email: europe.support@nsc.com
 Deutsch Tel: +49 (0) 8 141-35-0
 English Tel: +44 (0) 1 793-85-68-56
 Italy Tel: +39 (0) 2 57 5631

Fairchild Semiconductor Hong Kong Ltd.
 13th Floor, Straight Block,
 Ocean Centre, 5 Canton Rd.
 Tsimshatsui, Kowloon
 Hong Kong
 Tel: +852 2737-7200
 Fax: +852 2314-0061

National Semiconductor Japan Ltd.
 Tel: 81-3-5620-6175
 Fax: 81-3-5620-6179

www.fairchildsemi.com