

Product Summary

- Continuous Drain Source Voltage: $V_{DS} = 60V$
- On-State Resistance: 550m Ω
- Nominal Load Current ($V_{IN} = 5V$) : 1.4A
- Clamping Energy: 550mJ

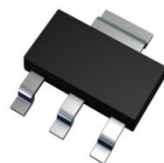
Description

The BSP75GQ is a Self-protected low-side MOSFET. It features monolithic over temperature, over current, over voltage (active clamp) and ESD protected logic level functionality. It is intended as a general purpose switch.

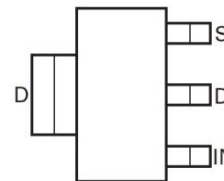
Applications

- Especially Suited for Loads with a High In-Rush Current such as Lamps and Motors
- All Types of Resistive, Inductive and Capacitive Loads in Switching Applications
- μC Compatible Power Switch for 12V and 24V DC Applications
- Automotive Rated
- Replaces Electromechanical Relays and Discrete Circuits
- Linear Mode Capability - the current-limiting protection circuitry is designed to de-activate at low V_{DS} in order not to compromise the load current during normal operation. The maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry.

SOT223 (Type DN)



Top View



Top View
Pin Out

Note: The tab is connected to the drain pin, and must be electrically isolated from the source pin. Connection of significant copper to the tab is recommended for best thermal performance.

Ordering Information (Note 5)

Product	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
BSP75GQTA	BSP75G	7	12	1,000 Units
BSP75GQTC	BSP75G	13	12	4,000 Units

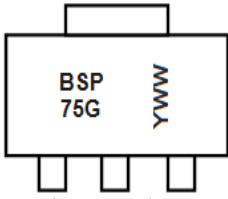
- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to <https://www.diodes.com/quality/>.
 5. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

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BSP75GQ

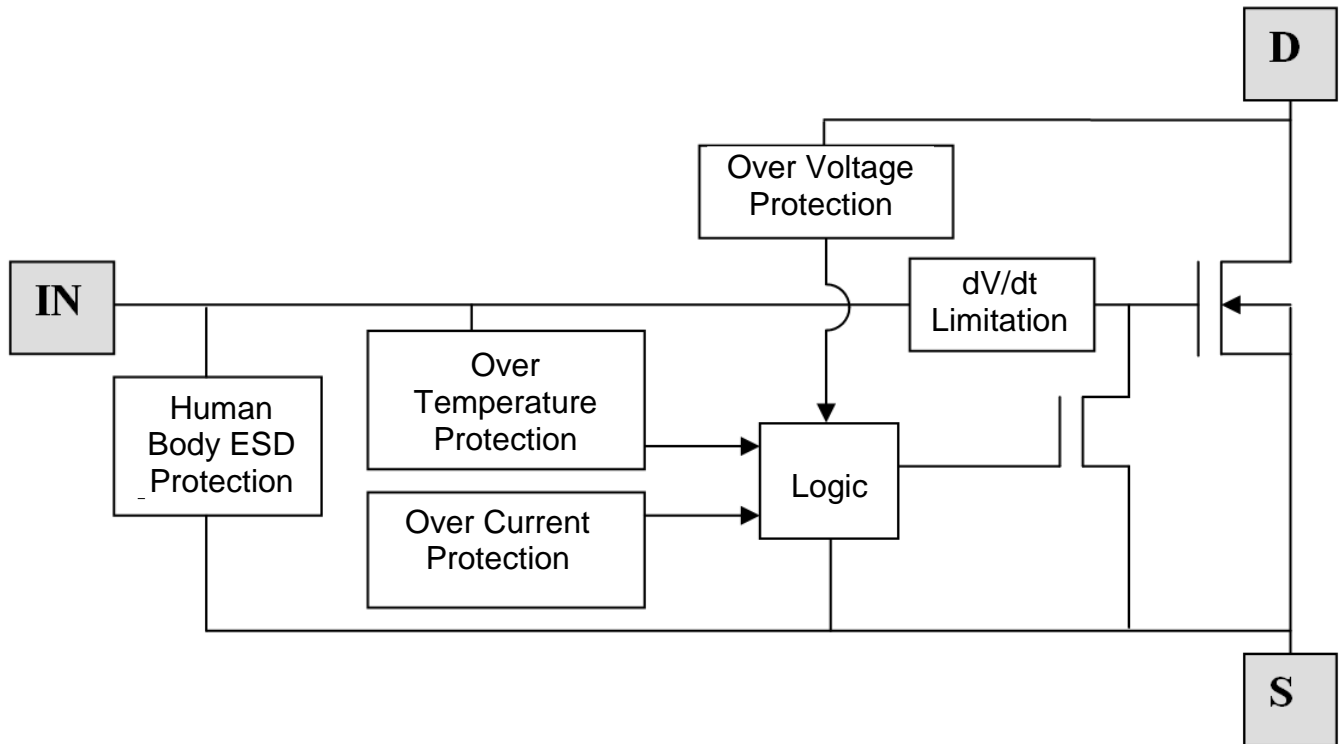
Document number: DS37144 Rev. 5 - 2

Marking Information



BSP75G = Product Type Marking Code
 YWW = Date Code Marking
 Y or \bar{Y} = Last Digit of Year (ex: 8 = 2018)
 WW or \bar{WW} = Week Code (01 to 53)

Functional Block Diagram



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise stated.)

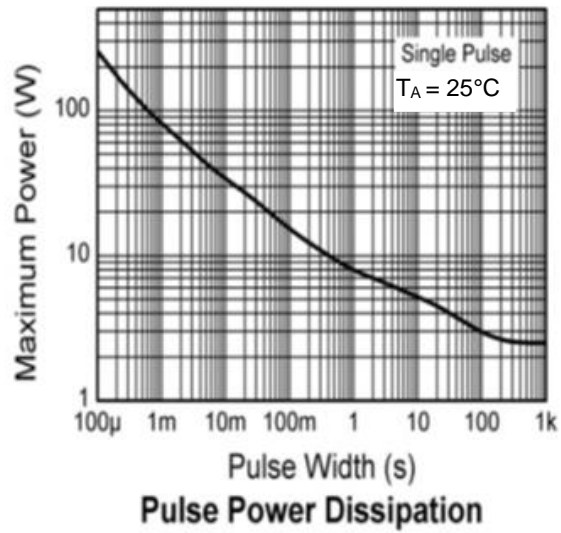
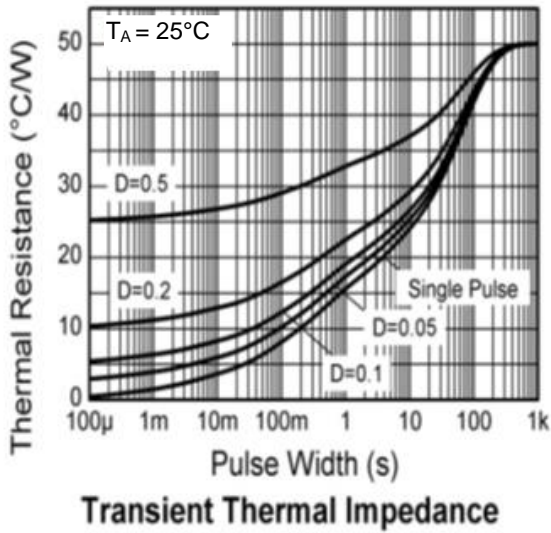
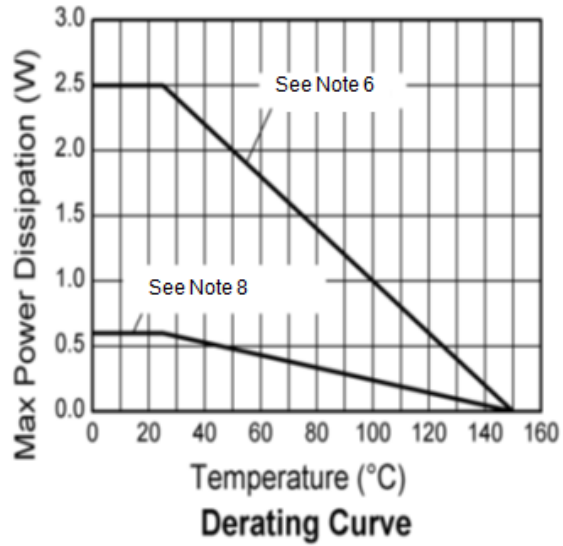
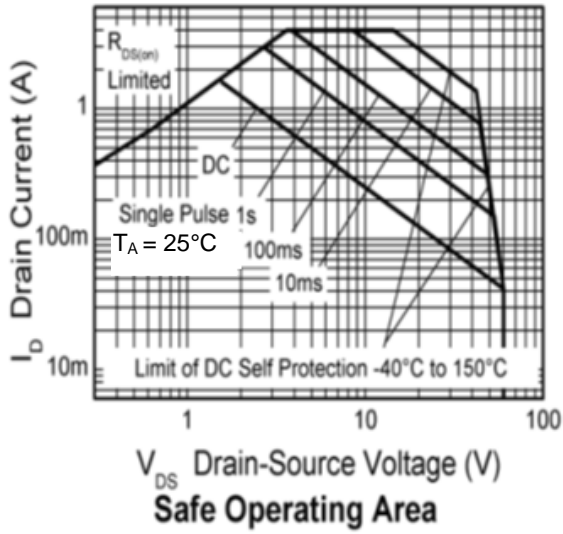
Parameter	Symbol	Limit	Unit
Continuous Drain-Source Voltage	V _{DS}	60	V
Drain-Source Voltage for Short Circuit Protection V _{IN} = 5V	V _{DS(SC)}	36	V
Continuous Input Voltage	V _{IN}	-0.2 to +10	V
Peak Input Voltage	V _{IN}	-0.2 to +20	V
Operating Temperature Range	T _J	-40 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C
Power Dissipation at T _A = +25°C (Note 6)	P _D	2.5	W
Continuous Drain Current @ V _{IN} = 10V; T _A = +25°C (Note 6)	I _D	1.6	A
Continuous Drain Current @ V _{IN} = 5V; T _A = +25°C (Note 6)	I _D	1.4	A
Pulsed Drain Current @ V _{IN} = 10V	I _{DM}	5	A
Continuous Source Current (Body Diode) (Note 6)	I _S	3	A
Pulsed Source Current (Body Diode)	I _S	5	A
Unclamped Single Pulse Inductive Energy	E _{AS}	550	mJ
Load Dump Protection	V _{LOAD_DUMP}	80	V
Electrostatic Discharge (Human Body Model)	V _{ESD}	4000	V
DIN Humidity Category, DIN 40 040	—	E	—
IEC Climatic Category, DIN IEC 68-1	—	40/150/56	—

Thermal Resistance

Characteristic	Symbol	Limit	Unit
Junction to Ambient (Note 6)	R _{θJA}	50	°C/W
Junction to Ambient (Note 7)	R _{θJA}	24	°C/W
Junction to Ambient (Note 8)	R _{θJA}	208	°C/W

- Notes:
6. For a device surface mounted on 37mm x 37mm x 1.6mm FR-4 board with a high coverage of single sided 2oz weight copper.
 7. For a device surface mounted on FR-4 board and measured at t<=10s.
 8. For a device mounted on FR-4 board with the minimum copper required for electrical connections.

Typical Characteristics



Electrical Characteristics (@T_A = +25°C, unless otherwise stated.)

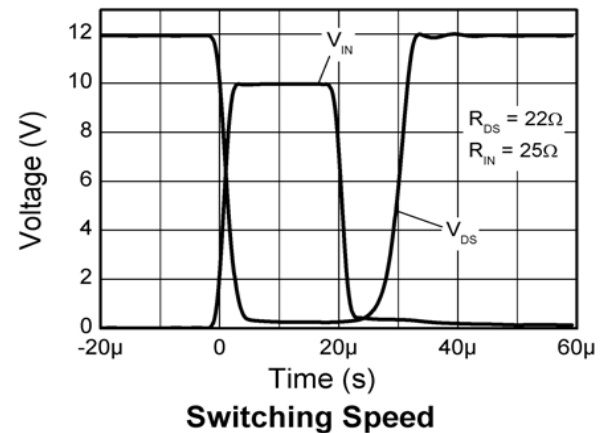
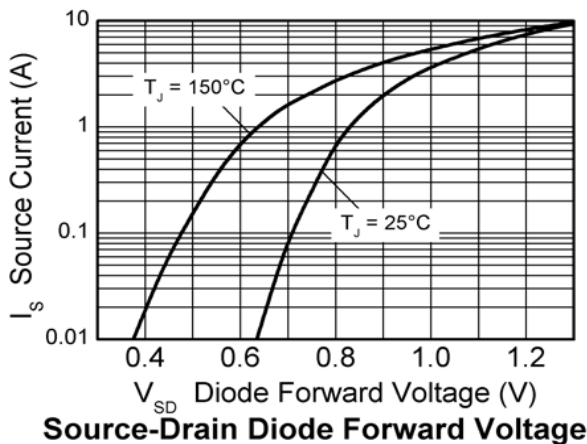
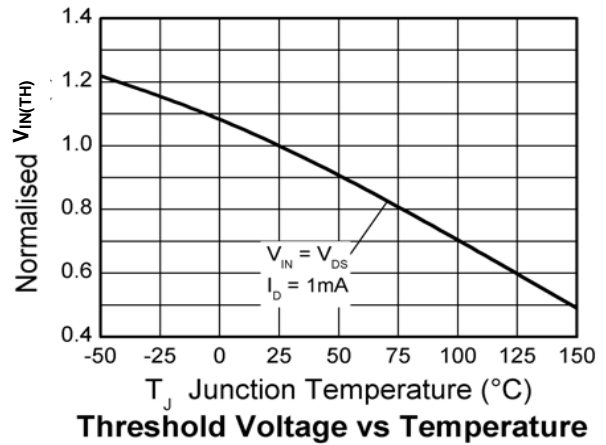
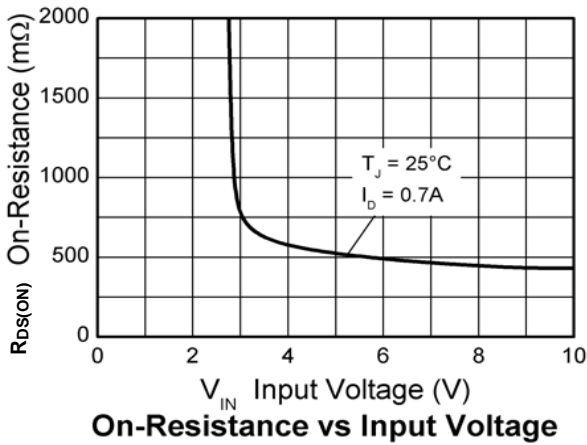
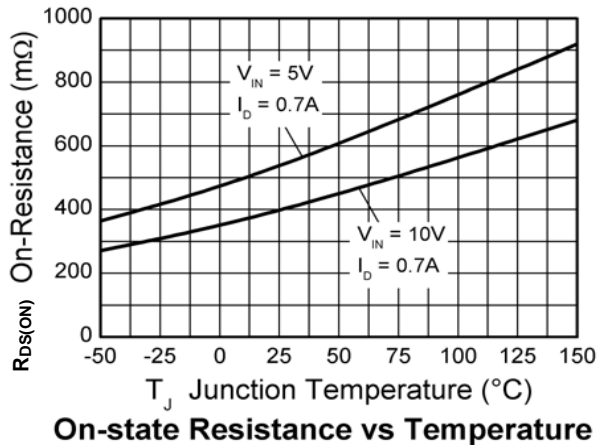
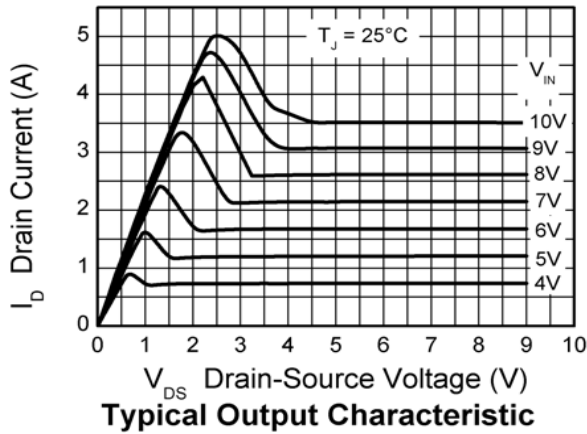
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Static Characteristics						
Drain-Source Clamp Voltage	V _{DS(AZ)}	60	70	75	V	I _D =10mA
Off state Drain Current	I _{DSS}	—	0.1	3	μA	V _{DS} =12V, V _{IN} =0V
Off state Drain Current	I _{DSS}	—	3	15	μA	V _{DS} =32V, V _{IN} =0V
Input Threshold Voltage (Note 9)	V _{IN(TH)}	1	2.1	—	V	V _{DS} =V _{GS} , I _D =1mA
Input Current	I _{IN}	—	0.7	1.2	mA	V _{IN} =5V
Input Current	I _{IN}	—	1.5	2.7	mA	V _{IN} =7V
Input Current	I _{IN}	—	4	7	mA	V _{IN} =10V
Static Drain-Source On-State Resistance	R _{DS(ON)}	—	520	675	mΩ	V _{IN} =5V, I _D =0.7A
Static Drain-Source On-State Resistance	R _{DS(ON)}	—	385	550	mΩ	V _{IN} =10V, I _D =0.7A
Current Limit (Note 10)	I _{D(LIM)}	0.7	1.1	1.75	A	V _{IN} =5V, V _{DS} >5V
Current Limit (Note 10)	I _{D(LIM)}	2	3	4	A	V _{IN} =10V, V _{DS} >5V
Dynamic Characteristics						
Turn-On Time (V _{IN} to 90% I _D)	t _{ON}	—	2.2	—	μs	R _L =22Ω, V _{IN} =0 to 10V, V _{DD} =12V
Turn-Off Time (V _{IN} to 90% I _D)	t _{OFF}	—	13	—	μs	R _L =22Ω, V _{IN} =10V to 0V, V _{DD} =12V
Slew Rate On (70 to 50% V _{DD})	-dV _{DS} /dt _{ON}	—	10	—	V/μs	R _L =22Ω, V _{IN} =0 to 10V, V _{DD} =12V
Slew Rate Off (50 to 70% V _{DD})	dV _{DS} /dt _{ON}	—	3.2	—	V/μs	R _L =22Ω, V _{IN} =10V to 0V, V _{DD} =12V
Protection Functions (Note 11)						
Minimum Input Voltage for Over Temperature Protection	V _{PROT}	4.5	—	—	V	—
Thermal Overload Trip Temperature	T _{JT}	+150	+175	—	°C	—
Thermal Hysteresis	—	—	+10	—	°C	—
Unclamped Single Pulse Inductive Energy T _J = +25°C	E _{AS}	550	—	—	mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Unclamped Single Pulse Inductive Energy T _J = +150°C	E _{AS}	200	—	—	mJ	I _{D(ISO)} =0.7A, V _{DD} =32V
Inverse Diode						
Source Drain Voltage	V _{SD}	—	—	1	V	V _{IN} =0V, -I _D =1.4A

Notes: 9. Protection features may operate outside spec for V_{IN} < 4.5V.

10. The drain current is limited to a reduced value when V_{DS} exceeds a safe level.

11. Integrated protection functions are designed to prevent IC destruction under fault conditions described in the datasheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous, repetitive operation.

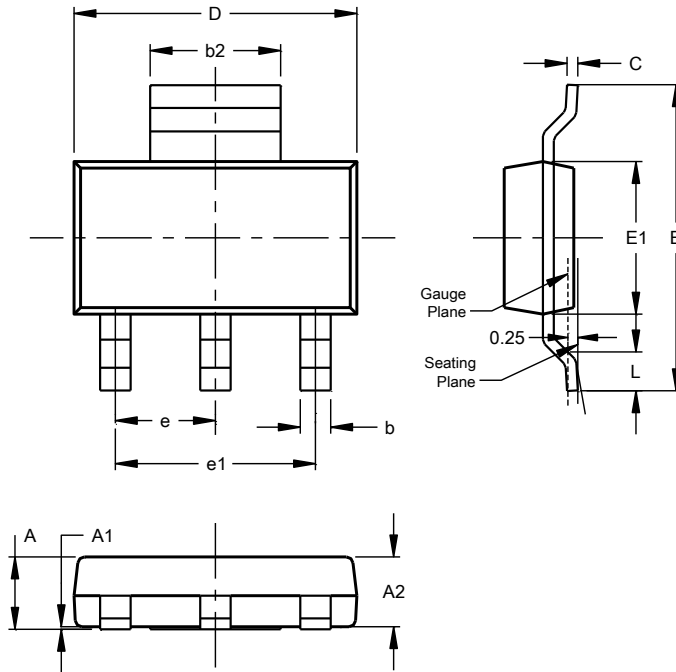
Typical Characteristics



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)

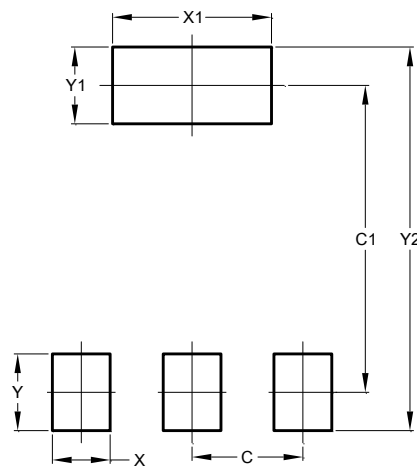


SOT223 (Type DN)			
Dim	Min	Max	Typ
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

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