Series From .22 µH to 22 µH



CHARACTERISTICS

Description: SMD (shielded) power inductor.

Applications: Notebook, Desktop, Server applications, Low profile, high current power supplies, battery powered devices, DC/DC converter for Field Programmable Gate Array (FPGA).

Operating Temperature: -55°C to +125°C (The part temperature (ambient + temp. rise) should not exceed 125°C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application)

Inductance Tolerance: ±20%, ±30%

Testing: Inductance is tested on an HP4285A at 200KHz, 0.25V

Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical

information available upon request.

Samples available. See website for ordering information.

SPECIFICATIONS

Part numbers indicate inductance tolerance available.

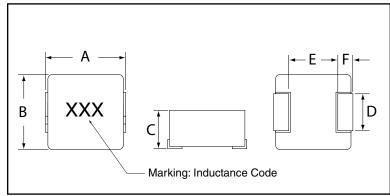
 $M = \pm 20\%, N = \pm 30\%$

*Irms DC current (A) that will cause an approximate ΔT of 40°C.
**Isat DC current (A) that will cause L0 to drop approximately 20%.

Part Number	Inductance (µH)	L Test Freq. (KHz)	DCR Max. (mΩ)	*Irms Typ. (A)	**Isat Typ. (A)
CTIHLP2712F-R22N	0.22	200	3.1	22.0	34
CTIHLP2712F-R33M	0.33	200	3.9	20.0	28
CTIHLP2712F-R47M	0.47	200	4.2	17.5	24
CTIHLP2712F-R56M	0.56	200	5.3	15.5	23
CTIHLP2712F-R68M	0.68	200	5.5	15.5	22
CTIHLP2712F-R82M	0.82	200	8.0	13.0	19
CTIHLP2712F-1R0M	1.0	200	10	11.0	17
CTIHLP2712F-1R5M	1.5	200	15	9.0	15
CTIHLP2712F-2R2M	2.2	200	20	8.0	12
CTIHLP2712F-2R5M	2.5	200	22	8.0	11
CTIHLP2712F-3R3M	3.3	200	30	6.0	10
CTIHLP2712F-4R7M	4.7	200	40	5.0	8.0
CTIHLP2712F-5R6M	5.6	200	54	5.0	7.5
CTIHLP2712F-6R8M	6.8	200	60	4.5	7.0
CTIHLP2712F-8R2M	8.2	200	68	4.0	6.0
CTIHLP2712F-100M	10.0	200	105	3.0	5.5
CTIHLP2712F-220M	22.0	200	230	2.0	3.0

PHYSICAL DIMENSIONS

Size	A Max.	B Max.	C Max.	D	E Ref.	F Ref.	
mm	7.3	6.8	3.0	3.2±0.2	4.2	1.3	
inches	0.290	0.270	0.12	0.126±0.008	0.165	0.051	



PAD LAYOUT

