

# FDMC010N08LC

## N-Channel Shielded Gate POWER trench<sup>®</sup> MOSFET

80 V, 50 A, 10.9 mΩ

### General Description

This N-Channel MV MOSFET is produced using ON Semiconductor's advanced PowerTrench process that incorporates Shielded Gate technology. This process has been optimized to minimize on-state resistance and yet maintain superior switching performance with best in class soft body diode.

### Features

- Shielded Gate MOSFET Technology
- Max  $R_{DS(on)}$  = 10.9 mΩ at  $V_{GS} = 10$  V,  $I_D = 16$  A
- Max  $R_{DS(on)}$  = 18.4 mΩ at  $V_{GS} = 4.5$  V,  $I_D = 13$  A
- 50% Lower  $Q_{rr}$  than Other MOSFET Suppliers
- Lowers Switching Noise/EMI
- MSL1 Robust Package Design
- 100% UIL Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Applications

- Primary DC-DC MOSFET
- Synchronous Rectifier in DC-DC and AC-DC
- Motor Drive
- Solar

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain to Source Voltage	80	V
$V_{GS}$	Gate to Source Voltage	±20	V
$I_D$	Drain Current: Continuous, $T_C = 25^\circ\text{C}$ (Note 5) Continuous, $T_C = 100^\circ\text{C}$ (Note 5) Continuous, $T_A = 25^\circ\text{C}$ (Note 1a) Pulsed (Note 4)	50 32 11 200	A
$E_{AS}$	Single Pulse Avalanche Energy (Note 3)	96	mJ
$P_D$	Power Dissipation: $T_C = 25^\circ\text{C}$ $T_A = 25^\circ\text{C}$ (Note 1a)	52 2.3	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

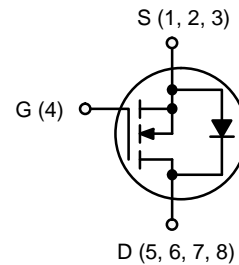
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



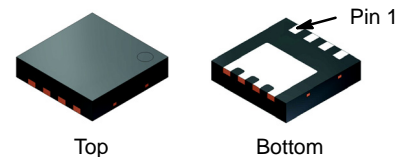
ON Semiconductor<sup>®</sup>

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$V_{DS}$	$R_{DS(on)}$ MAX	$I_D$ MAX
80 V	10.9 mΩ @ 10 V	50 A
	18.4 mΩ @ 4.5 V	

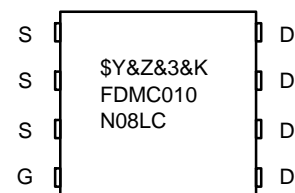


N-CHANNEL MOSFET



Power 33  
(PQFN8)  
CASE 483AX

### MARKING DIAGRAM



\$Y = ON Semiconductor Logo  
 &Z = Assembly Plant Code  
 &3 = Numeric Date Code  
 &K = Lot Code  
 FDMC010N08LC = Specific Device Code

### ORDERING INFORMATION

See detailed ordering and shipping information on page 3 of this data sheet.

# FDMC010N08LC

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250 \mu\text{A}, V_{GS} = 0 \text{ V}$	80			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , referenced to $25^\circ\text{C}$		76		mV/°C
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}$			1	$\mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			$\pm 100$	nA

### ON CHARACTERISTICS

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 90 \mu\text{A}$	1.0	1.3	3.0	V
$\Delta V_{GS(th)} / \Delta T_J$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 90 \mu\text{A}$ , referenced to $25^\circ\text{C}$		-5		mV/°C
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 16 \text{ A}$		8.9	10.9	m $\Omega$
		$V_{GS} = 4.5 \text{ V}, I_D = 13 \text{ A}$		12.5	18.4	
		$V_{GS} = 10 \text{ V}, I_D = 16 \text{ A}, T_J = 125^\circ\text{C}$		15.0	17.6	
$g_{FS}$	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_D = 16 \text{ A}$		55		S

### DYNAMIC CHARACTERISTICS

$C_{iss}$	Input Capacitance	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1525	2135	pF
$C_{oss}$	Output Capacitance			369	515	pF
$C_{rss}$	Reverse Transfer Capacitance			20	30	pF
$R_g$	Gate Resistance		0.1	0.3	0.7	$\Omega$

### SWITCHING CHARACTERISTICS

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 40 \text{ V}, I_D = 16 \text{ A}, V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		8	16	ns
$t_r$	Rise Time			3	10	ns
$t_{d(off)}$	Turn-Off Delay Time			27	44	ns
$t_f$	Fall Time			5	10	ns
$Q_g$	Total Gate Charge	$V_{GS} = 0 \text{ V to } 10 \text{ V}, V_{DD} = 40 \text{ V}, I_D = 16 \text{ A}$		22	31	nC
		$V_{GS} = 0 \text{ V to } 4.5 \text{ V}, V_{DD} = 40 \text{ V}, I_D = 16 \text{ A}$		11	15	nC
$Q_{gs}$	Gate to Source Charge	$V_{DD} = 40 \text{ V}, I_D = 16 \text{ A}$		3		nC
$Q_{gd}$	Gate to Drain "Miller" Charge	$V_{DD} = 40 \text{ V}, I_D = 16 \text{ A}$		3		nC
$Q_{oss}$	Output Charge	$V_{DD} = 40 \text{ V}, V_{GS} = 0 \text{ V}$		21		nC
$Q_{sync}$	Total Gate Charge Sync	$V_{DS} = 0 \text{ V}, I_D = 16 \text{ A}$		19.5		nC

# FDMC010N08LC

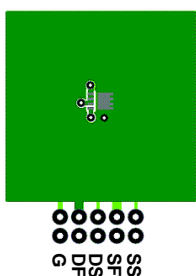
## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted) (continued)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 2 A (Note 2)		0.7	1.2	V
		V <sub>GS</sub> = 0 V, I <sub>S</sub> = 16 A (Note 2)		0.8	1.3	
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 16 A, di/dt = 300 A/μs		15	27	ns
Q <sub>rr</sub>	Reverse Recovery Charge			18	33	nC
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 16 A, di/dt = 1000 A/μs		12	21	ns
Q <sub>rr</sub>	Reverse Recovery Charge			38	61	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. R<sub>θJA</sub> is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 × 1.5 in. board of FR-4 material. R<sub>θCA</sub> is determined by the user's board design.

### NOTES:



a) 53°C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

- Pulse Test: Pulse Width < 300 μs, Duty cycle < 2.0%.
- E<sub>AS</sub> of 96 mJ is based on starting T<sub>J</sub> = 25°C; N-ch: L = 3 mH, I<sub>AS</sub> = 8 A, V<sub>DD</sub> = 80 V, V<sub>GS</sub> = 10 V. 100% test at L = 0.1 mH, I<sub>AS</sub> = 24 A.
- Pulsed Id please refer to Figure 11 SOA graph for more details.
- Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

### ORDERING INFORMATION

Device	Marking	Package	Reel Size	Tape Width	Quantity
FDMC010N08LC	FDMC010N08LC	Power 33 (PQFN8) (Pb-Free / Halogen Free)	13"	12 mm	3000 Units

# FDMC010N08LC

## TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

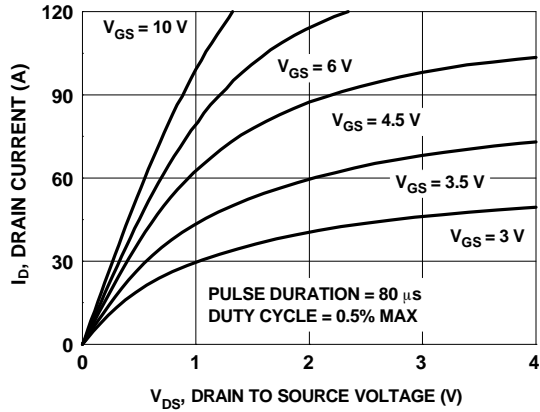


Figure 1. On Region Characteristics

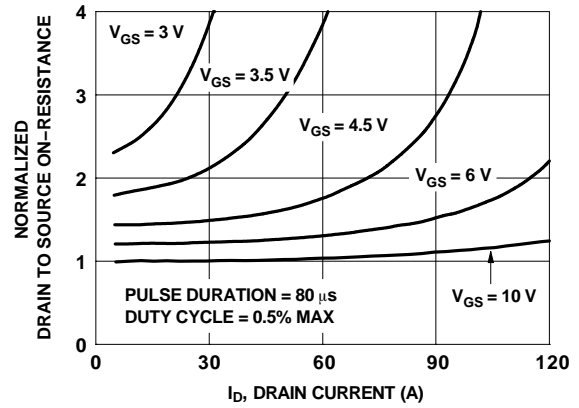


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

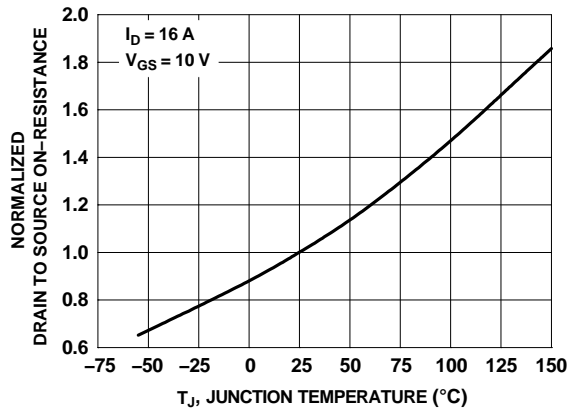


Figure 3. Normalized On-Resistance vs. Junction Temperature

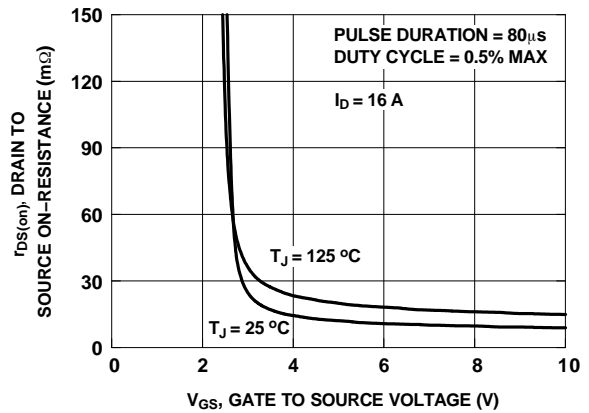


Figure 4. On-Resistance vs. Gate to Source Voltage

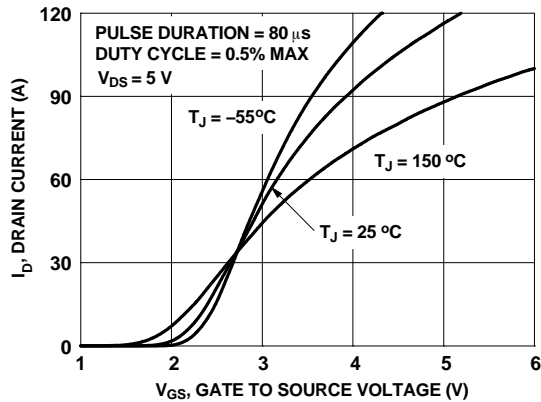


Figure 5. Transfer Characteristics

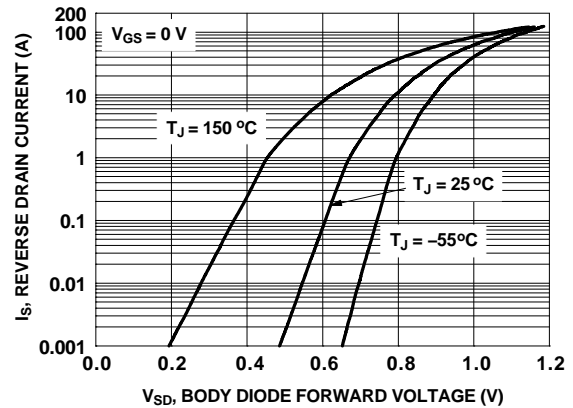


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

# FDMC010N08LC

## TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

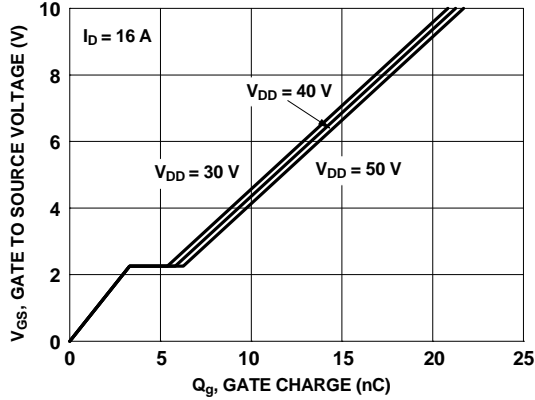


Figure 7. Gate Charge Characteristics

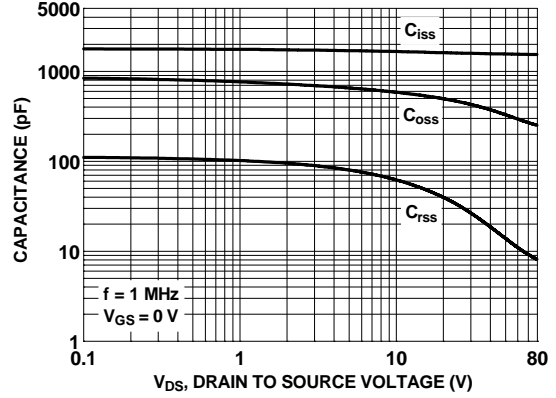


Figure 8. Capacitance vs. Drain to Source Voltage

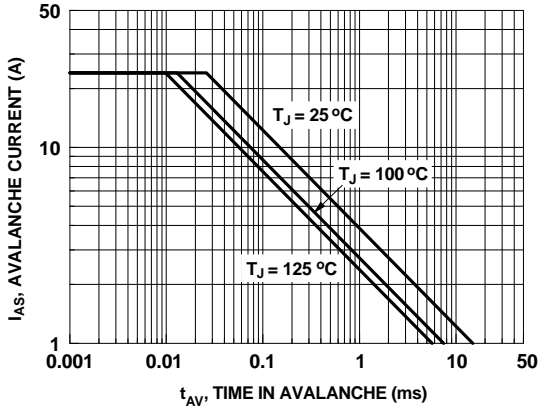


Figure 9. Unclamped Inductive Switching Capability

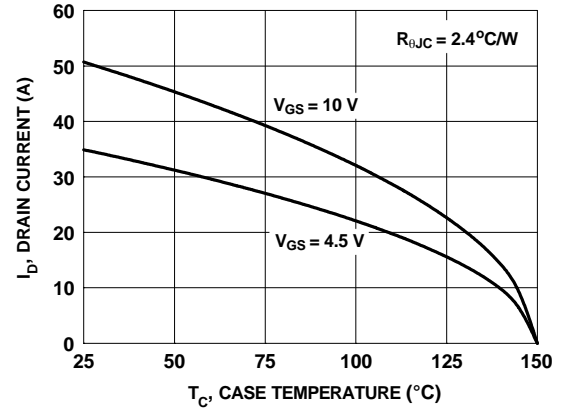


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

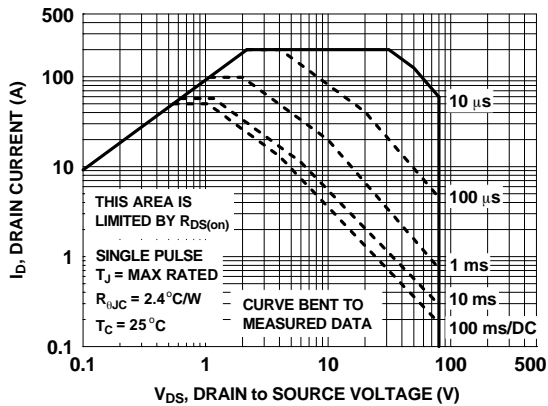


Figure 11. Forward Bias Safe Operating Area

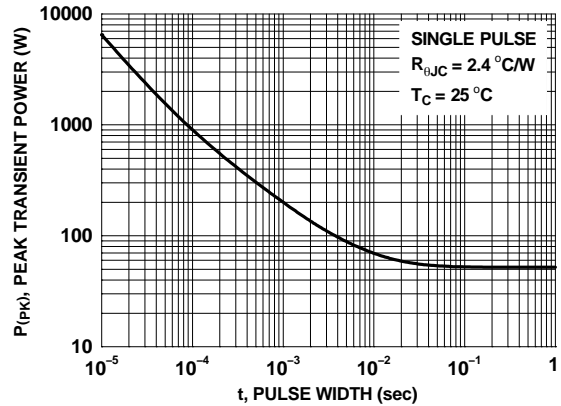


Figure 12. Single Pulse Maximum Power Dissipation

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## TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

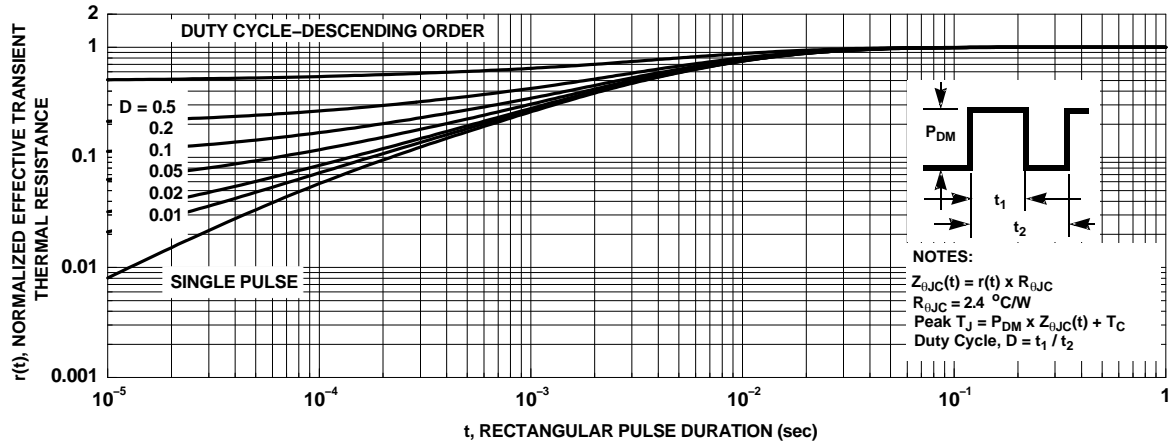
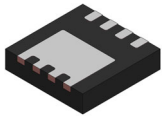


Figure 13. Junction-to-Case Transient Thermal Response Curve

# MECHANICAL CASE OUTLINE

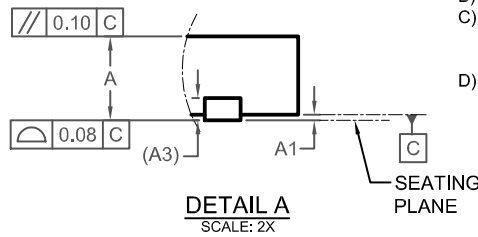
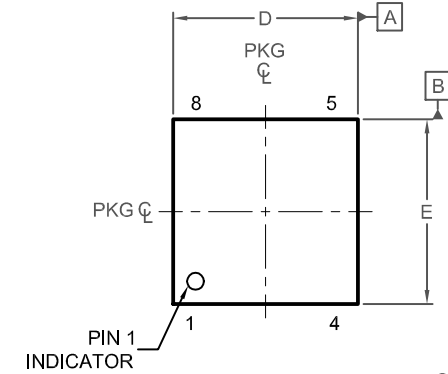
## PACKAGE DIMENSIONS

ON Semiconductor®



**PQFN8 3.3X3.3, 0.65P**  
**CASE 483AX**  
**ISSUE B**

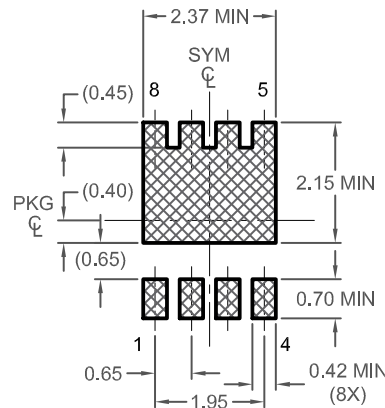
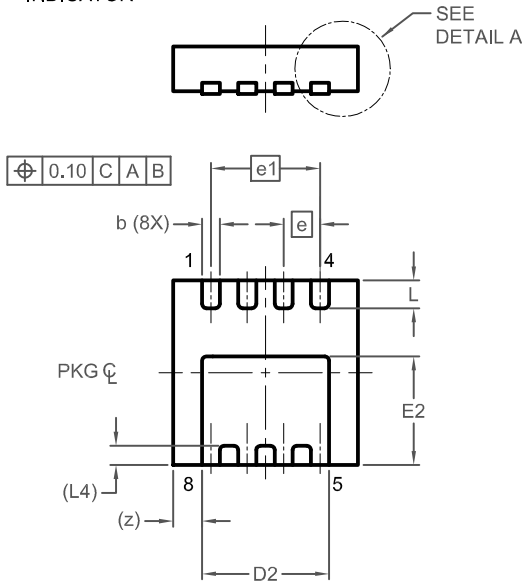
DATE 24 JUN 2022



NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC MO-240, ISSUE A, VAR. BA.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.00	-	0.05
A3	0.20 REF		
b	0.27	0.32	0.37
D	3.20	3.30	3.40
D2	2.17	2.27	2.37
E	3.20	3.30	3.40
E2	1.84	1.94	2.04
e	0.65 BSC		
e1	1.95 BSC		
L	0.40	0.50	0.60
L4	0.34 REF		
z	0.52 REF		



**LAND PATTERN RECOMMENDATION**

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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