

<For DTr1(NPN)>

| Parameter | Value |
|---------------|--------------|
| V_{CC} | 50V |
| $I_{C(MAX.)}$ | 100mA |
| R_1 | 47k Ω |
| R_2 | 47k Ω |

<For DTr2(PNP)>

| Parameter | Value |
|---------------|--------------|
| V_{CC} | -50V |
| $I_{C(MAX.)}$ | -100mA |
| R_1 | 10k Ω |
| R_2 | 47k Ω |

●Features

- 1)Both the DTA114Y chip and DTC144E chip in an EMT6 or UMT6 package.
- 2)Mounting possible with EMT3 or UMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4)Mounting cost and area can be cut in half.

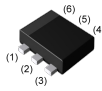
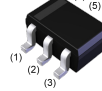
●Application

INVERTER, TERFACE, DRIVER

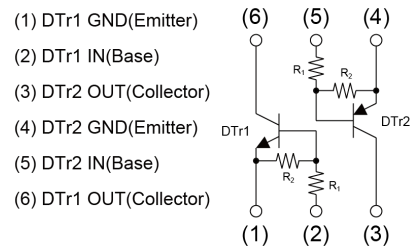
●Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|----------------|--------------|-------------|----------------|-----------------|---------------------------|---------|
| EMD4 | SOT-563 (EMT6) | 1616 | T2R | 180 | 8 | 8000 | D4 |
| UMD4N | SOT-363 (UMT6) | 2021 | TR | 180 | 8 | 3000 | D4 |

●Outline

| | |
|--|--|
| <p>SOT-563</p>  <p>EMD4 (EMT6)</p> | <p>SOT-363</p>  <p>UMD4N (UMT6)</p> |
|--|--|

●Inner circuit



● Absolute maximum ratings ($T_a = 25^\circ\text{C}$)

| Parameter | | Symbol | DTr1(NPN) | DTr2(PNP) | Unit |
|------------------------------|-------------|-------------------|-------------|-----------|------------------|
| Supply voltage | | V_{CC} | 50 | -50 | V |
| Input voltage | | V_{IN} | -10 to 40 | -40 to 6 | V |
| Output current | | I_O | 30 | -70 | mA |
| Collector current | | $I_{C(MAX)}^{*1}$ | 100 | -100 | mA |
| Power dissipation | EMD4/ UMD4N | P_D^{*2*3} | 150 | | mW/Total |
| Junction temperature | | T_j | 150 | | $^\circ\text{C}$ |
| Range of storage temperature | | T_{stg} | -55 to +150 | | $^\circ\text{C}$ |

● Electrical characteristics ($T_a = 25^\circ\text{C}$) <For DTr1(NPN)>

| Parameter | Symbol | Conditions | Values | | | Unit |
|----------------------|--------------|---|--------|------|------|---------------|
| | | | Min. | Typ. | Max. | |
| Input voltage | $V_{I(off)}$ | $V_{CC} = 5V, I_O = 100\mu\text{A}$ | - | - | 0.5 | V |
| | $V_{I(on)}$ | $V_O = 0.3V, I_O = 2\text{mA}$ | 3.0 | - | - | |
| Output voltage | $V_{O(on)}$ | $I_O = 10\text{mA}, I_I = 0.5\text{mA}$ | - | 100 | 300 | mV |
| Input current | I_I | $V_I = 5V$ | - | - | 180 | μA |
| Output current | $I_{O(off)}$ | $V_{CC} = 50V, V_I = 0V$ | - | - | 500 | nA |
| DC current gain | G_I | $V_O = 5V, I_O = 5\text{mA}$ | 68 | - | - | - |
| Input resistance | R_1 | - | 32.9 | 47 | 61.1 | k Ω |
| Resistance ratio | R_2/R_1 | - | 0.8 | 1.0 | 1.2 | - |
| Transition frequency | f_T^{*1} | $V_{CE} = 10V, I_E = -5\text{mA},$ $f = 100\text{MHz}$ | - | 250 | - | MHz |

● Electrical characteristics ($T_a = 25^\circ\text{C}$) <For DTr2(PNP)>

| Parameter | Symbol | Conditions | Values | | | Unit |
|----------------------|--------------|---|--------|------|------|---------------|
| | | | Min. | Typ. | Max. | |
| Input voltage | $V_{I(off)}$ | $V_{CC} = -5V, I_O = -100\mu\text{A}$ | - | - | -0.3 | V |
| | $V_{I(on)}$ | $V_O = -0.3V, I_O = -1\text{mA}$ | -1.4 | - | - | |
| Output voltage | $V_{O(on)}$ | $I_O = -5\text{mA}, I_I = -0.25\text{mA}$ | - | -100 | -300 | mV |
| Input current | I_I | $V_I = -5V$ | - | - | -880 | μA |
| Output current | $I_{O(off)}$ | $V_{CC} = -50V, V_I = 0V$ | - | - | -500 | nA |
| DC current gain | G_I | $V_O = -5V, I_O = -5\text{mA}$ | 68 | - | - | - |
| Input resistance | R_1 | - | 7 | 10 | 13 | k Ω |
| Resistance ratio | R_2/R_1 | - | 3.7 | 4.7 | 5.7 | - |
| Transition frequency | f_T^{*1} | $V_{CE} = -10V, I_E = 5\text{mA},$ $f = 100\text{MHz}$ | - | 250 | - | MHz |

*1 Characteristics of built-in transistor.

*2 Each terminal mounted on a reference land.

*3 120mW per element must not be exceeded.

●Electrical characteristic curves($T_a = 25^\circ\text{C}$) <For DTR1(NPN)>

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

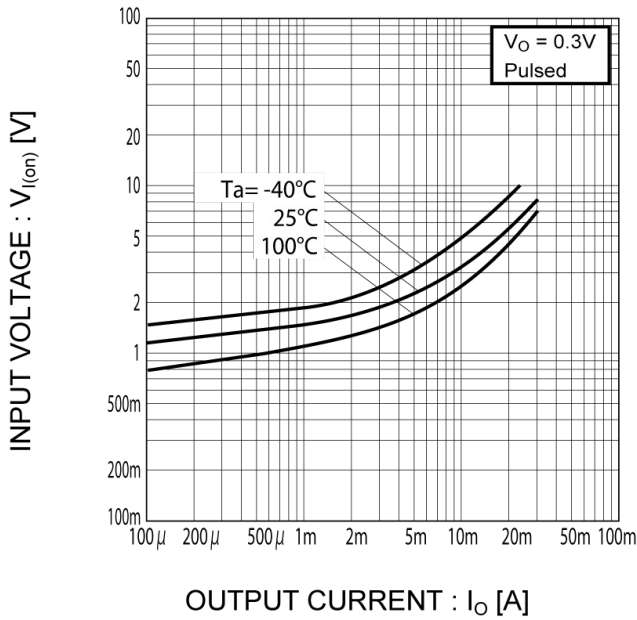


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

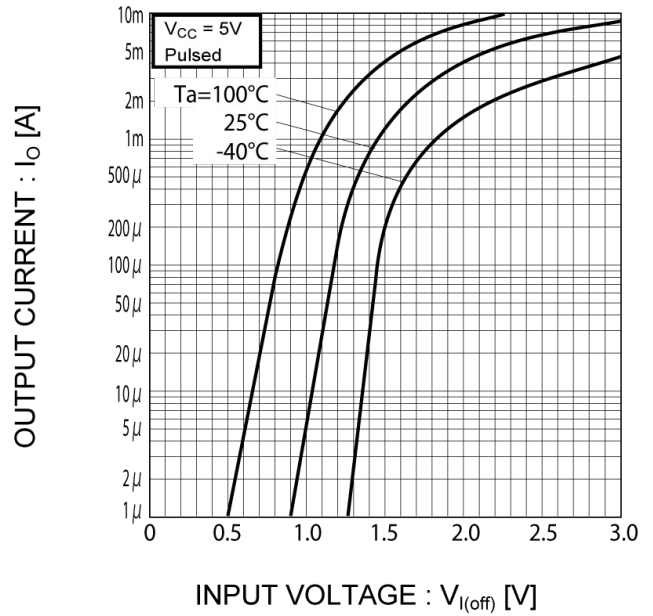


Fig.3 Output Current vs. Output Voltage

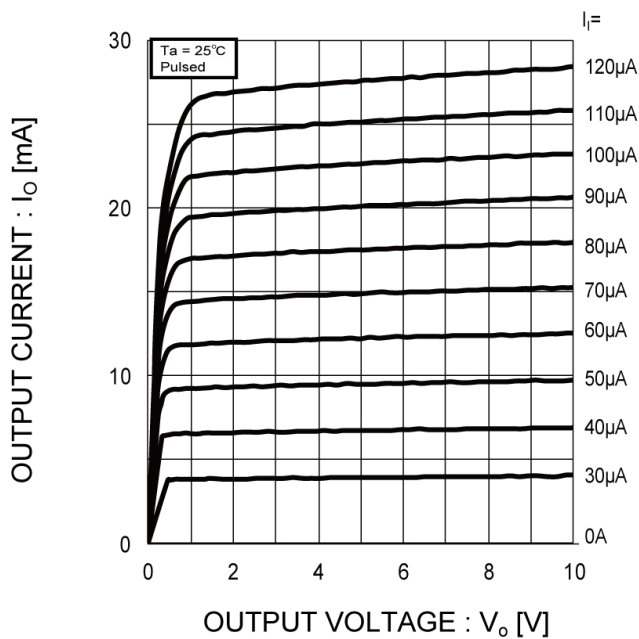
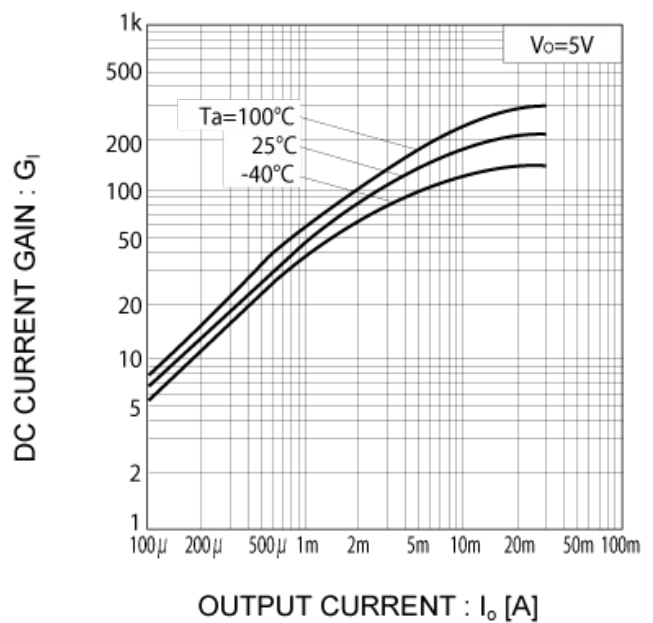
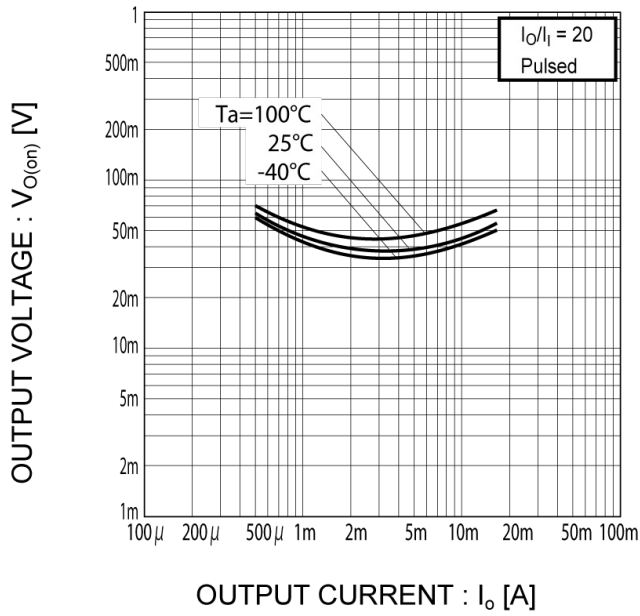


Fig.4 DC Current Gain vs. Output Current



● Electrical characteristic curves ($T_a = 25^\circ\text{C}$) <For DTR1(NPN)>

Fig.5 Output Voltage vs. Output Current



●Electrical characteristic curves($T_a=25^{\circ}\text{C}$) <For DTr2(PNP)>

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

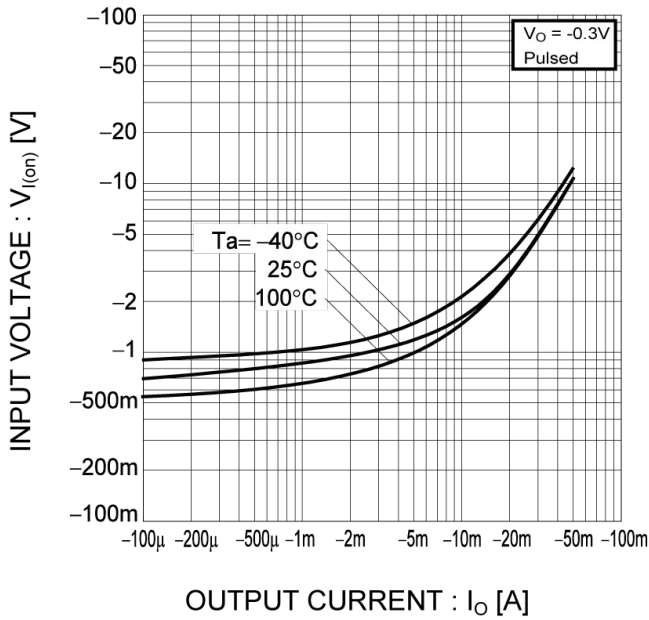


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

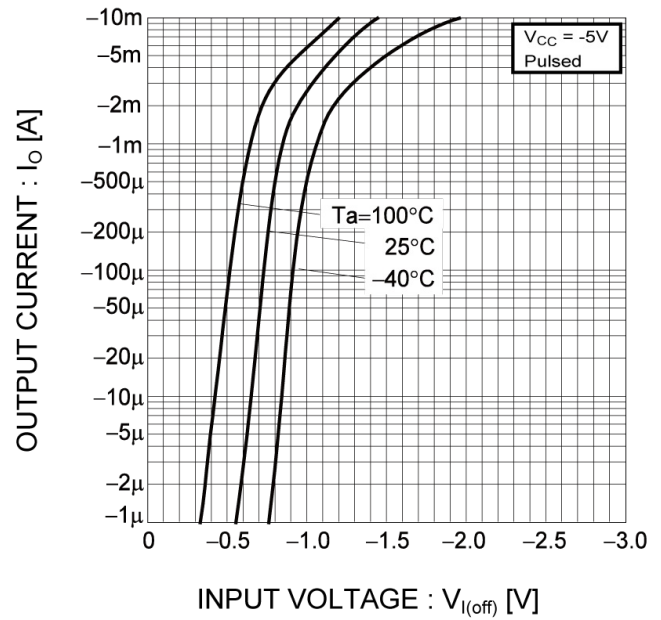


Fig.3 Output Current vs. Output Voltage

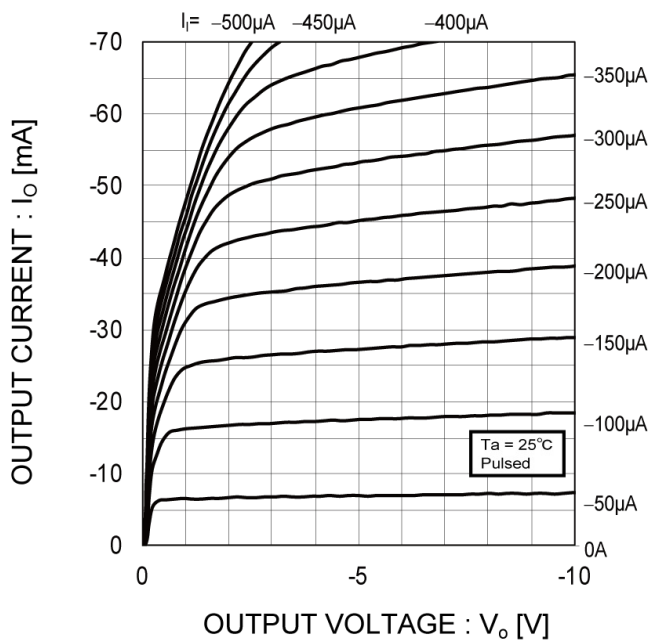
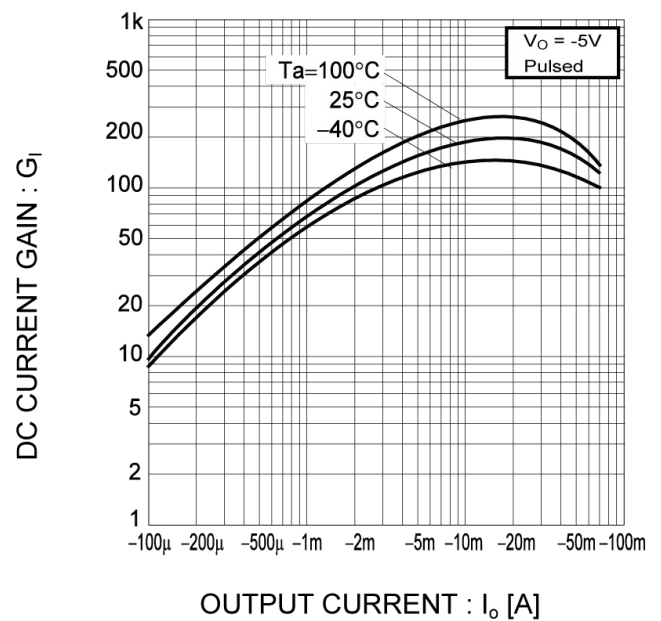
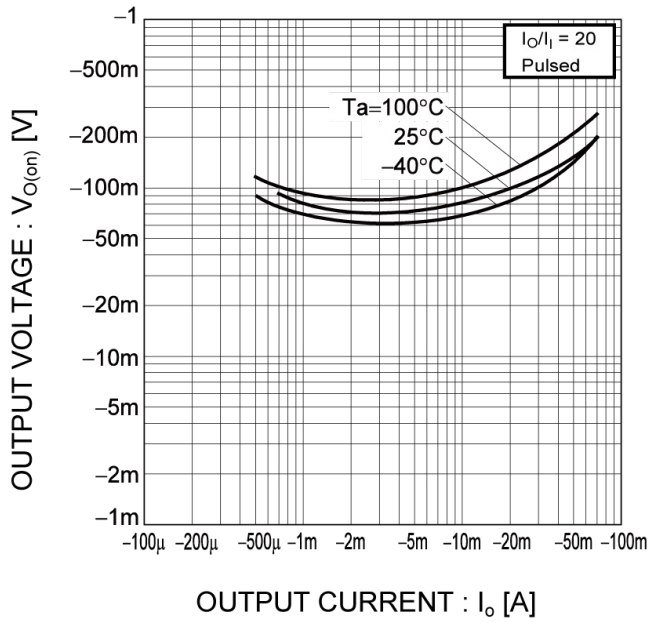


Fig.4 DC Current Gain vs. Output Current



● Electrical characteristic curves ($T_a=25^\circ\text{C}$) <For DTr2(PNP)>

Fig.5 Output Voltage vs. Output Current



●Dimensions



Pattern of terminal position areas
[Not a pattern of soldering pads]

| DIM | MILIMETERS | | INCHES | |
|-----|------------|------|--------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.45 | 0.55 | 0.018 | 0.022 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| b | 0.17 | 0.27 | 0.007 | 0.011 |
| c | 0.08 | 0.18 | 0.003 | 0.007 |
| D | 1.50 | 1.70 | 0.059 | 0.067 |
| E | 1.10 | 1.30 | 0.043 | 0.051 |
| e | 0.50 | | 0.020 | |
| HE | 1.50 | 1.70 | 0.059 | 0.067 |
| L | 0.10 | 0.30 | 0.004 | 0.012 |
| Lp | - | 0.35 | - | 0.014 |
| x | - | 0.10 | - | 0.004 |
| y | - | 0.10 | - | 0.004 |

| DIM | MILIMETERS | | INCHES | |
|-----|------------|------|--------|-------|
| | MIN | MAX | MIN | MAX |
| b2 | - | 0.37 | - | 0.015 |
| e1 | 1.25 | | 0.049 | |
| I1 | - | 0.45 | - | 0.018 |

Dimension in mm/inches

●Dimensions



| DIM | MILIMETERS | | INCHES | |
|-----|------------|------|--------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.80 | 1.00 | 0.031 | 0.039 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| A3 | 0.25 | | 0.010 | |
| b | 0.15 | 0.30 | 0.006 | 0.012 |
| c | 0.10 | 0.20 | 0.004 | 0.008 |
| D | 1.90 | 2.10 | 0.075 | 0.083 |
| E | 1.15 | 1.35 | 0.045 | 0.053 |
| e | 0.65 | | 0.026 | |
| HE | 2.00 | 2.20 | 0.079 | 0.087 |
| L1 | 0.20 | 0.50 | 0.008 | 0.020 |
| Lp | 0.25 | 0.55 | 0.010 | 0.022 |
| Q | 0.10 | 0.30 | 0.004 | 0.012 |
| x | - | 0.10 | - | 0.004 |
| y | - | 0.10 | - | 0.004 |

| DIM | MILIMETERS | | INCHES | |
|-----|------------|------|--------|-------|
| | MIN | MAX | MIN | MAX |
| b2 | - | 0.40 | - | 0.016 |
| e1 | 1.55 | | 0.061 | |
| l1 | - | 0.65 | - | 0.026 |

Dimension in mm/inches

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

| JAPAN | USA | EU | CHINA |
|-----------|-----------|------------|-----------|
| CLASS III | CLASS III | CLASS II b | CLASS III |
| CLASS IV | | CLASS III | |

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 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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