

DRV612EVM

This user's guide describes the operation of the DRV612 evaluation module. This document provides design information including a schematic, bill of materials, and printed circuit board (PCB) layout drawings.

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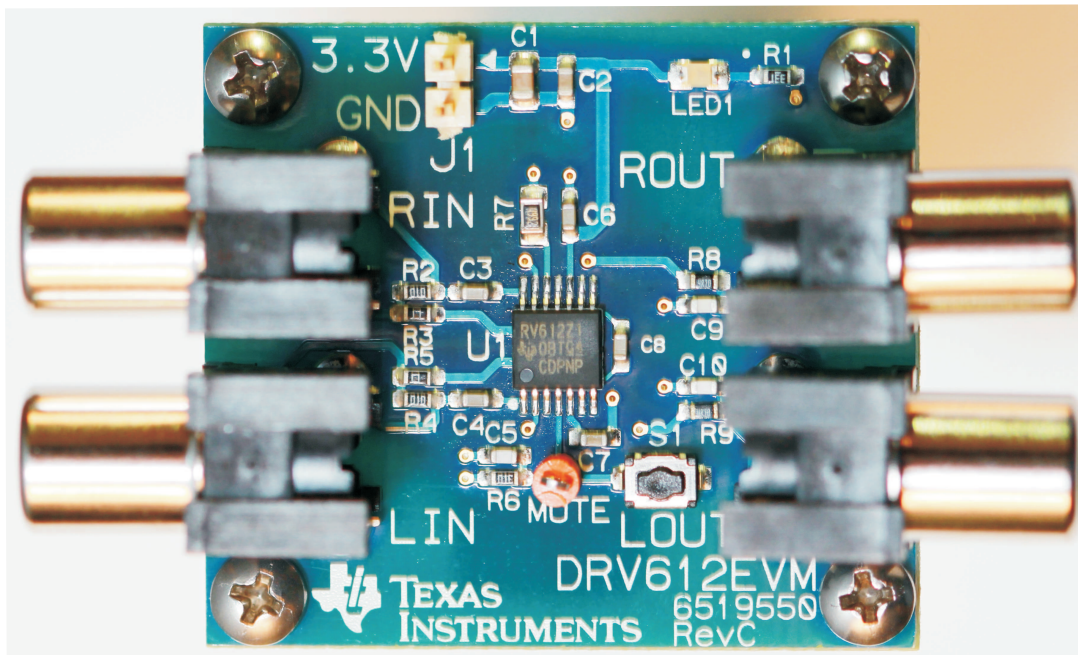


Figure 1. DRV612EVM - Top View

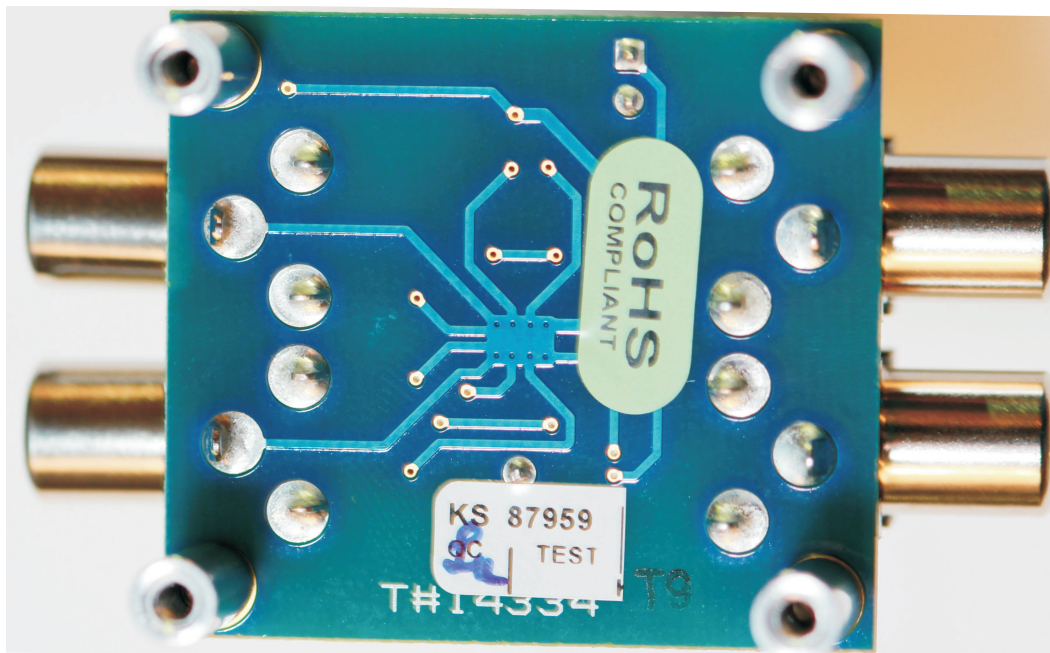


Figure 2. DRV612EVM - Bottom View

1 Overview

The DRV612EVM customer evaluation module (EVM) demonstrates the capabilities and operation of the DRV612 integrated circuit from Texas Instruments.

The DRV612 is a 2-VRMS pop-free stereo line driver designed to allow removal of the output dc-blocking capacitors and combined with the internal programmable gain resistors reduce component count and overall cost. The device is ideal for single-supply electronics where size and cost are critical design parameters.

Designed using TI's patented DirectPath™ technology, the DRV612 is capable of driving 2 VRMS into a 600Ω load. The device has fixed-gain single-ended inputs with a gain select pin. Line outputs have ±8kV HBM ESD protection enabling and a simple ESD protection circuit. The DRV612 has built-in active mode control with more than 80 dB attenuation for pop-free mute on/off control.

The DRV612 does not require a power supply greater than 3.3V to generate its 5.6Vpp output, nor does it require a split rail power supply. The DRV612 integrates its own charge pump to generate a negative supply rail that provides a clean, pop-free ground biased 2Vrms output.

The DRV612 is available in a 14-pin TSSOP package.

This EVM is configured with RCA stereo connectors for analog input and stereo output; The power supply is connected via a two-pin, 2.54-mm header.

This evaluation board is designed for testing applications such as set-top boxes, LCD/PDP TVs, Blu-ray Disc™ DVD players, DVD mini-component systems, home theater-in-a-box (HTIB) systems, or soundcards.

NOTE: This document presents EVM specifications, audio performance measurements graphs, and design documentation that includes complete circuit descriptions, schematic diagrams, a parts list, and PCB layout design. Gerber (layout) files are available from the TI web site at <http://www.ti.com>.

Throughout this document, the abbreviation EVM and the term evaluation module are synonymous with the DRV612EVM.

1.1 DRV612EVM Features

The DRV612EVM has these features:

- Two-channel evaluation module (double-sided, plated-through PCB layout)
- 2- V_{RMS} stereo line output
- Single-ended analog inputs
- No output capacitor required
- Mute button

Figure 3 illustrates the functional diagram for the EVM. Table 1 summarizes the key parameters.

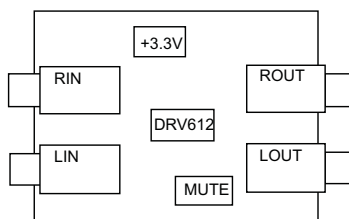


Figure 3. DRV612EVM Functional Block Diagram

Table 1. Key Parameters

Key Parameters	Specification/Unit
Supply Voltage	3.3 V
Number of channels	Two
Load Impedance	600 Ω
Output Voltage	2 Vrms
Dynamic Range	105 dB

2 Quick Setup

This section describes the DRV612EVM board with regards to the power supply and system interfaces. It provides information about handling and unpacking the DRV612EVM, absolute operating conditions, and a description of the factory default configurations.

The following subsections provide a step-by-step guide to configuring the DRV612EVM for device evaluation.

2.1 Electrostatic Discharge Notice

CAUTION

Many of the components on the DRV612EVM are susceptible to damage by electrostatic discharge (ESD). Customers are advised to observe proper ESD handling precautions when unpacking and handling the EVM, including the use of a grounded wrist strap at an approved ESD workstation. Failure to observe proper ESD handling procedures may result in damage to EVM components.

2.2 Unpacking the EVM

Upon opening the DRV612EVM package, ensure that the following items are included:

- One DRV612EVM evaluation board, including:
 - One DRV612PW device

If either item is missing, please contact the Texas Instruments Product Information Center nearest you to inquire about a replacement.

2.3 Power Supply Setup

A single power supply is required to power up the EVM. The power supply is connected to the EVM board using a two-pin, 2.54-mm header (J1). [Table 2](#) lists the recommended supply voltage.

Table 2. Recommended Supply Voltage

Description	Voltage Limitations	Current Requirement	Cable
Power supply	3.3 V	0.10 A	—

CAUTION

Applying voltages above the limitations given in [Table 2](#) may cause permanent damage to your hardware.

2.3.1 Pop-Free Power Up

Pop-free power up is ensured by keeping the $\overline{\text{MUTE}}$ low during power supply ramp up and down. The pins should be kept low until the input AC-coupling capacitors are fully charged before asserting the MUTE pin high, this way proper pre-charge of the ac-coupling is performed and pop-less power-up is achieved.

Figure 4 illustrates the preferred sequence.

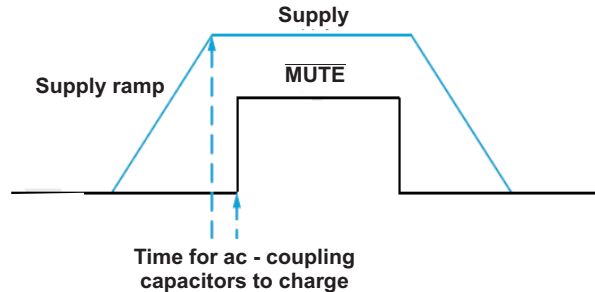


Figure 4. Power-Up/Down Sequence

3 Component Selection

3.1 Charge Pump

The charge pump flying capacitor, C6, serves to transfer charge during the generation of the negative supply voltage. The PVSS capacitor must be at least equal to the charge pump capacitor in order to allow a maximum charge transfer. Low equivalent series resistance (ESR) capacitors are an ideal selection, with a typical value of 1 μF . Capacitor values less than 1 μF can be used, but the maximum output can be reduced. It is therefore recommended to validate the design with thorough testing.

3.2 Power-Supply Decoupling Capacitors

The DRV612 is a DirectPath™ line driver amplifier that requires adequate power-supply decoupling to ensure that noise and total harmonic distortion (THD) are low. A good low ESR ceramic capacitor, C5 (1 μF typical), placed as close as possible to the device P_{VDD} leads is the best option. Placing this decoupling capacitor close to the DRV612 device is important for amplifier performance. For filtering lower-frequency noise signals, a 10- μF or greater capacitor placed near the audio amplifier may also help, but is not required in most applications because of the high PSRR of the DRV612.

The charge pump circuit does apply ripple current on the P_{VDD} line, and an LC or RC filter may be needed if noise-sensitive audio devices share the P_{VDD} supply.

4 Layout Recommendations

4.1 PGND and GND Connections

The PGND and GND pins of the DRV612 must be routed separately back to the decoupling capacitor in order to facilitate proper device operation.

On the DRV612EVM, a star ground routing pattern is used; the star point is located directly below the DRV612 device itself.

5 Design Documentation

5.1 Schematic and PCB Layout

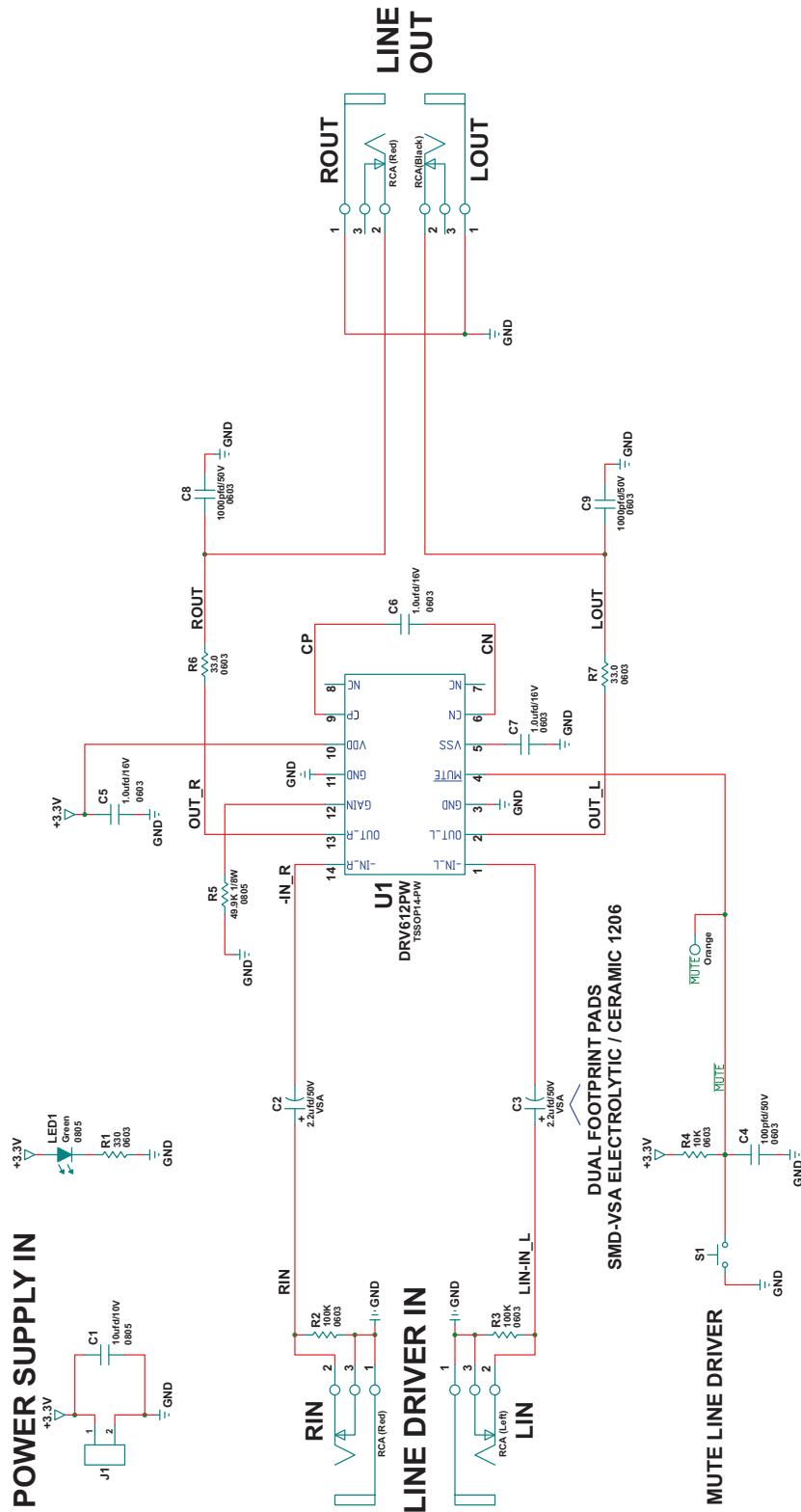
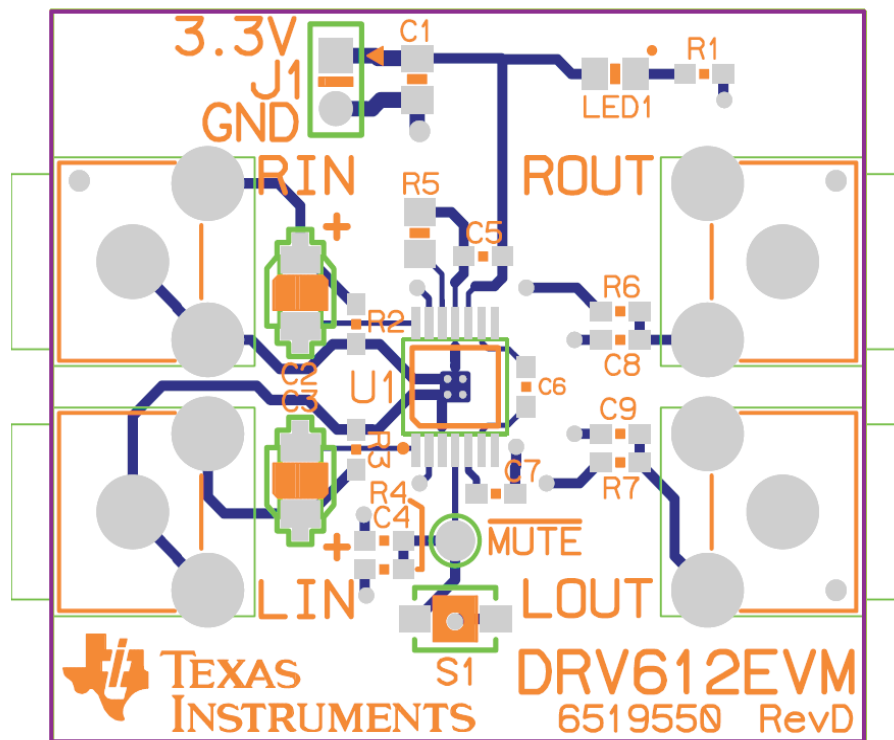


Figure 5. DRV612EVm Schematic: DRV612


Figure 6. DRV612EVM PCB Layout

5.2 Parts List

The bill of materials (BOM) for the DRV612EVM is listed in [Table 3](#).

Table 3. Bill of Materials

Qty	Part Reference	Description	Manufacturer	Mfr Part No
1	U1	DIRECTPATH 2V LINE DRIVER/ FIXED GAIN 14-pin HTSSOP PW ROHS	TEXAS INSTRUMENTS	DRV612PW
1	LED1	LED, GREEN 2.0V SMD0805 ROHS	LUMEX OPTO	SML-LXT0805GW-TR
2	C8, C9	CAP SMD0603 CERM 1000PFD 50V 5% COG ROHS	TDK CORP.	C1608C0G1H102J
1	C4	CAP SMD0603 CERM 100PFD 50V 5% COG ROHS	MURATA	GRM1885C1H101JA01D
2	C2, C3	CAP ALUM-ELECT SMD-VSA 2.2UF 50V 20% ROHS	PANASONIC	EEE-1HS2R2SR
3	C5, C6, C7	CAP SMD0603 CERM 1.0UF 16V 10% X7R ROHS	TDK	C1608X7R1C105K
1	C1	CAP SMD0805 CERM 10UF 10V10% X7R ROHS	MURATA	GRM21BR71A106KE51L
2	R6, R7	RESISTOR SMD0603 33.0 OHMS 1% 1/10W ROHS	PANASONIC	ERJ-3EKF33R0
1	R1	RESISTOR SMD0603 330 OHMS 5% 1/10W ROHS	PANASONIC	ERJ-3GEYJ331V
1	R4	RESISTOR SMD0603 10K 5% 1/10W ROHS	PANASONIC	ERJ-3GEYJ103V
1	R5	RESISTOR SMD0805 49.9K OHMS 1% 1/8W THICK FILM ROHS	VISHAY	CRCW080549K9FKEA
2	R2, R3	RESISTOR SMD0603 100K OHM 5% THICK FILM 1/10W ROHS	PANASONIC	CRCW06031K02FKTA-ND
1	J1	HEADER THRU MALE 2 PIN 100LS GOLD ROHS	SULLINS	PBC02SAAN
2	LIN, LOUT	JACK, RCA 3-PIN PCB-RA BLACK ROHS	SWITCHCRAFT	PJRN1X1U01X
2	RIN, ROUT	JACK, RCA 3-PIN PCB-RA RED ROHS	SWITCHCRAFT	PJRN1X1U03X
1	MUTE	PC TESTPOINT, ORANGE, ROHS	KEYSTONE ELECTRONICS	5003
1	S1	SWITCH, MOM, 160G SMT 4X3MM ROHS	E-SWITCH	TL1015AF160QG
4	NA	STANDOFF, 4-40, 0.5INx3/16IN, ALUM RND F-F	KEYSTONE ELECTRONICS	2027
4	NA	4-40 SCREW, STEEL 0.250 IN	BUILDING FASTENERS	PMS 440 0025 PH

5.3 PCB Specification

Table 4 summarizes the specifications for the evaluation board PCB.

Table 4. PCB Specification

Board Identification	A852-PCB-001 (2.00)
Board type	Double-sided plated-through board
Laminate type	FR4
Laminate thickness	1,0 mm
Copper thickness	35 µm (including plating exterior layers)
Copper plating of holes	>25 µm
Minimum hole diameter	0.3 mm
Silkscreen, component side	White; remove silkscreen from solder and pre-tinned areas
Silkscreen, component side	None
Soldermask, component side	Green
Soldermask, solder side	Green
Protective coating	Solder coating and chemical silver on free copper
Electrical test	PCB must be electrically tested
Manufactured to	PERFAG 2E (http://www.perfag.dk)
Aperture table	PERFAG 10A (http://www.perfag.dk)
Board dimensions	60 mm × 90 mm

5.4 PCB Layers

Gerber files are available for download at the DRV612EVM product folder page on the TI web site. Component placement and board layout are illustrated in Figure 6.

NOTE: Board layouts are not to scale. These figures are intended to show how the board is laid out; they are not intended to be used for manufacturing DRV612EVM PCBs.

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EVM Warnings and Restrictions

It is important to operate this EVM within the input voltage range of 0 V to 3 V and the output voltage range of 0 V to 3.3 V .

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 60°C. The EVM is designed to operate properly with certain components above 40°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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