# CTIHLP1604AF Series From .10 µH to 10 µH



### **CHARACTERISTICS**

Description: SMD (shielded) power inductor.

Applications: Notebook, Desktop, Server applications, Low profile, high current power supplies, battery powered devices, DC/DC converter for Field Programmable Gate Array (FPGA).

Operating Temperature: -55°C to +125°C (The part temperature (ambient + temp. rise) should not exceed 125°C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application)

Inductance Tolerance: ±20%, ±30%

Testing: Inductance is tested on an HP4285A at 100KHz, 1.0V

Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical

information available upon request.

Samples available. See website for ordering information.

## **SPECIFICATIONS**

Part numbers indicate inductance tolerance available.

 $M = \pm 20\%, N = \pm 30\%$ 

\*Irms: Will cause the coil temp. rise approximately ΔT of 40°C. (Keep 1 Min) \*\*Isat: Will cause L0 to drop approximately 20%. (Keep quickly)

Part Number	Inductance (µH)	L Test Freq. (KHz)	DCR Typ. (mΩ)	DCR Max. (mΩ)	*Irms Typ. (A)	**Isat Typ. (A)
CTIHLP1604AF-R10N CTIHLP1604AF-R22M CTIHLP1604AF-R47M	0.10 0.22 0.47	100 100 100	4.3 6.6 18.0	5.5 8.0 20	11.5 8.5 6.0	25 20 6.5
CTIHLP1604AF-1R0M CTIHLP1604AF-1R5M CTIHLP1604AF-2R2M CTIHLP1604AF-3R3M CTIHLP1604AF-4R7M CTIHLP1604AF-5R6M CTIHLP1604AF-6R8M CTIHLP1604AF-6R8M CTIHLP1604AF-8R2M	1.00 1.50 2.20 3.30 4.70 5.60 6.80 8.20	100 100 100 100 100 100 100 100	41.0 55.0 69.2 84.0 128 180 300 313	47 63.3 80 97 145 208 360 376	4.0 3.0 2.8 2.3 2.0 1.7 1.5	6.0 4.0 3.5 3.0 2.5 2.3 1.7
CTIHLP1604AF-100M	10.0	100	410	463	1.3	1.4

### PHYSICAL DIMENSIONS

Size	A	В	С	D	E
mm inches	4.45±0.25 0.17±0.012	4.06±0.25 0.16±0.008	1.0±0.2 0.04±0.008	0.76±0.3 0.03±0.012	2.0±0.2 0.08±0.008
a .		25	<del>+</del> C+	D	
			Marking: Ind	ductance Code	<del>j</del>

# **PAD LAYOUT**

