



Parameter	Rating	Units
Blocking Voltage	350	V <sub>P</sub>
Load Current	120	mA
Max On-Resistance	35	Ω

### Features

- Current Limited
- 3750V<sub>rms</sub> Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- No Moving Parts
- Small 8-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount and Tape & Reel Versions Available

### Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
  - Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

### Description

The TS120L integrated circuit device combines a 350V, current-limited, normally open (1-Form-A) relay with a Darlington transistor optocoupler in a single package. The relay uses optically coupled MOSFET technology to provide 3750V<sub>rms</sub> of input to output isolation. The efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS® architecture, in which highly efficient GaAIAs infrared LEDs control the optically coupled output.

Telecom circuit designers, using the TS120L, can now take advantage of two discrete functions in a single component that uses less space than traditional discrete component solutions.

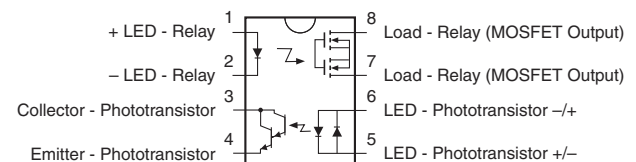
### Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950 Certified Component:  
TUV Certificate: B 10 05 49410 006

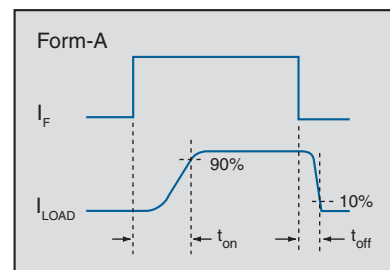
### Ordering Information

Part #	Description
TS120L	8-Pin DIP (50/Tube)
TS120PL	8-Pin Flatpack (50/Tube)
TS120PLTR	8-Pin Flatpack (1000/Reel)
TS120LS	8-Pin Surface Mount (50/Tube)
TS120LSTR	8-Pin Surface Mount (1000/Reel)

### Pin Configuration



### Switching Characteristics of Normally Open Devices



**Absolute Maximum Ratings @ 25°C**

Parameter	Ratings	Units
Relay Blocking Voltage	350	V <sub>P</sub>
Input Power Dissipation <sup>1</sup>	150	mW
Input Control Current, Relay	50	mA
Peak (10ms)	1	A
Input Control Current, Detector	100	mA
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.*

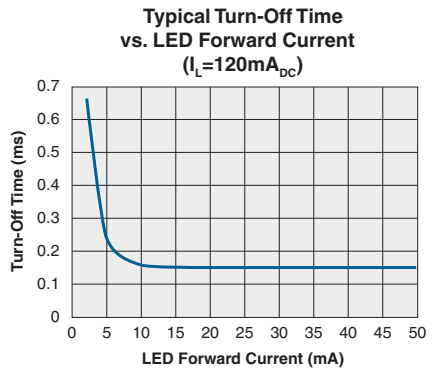
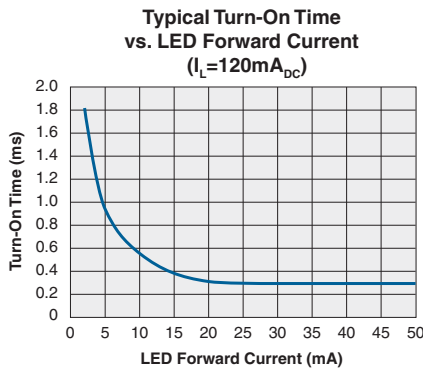
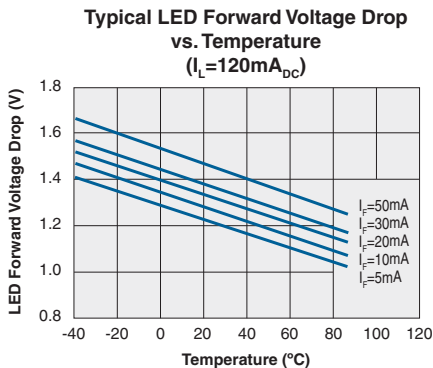
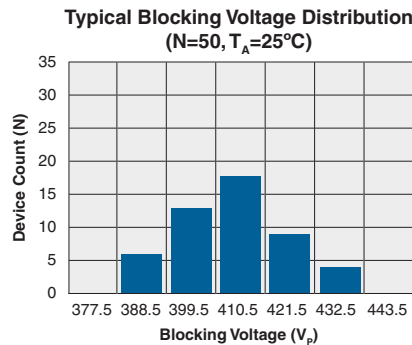
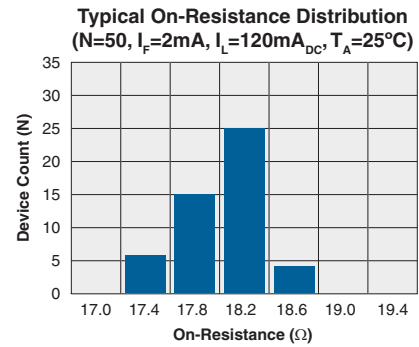
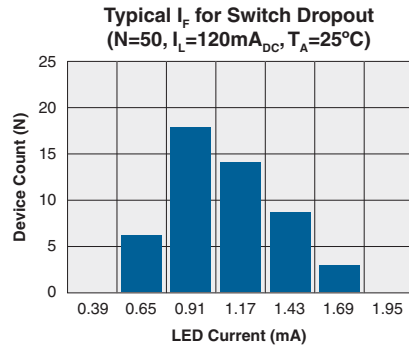
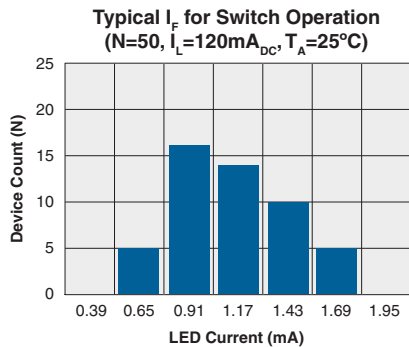
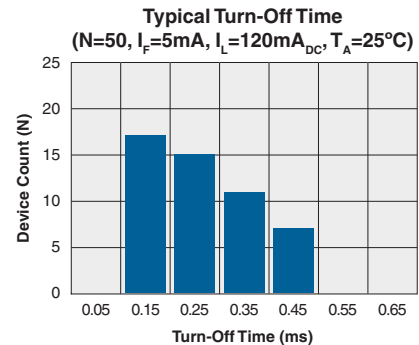
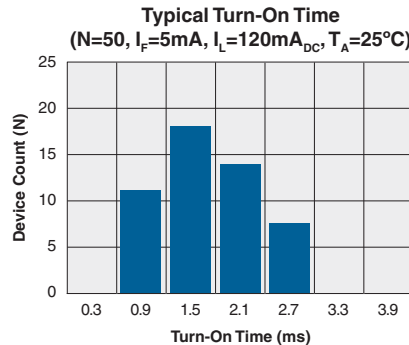
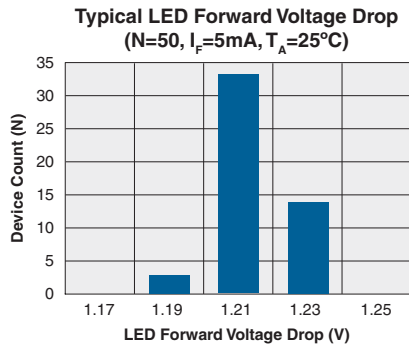
**Electrical Characteristics @25°C: Relay Section**

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current						
Continuous	-	I <sub>L</sub>	-	-	120	mA
Peak	t=10ms	I <sub>LPK</sub>	-	-	350	mA
Current Limit	I <sub>F</sub> =5mA	I <sub>CL</sub>	130	170	210	mA
On-Resistance	I <sub>L</sub> =120mA	R <sub>ON</sub>	-	-	35	Ω
Off-State Leakage Current	V <sub>L</sub> =350V	I <sub>LEAK</sub>	-	-	1	μA
<b>Switching Speeds</b>						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	-	2.5	ms
Turn-Off		t <sub>off</sub>	-	-	2.5	ms
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	25	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate	I <sub>L</sub> =120mA	I <sub>F</sub>	-	-	5	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.4	0.7	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Voltage	-	V <sub>R</sub>	-	-	5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics</b>						
Input to Output Capacitance	-	C <sub>I/O</sub>	-	3	-	pF

**Electrical Characteristics @25°C: Detector Section**

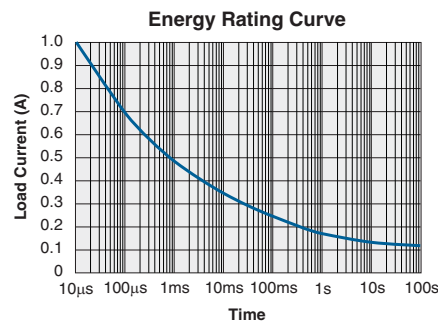
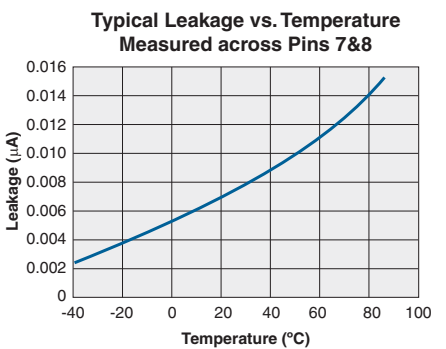
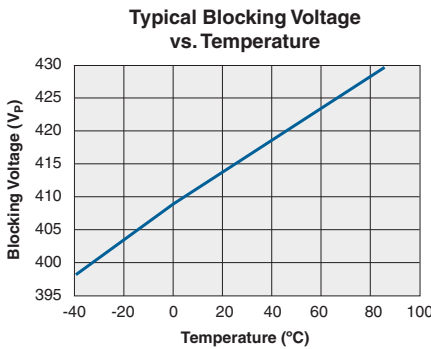
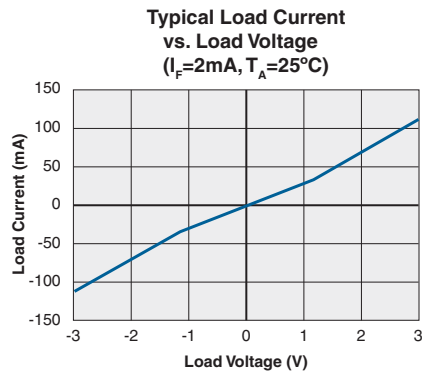
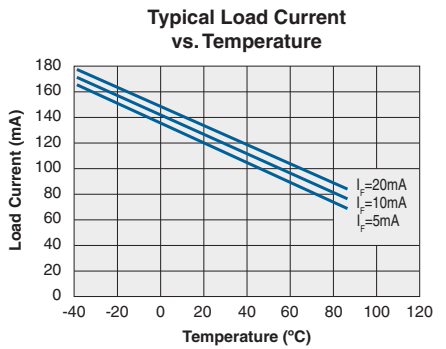
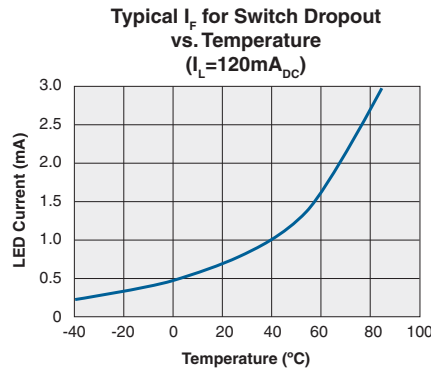
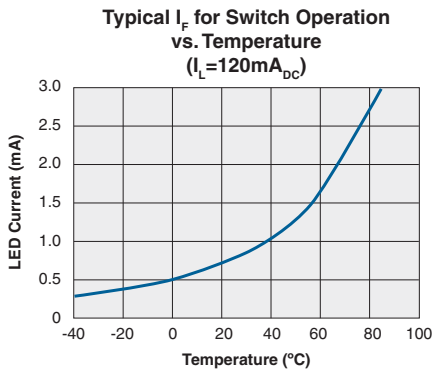
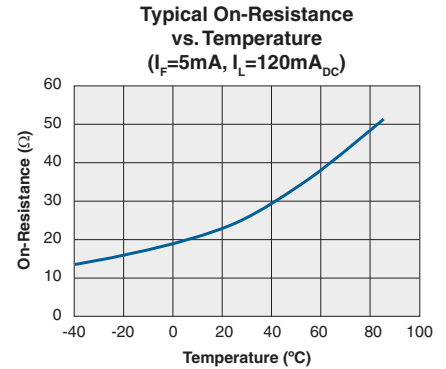
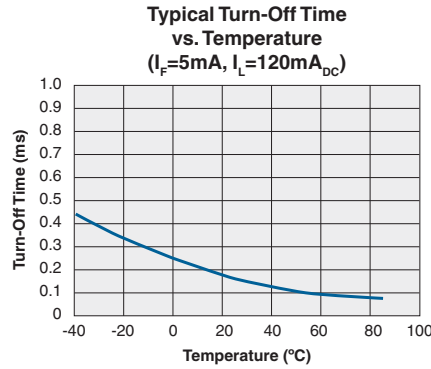
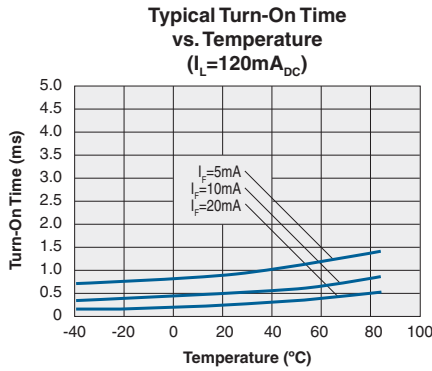
Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Phototransistor Blocking Voltage	I <sub>C</sub> =10μA	BV <sub>CEO</sub>	20	50	-	V
Phototransistor Dark Current	V <sub>CE</sub> =5V, I <sub>F</sub> =0mA	I <sub>CEO</sub>	-	100	1000	nA
Saturation Voltage	I <sub>C</sub> =0.15mA, I <sub>F</sub> =0.05mA	V <sub>SAT</sub>	-	0.5	0.8	V
Current Transfer Ratio	I <sub>F</sub> =0.05mA, V <sub>CE</sub> =0.8V	CTR	300	1000	-	%
<b>Input Characteristics</b>						
Input Control Current	I <sub>C</sub> =2mA, V <sub>CE</sub> =0.5V	I <sub>F</sub>	-	1	2	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Input to Output Capacitance	-	-	-	3	-	pF
Isolation, Input to Output	-	V <sub>I/O</sub>	3750	-	-	V <sub>rms</sub>

**PERFORMANCE DATA: RELAY**



\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

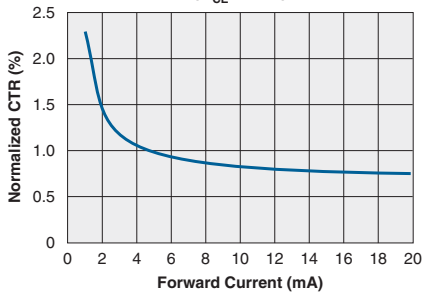
PERFORMANCE DATA: RELAY (cont.)



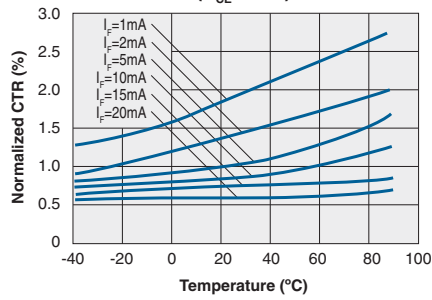
\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**PERFORMANCE DATA: DETECTOR**

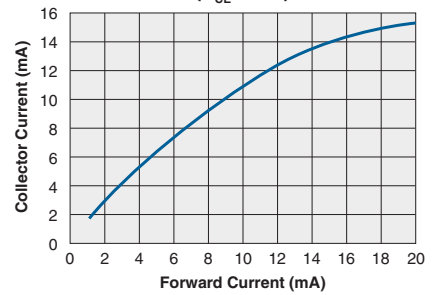
**Typical Normalized CTR vs. Forward Current**  
( $V_{CE}=0.8V$ )



**Typical Normalized CTR vs. Temperature**  
( $V_{CE}=0.8V$ )



**Typical Collector Current vs. Forward Current**  
( $V_{CE}=0.8V$ )



\* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**Manufacturing Information**

**Moisture Sensitivity**



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
TS120L / TS120PL / TS120LS	MSL 1

**ESD Sensitivity**



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

**Reflow Profile**

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
TS120L / TS120LS	250°C for 30 seconds
TS120PL	260°C for 30 seconds

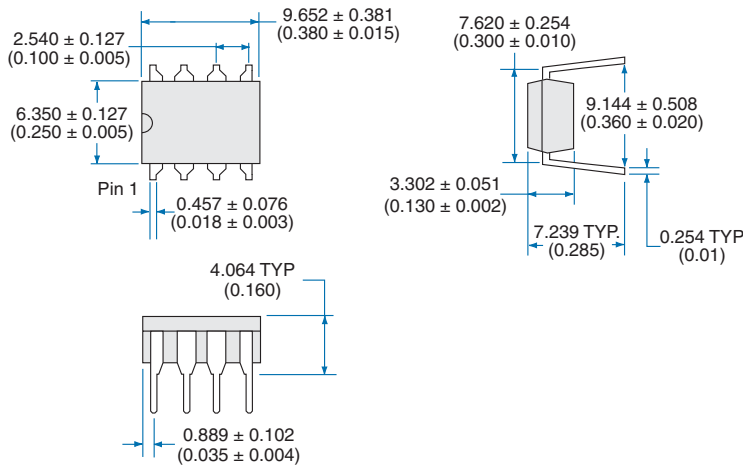
**Board Wash**

Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

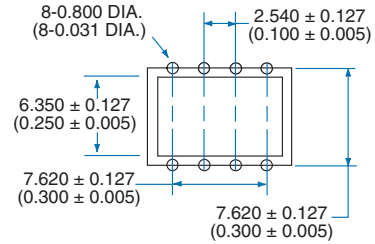


### Mechanical Dimensions

#### TS120L

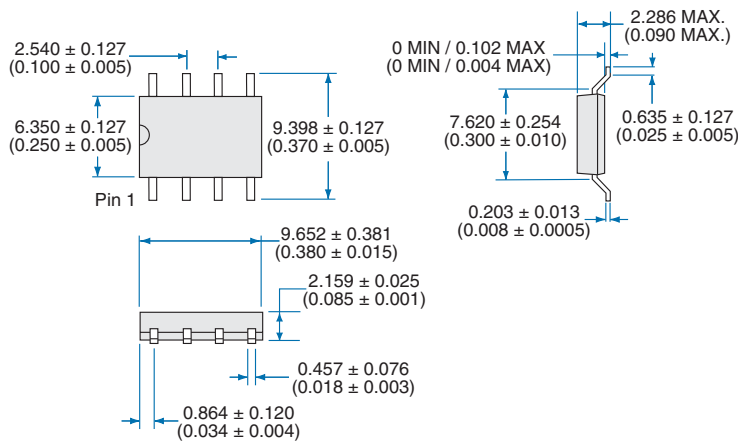


#### PCB Hole Pattern

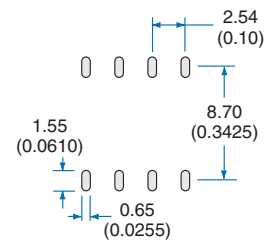


Dimensions  
mm  
(inches)

#### TS120PL

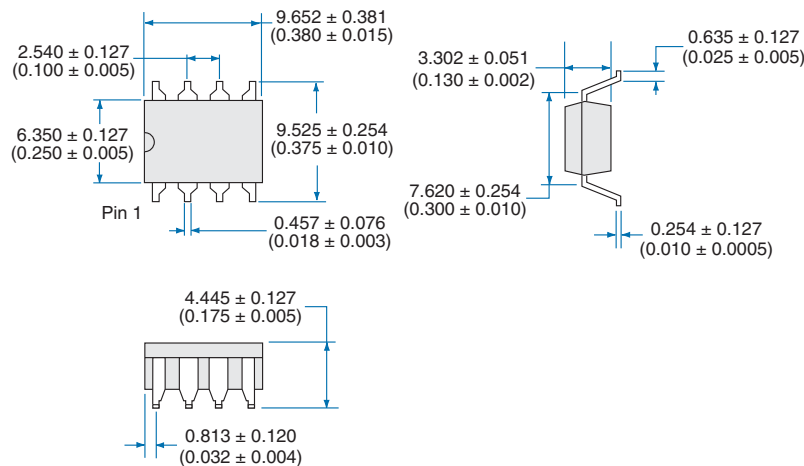


#### PCB Land Pattern

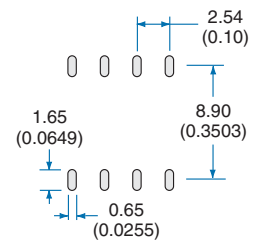


Dimensions  
mm  
(inches)

#### TS120LS



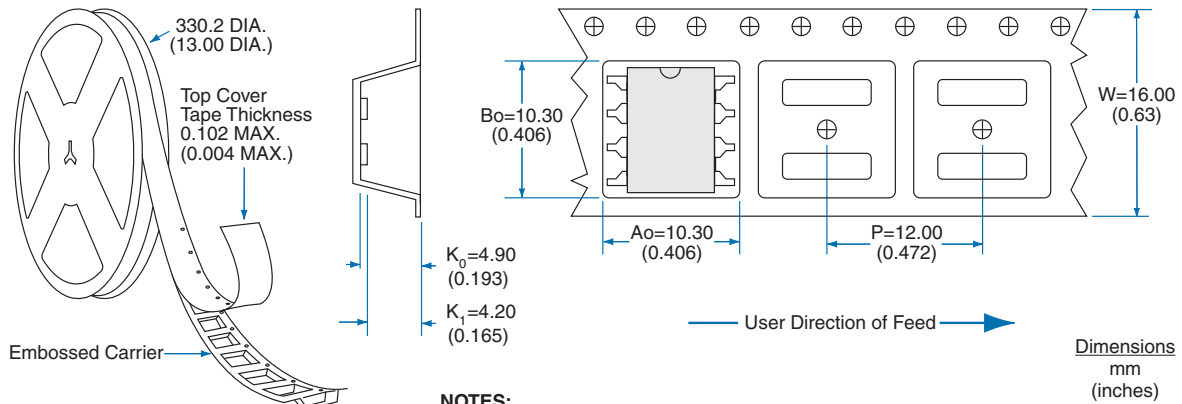
#### PCB Land Pattern



Dimensions  
mm  
(inches)

### Mechanical Dimensions

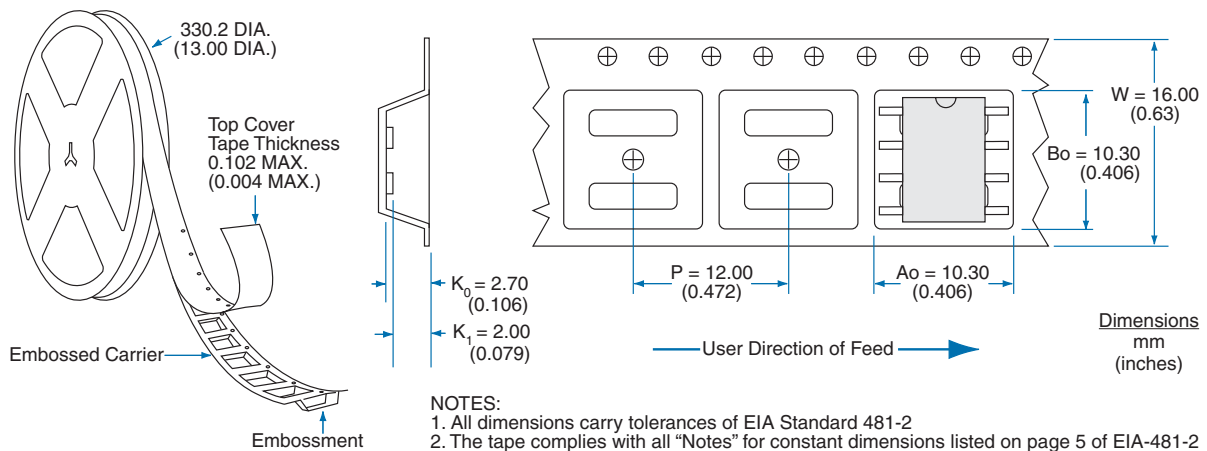
#### TS120LS Tape & Reel



**NOTES:**

1. Dimensions carry tolerances of EIA Standard 481-2
2. Tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

#### TS120PL Tape & Reel



**NOTES:**

1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

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