

## 1. General description

Planar passivated high commutation three quadrant triac in a SOT223 surface mountable plastic package. This triac is intended for use in motor control circuits where very high blocking voltage can occur. It is used in applications where "high junction operating temperature capability ( $T_{j(max)} = 150\text{ °C}$ )" is required.

## 2. Features and benefits

- 3Q technology for improved noise immunity
- High junction operating temperature capability ( $T_{j(max)} = 150\text{ °C}$ )
- Over-voltage withstand capability to IEC 61000-4-5
- Planar passivated for voltage ruggedness and reliability
- High voltage capability
- High immunity to false turn on by dV/dt
- Triggering in three quadrants only
- Surface mountable package

## 3. Applications

- AC fan, pump and compressor controls
- Highly inductive, resistive and safety loads
- Large and small appliances (White Goods)
- Reversing induction motor controls e.g. vertical axis washing machines

## 4. Quick reference data

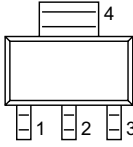
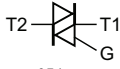
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	-	1000	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{sp} \leq 117\text{ °C}$ ; <a href="#">Fig.1</a> ; <a href="#">Fig. 2</a> ; <a href="#">Fig. 3</a>	-	-	2	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 20\text{ ms}$ ; <a href="#">Fig. 4</a> ; <a href="#">Fig. 5</a>	-	-	25	A
		full sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 16.7\text{ ms}$	-	-	27.5	A
$T_j$	junction temperature		-	-	150	°C
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 7</a>	-	-	35	mA

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 9</a>	-	-	40	mA
$V_T$	on-state voltage	$I_T = 3\text{ A}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 10</a>	-	-	1.5	V
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 670\text{ V}$ ; $T_j = 150\text{ °C}$ ; ( $V_{DM} = 67\%$ of $V_{DRM}$ ); exponential waveform; gate open circuit	1000	-	-	V/ $\mu$ s

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		 sym051
2	T2	main terminal 2		
3	G	gate		
4	mb	mounting base; connected to main terminal 2		

## 6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BTA202W-1000CT	SOT223	BTA202W-1000CTF	Reel	4000	SOT223	16-Mar-2006

## 7. Marking

Table 4. Marking codes

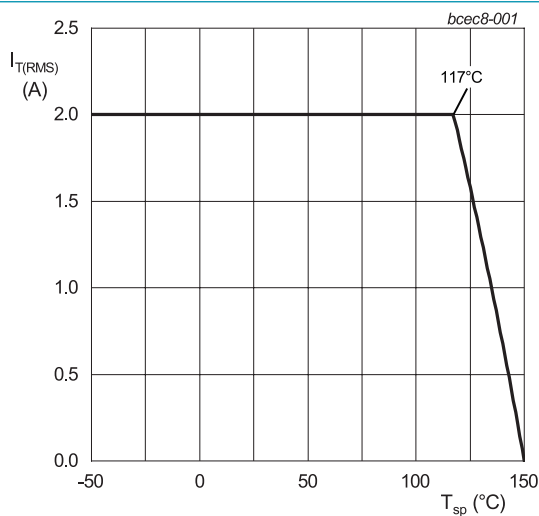
Type number	Marking codes
BTA202W-1000CT	2W 1000CT

## 8. Limiting values

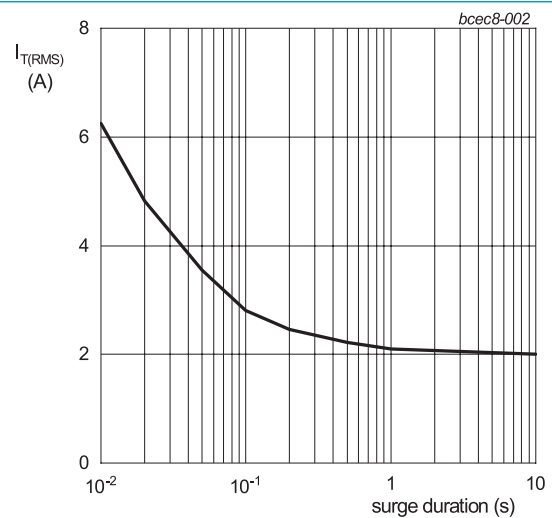
**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

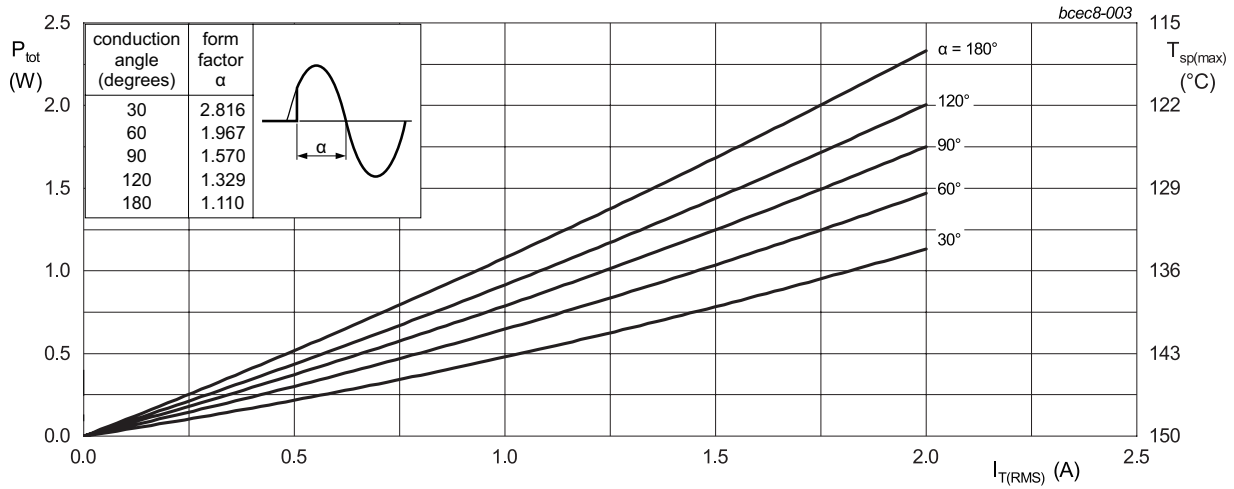
Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	1000	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{sp} \leq 117\text{ }^{\circ}\text{C}$ ; <a href="#">Fig.1</a> ; <a href="#">Fig. 2</a> ; <a href="#">Fig. 3</a>	-	2	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$ ; $t_p = 20\text{ ms}$ ; <a href="#">Fig. 4</a> ; <a href="#">Fig. 5</a>	-	25	A
		full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$ ; $t_p = 16.7\text{ ms}$	-	27.5	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ ms}$ ; sine wave pulse	-	3.125	$\text{A}^2\text{s}$
$di_T/dt$	rate of rise of on-state current	$I_G = 70\text{ mA}$	-	100	$\text{A}/\mu\text{s}$
$I_{GM}$	peak gate current		-	2	A
$P_{GM}$	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
$T_{stg}$	storage temperature		-40	150	$^{\circ}\text{C}$
$T_j$	junction temperature		-	150	$^{\circ}\text{C}$



**Fig. 1. RMS on-state current as a function of solder point temperature; maximum values**

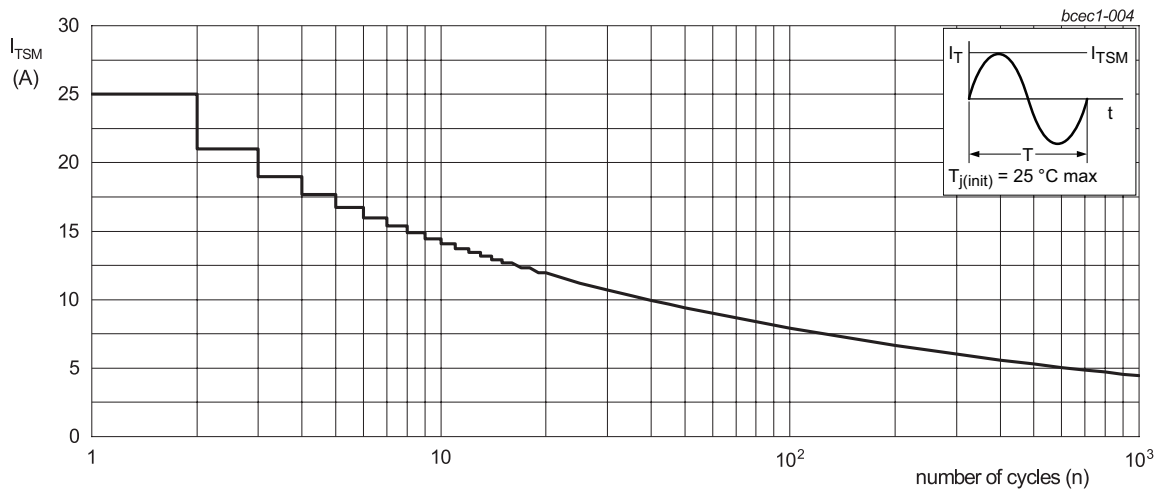


$f = 50\text{ Hz}$ ;  $T_{sp} = 117\text{ }^{\circ}\text{C}$   
**Fig. 2. RMS on-state current as a function of surge duration; maximum values**



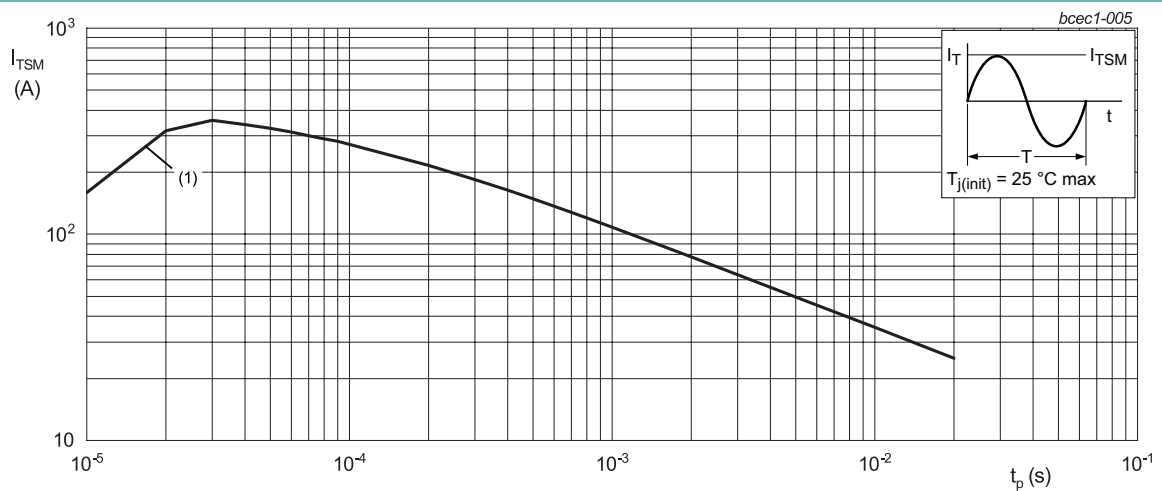
$a = \text{form factor} = I_{T(RMS)} / I_{T(AV)}$   
 $\alpha = \text{conduction angle}$

Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values



$f = 50 \text{ Hz}$

Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



$t_p \leq 20 \text{ ms}$   
 (1)  $di_T/dt$  limit

Fig. 5. Non-repetitive peak on-state current as a function of pulse duration; maximum values

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	<a href="#">Fig. 6</a>	-	-	14	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air; printed circuit board mounted; minimum pad area; <a href="#">Fig. 7</a>	-	70	-	K/W
		in free air; printed circuit board mounted; minimum footprint; <a href="#">Fig. 8</a>	-	156	-	K/W

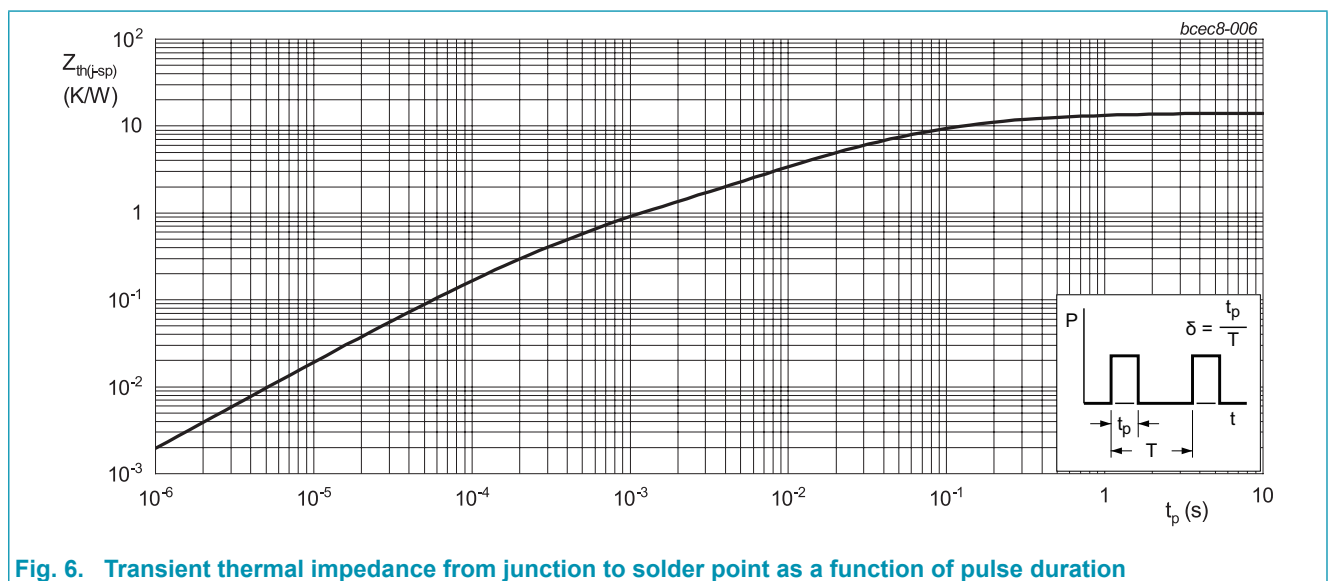
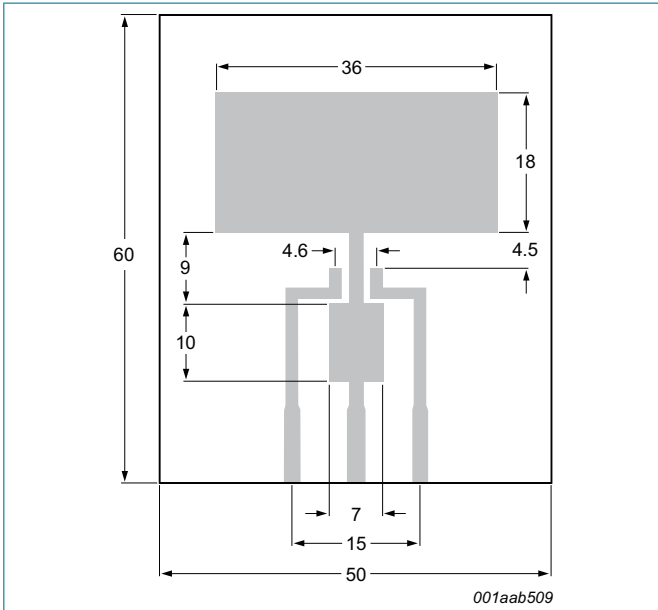
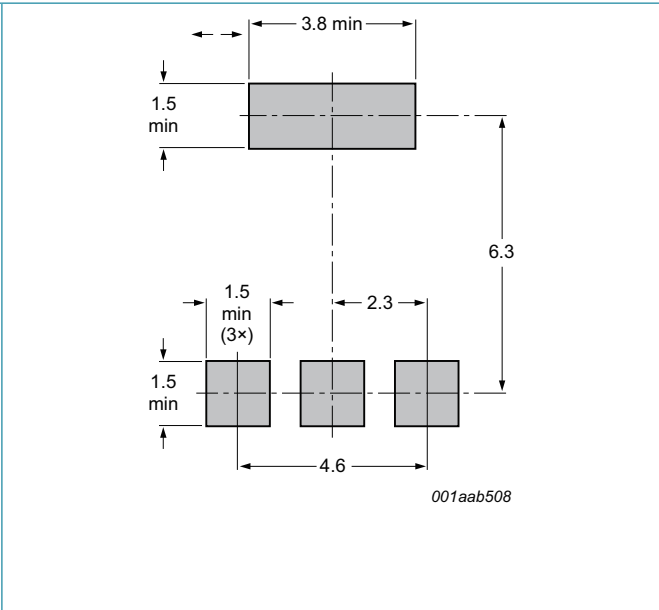


Fig. 6. Transient thermal impedance from junction to solder point as a function of pulse duration



All dimensions are in mm  
 Printed circuit board:  
 FR4 epoxy glass (1.6 mm thick), copper laminate  
 (35 μm thick)

**Fig. 7. Printed circuit board pad area: SOT223**



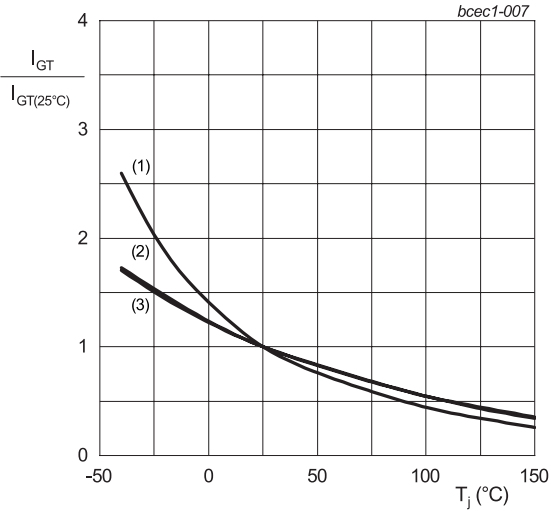
All dimensions are in mm

**Fig. 8. Minimum footprint SOT223**

## 10. Characteristics

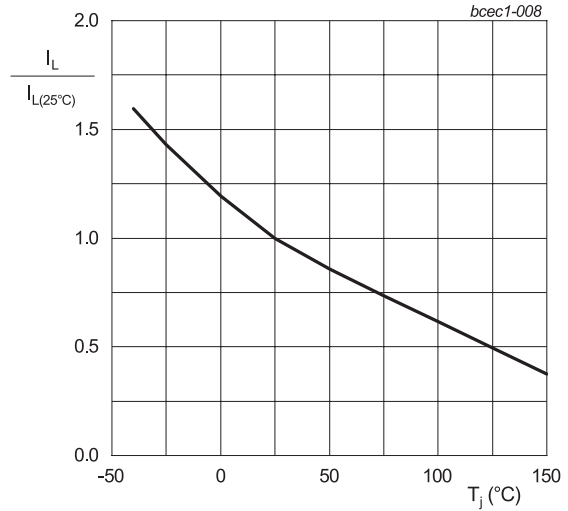
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 9</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 9</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 9</a>	-	-	35	mA
$I_L$	latching current	$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G+; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 10</a>	-	-	60	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G-; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 10</a>	-	-	70	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2- G-; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 10</a>	-	-	60	mA
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 11</a>	-	-	40	mA
$V_T$	on-state voltage	$I_T = 3\text{ A}$ ; $T_J = 25\text{ °C}$ ; <a href="#">Fig. 12</a>	-	-	1.5	V
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_J = 25\text{ °C}$ <a href="#">Fig. 13</a>	-	0.8	1	V
		$V_D = 400\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_J = 150\text{ °C}$	0.2	0.45	-	V
$I_D$	off-state current	$V_D = 1000\text{ V}$ ; $T_J = 25\text{ °C}$	-	-	10	$\mu\text{A}$
		$V_D = 1000\text{ V}$ ; $T_J = 150\text{ °C}$	-	-	1	mA
$I_R$	reverse current	$V_R = 1000\text{ V}$ ; $T_J = 25\text{ °C}$	-	-	10	$\mu\text{A}$
		$V_R = 1000\text{ V}$ ; $T_J = 150\text{ °C}$	-	-	1	mA
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 670\text{ V}$ ; $T_J = 150\text{ °C}$ ; ( $V_{DM} = 67\%$ of $V_{DRM}$ ); exponential waveform; gate open circuit	1000	-	-	V/ $\mu\text{s}$
$dI_{com}/dt$	rate of change of commutating current	$V_D = 400\text{ V}$ ; $T_J = 150\text{ °C}$ ; $I_{T(RMS)} = 2\text{ A}$ ; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$ ; gate open circuit; snubberless condition	6	-	-	A/ms
		$V_D = 400\text{ V}$ ; $T_J = 150\text{ °C}$ ; $I_{T(RMS)} = 2\text{ A}$ ; $dV_{com}/dt = 10\text{ V}/\mu\text{s}$ ; gate open circuit	8	-	-	A/ms
		$V_D = 400\text{ V}$ ; $T_J = 150\text{ °C}$ ; $I_{T(RMS)} = 2\text{ A}$ ; $dV_{com}/dt = 1\text{ V}/\mu\text{s}$ ; gate open circuit	10	-	-	A/ms

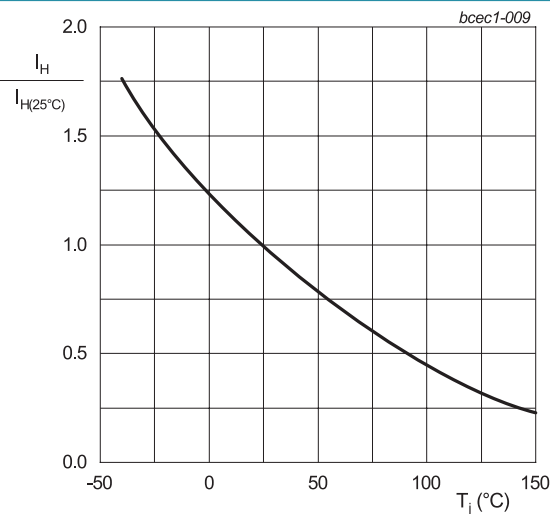


- (1) T2- G-
- (2) T2+ G-
- (3) T2+ G+

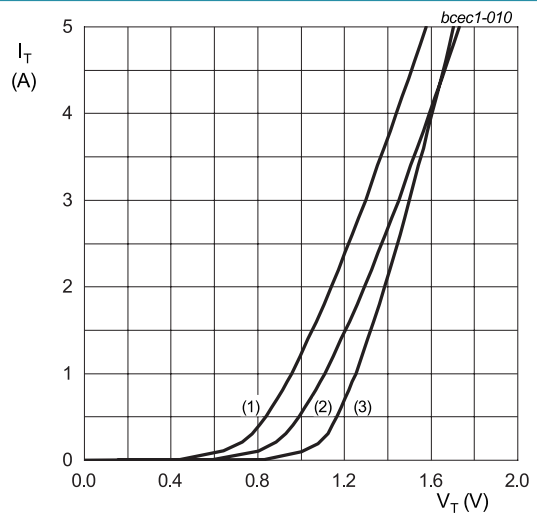
**Fig. 9. Normalized gate trigger current as a function of junction temperature**



**Fig. 10. Normalized latching current as a function of junction temperature**



**Fig. 11. Normalized holding current as a function of junction temperature**



- $V_o = 1.101\text{ V}; R_s = 0.0875\ \Omega$
- (1)  $T_j = 150^\circ\text{C}$ ; typical values
  - (2)  $T_j = 150^\circ\text{C}$ ; maximum values
  - (3)  $T_j = 25^\circ\text{C}$ ; maximum values

**Fig. 12. On-state current as a function of on-state voltage**



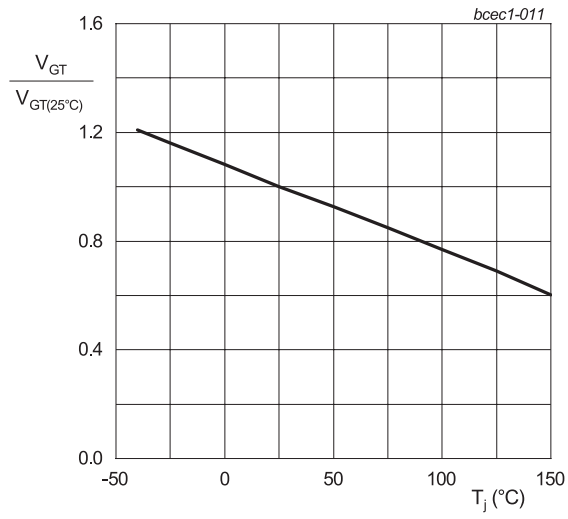
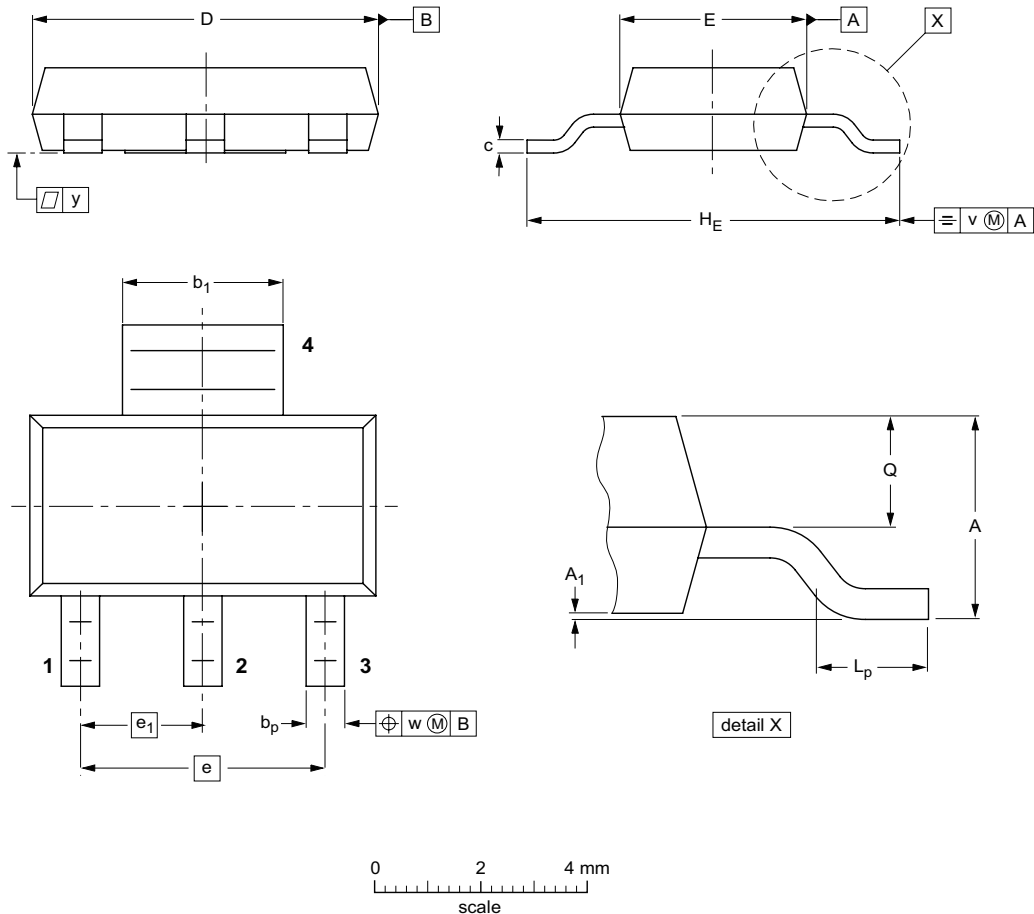


Fig. 13. Normalized gate trigger voltage as a function of junction temperature

### 11. Package outline

Plastic surface-mounted package with increased heatsink; 4 leads

SOT223



**DIMENSIONS** (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	b <sub>p</sub>	b <sub>1</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w	y
mm	1.8	0.10	0.80	3.1	0.32	6.7	3.7	4.6	2.3	7.3	1.1	0.95	0.2	0.1	0.1
	1.5	0.01	0.60	2.9	0.22	6.3	3.3			6.7	0.7	0.85			

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT223			SC-73			04-11-10 06-03-16

## 12. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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## 13. Contents

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1. General description.....	1
2. Features and benefits .....	1
3. Applications .....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Limiting values .....	2
8. Limiting values .....	3
9. Thermal characteristics .....	5
10. Characteristics.....	6
11. Package outline .....	9
12. Legal information .....	11
13. Contents.....	13

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