

# MBT6429DW1T1G

## Amplifier Transistors

### NPN Silicon

#### Features

- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$	45	Vdc
Collector - Base Voltage	$V_{CBO}$	55	Vdc
Emitter - Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current - Continuous	$I_C$	200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) $T_A = 25^\circ\text{C}$	$P_D$	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

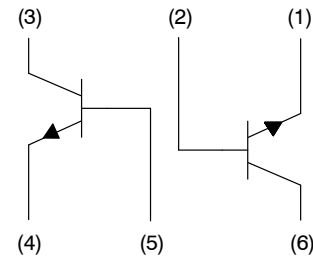
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended foot print.



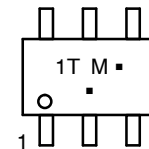
**ON Semiconductor®**

<http://onsemi.com>



**SC-88**  
**(SOT-363)**  
**CASE 419B**

#### MARKING DIAGRAM



1T = Specific Device Code  
M = Date Code  
■ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
MBT6429DW1T1G	SC-88 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MBT6429DW1T1G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector - Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	45	-	V <sub>dc</sub>
Collector - Base Breakdown Voltage (I <sub>C</sub> = 0.1 mA <sub>dc</sub> , I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	55	-	V <sub>dc</sub>
Collector Cutoff Current (V <sub>CE</sub> = 30 V <sub>dc</sub> )	I <sub>CES</sub>	-	0.1	μA <sub>dc</sub>
Collector Cutoff Current (V <sub>CB</sub> = 30 V <sub>dc</sub> , I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	0.01	μA <sub>dc</sub>
Emitter Cutoff Current (V <sub>EB</sub> = 5.0 V <sub>dc</sub> , I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	0.01	μA <sub>dc</sub>
<b>ON CHARACTERISTICS</b>				
DC Current Gain (I <sub>C</sub> = 0.01 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> ) (I <sub>C</sub> = 0.1 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> ) (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> ) (I <sub>C</sub> = 10 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> )	h <sub>FE</sub>	500 500 500 500	- 1250 - -	-
Collector - Emitter Saturation Voltage (I <sub>C</sub> = 10 mA <sub>dc</sub> , I <sub>B</sub> = 0.5 mA <sub>dc</sub> ) (I <sub>C</sub> = 100 mA <sub>dc</sub> , I <sub>B</sub> = 5.0 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>	- -	0.2 0.6	V <sub>dc</sub>
Base - Emitter On Voltage (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> )	V <sub>BE(on)</sub>	0.56	0.66	V <sub>dc</sub>
<b>SMALL-SIGNAL CHARACTERISTICS</b>				
Current - Gain - Bandwidth Product (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , V <sub>CE</sub> = 5.0 V <sub>dc</sub> , f = 100 MHz)	f <sub>T</sub>	100	700	MHz
Output Capacitance (V <sub>CB</sub> = 10 V <sub>dc</sub> , I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	3.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 V <sub>dc</sub> , I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	-	8.0	pF

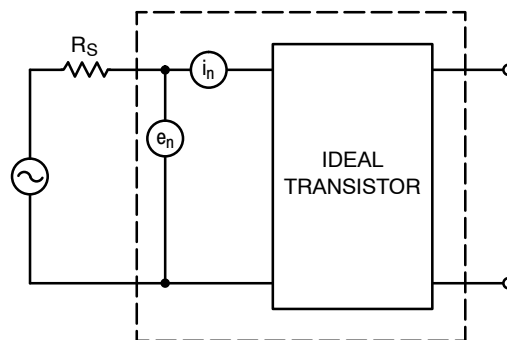


Figure 1. Transistor Noise Model

# MBT6429DW1T1G

## NOISE CHARACTERISTICS

( $V_{CE} = 5.0 \text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$ )

### NOISE VOLTAGE

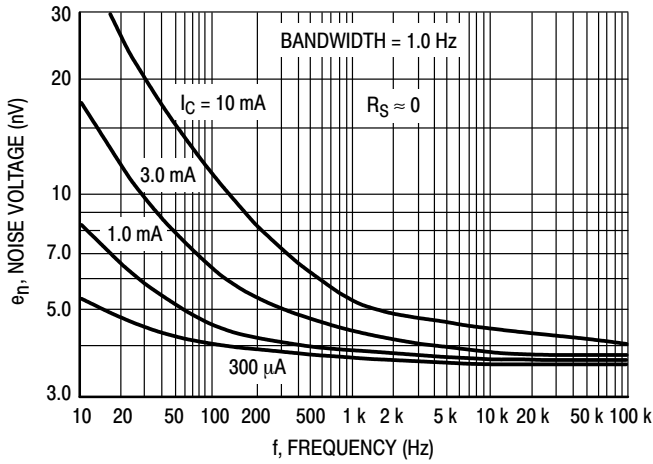


Figure 2. Effects of Frequency

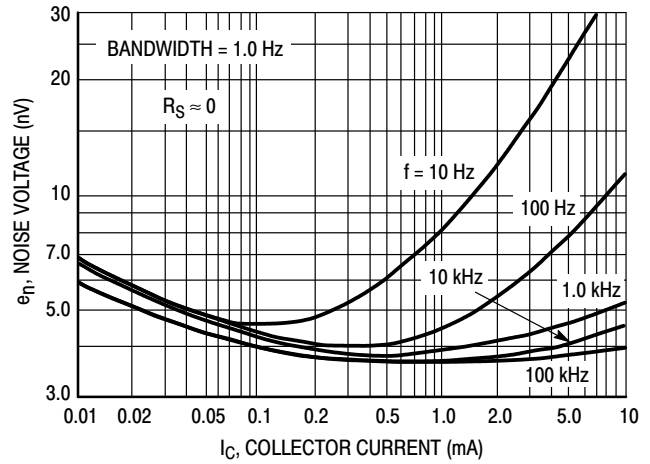


Figure 3. Effects of Collector Current

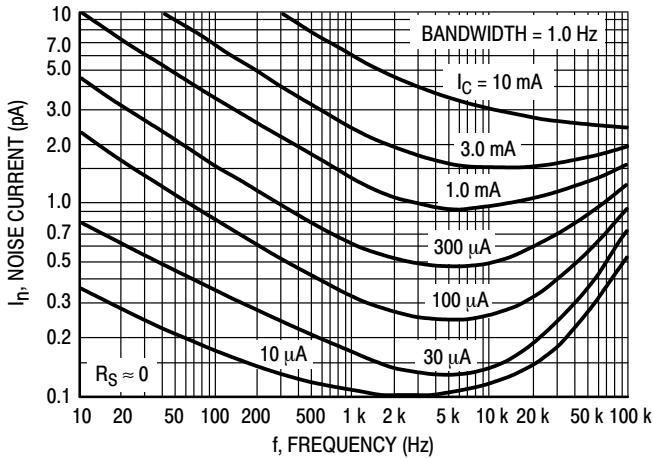


Figure 4. Noise Current

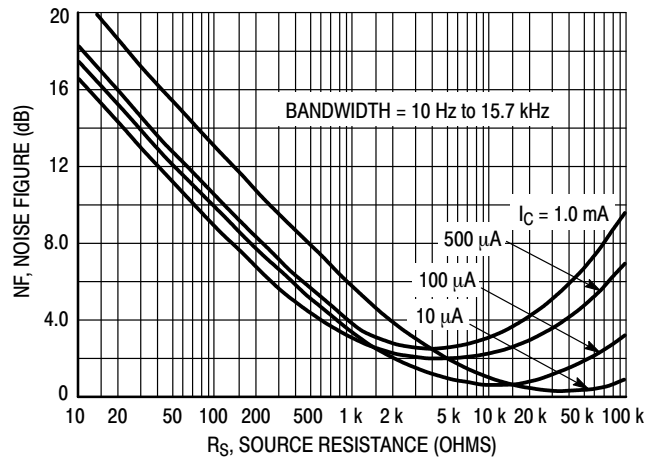


Figure 5. Wideband Noise Figure

### 100 Hz NOISE DATA

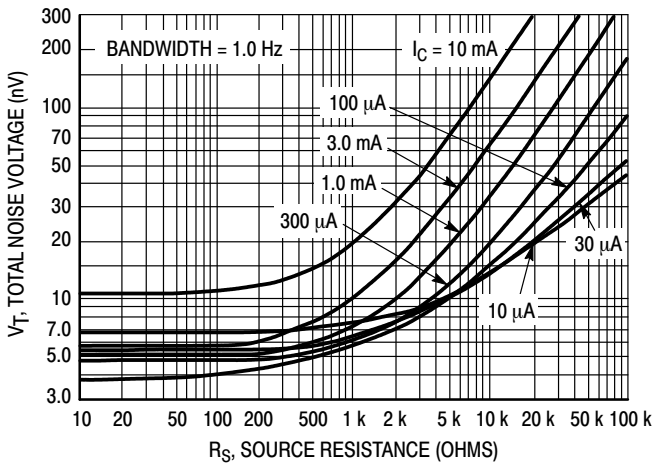


Figure 6. Total Noise Voltage

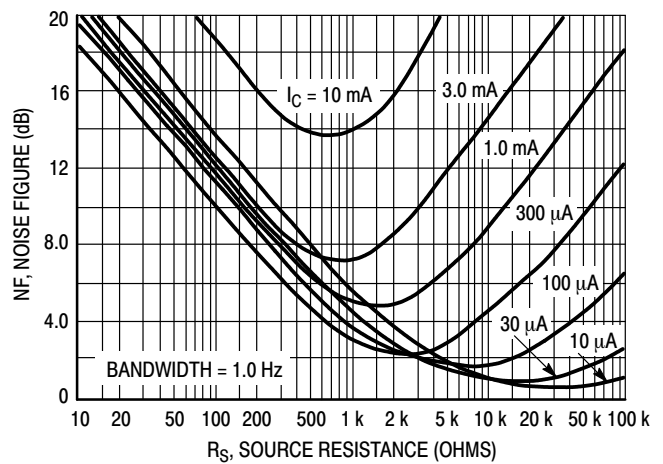


Figure 7. Noise Figure

# MBT6429DW1T1G

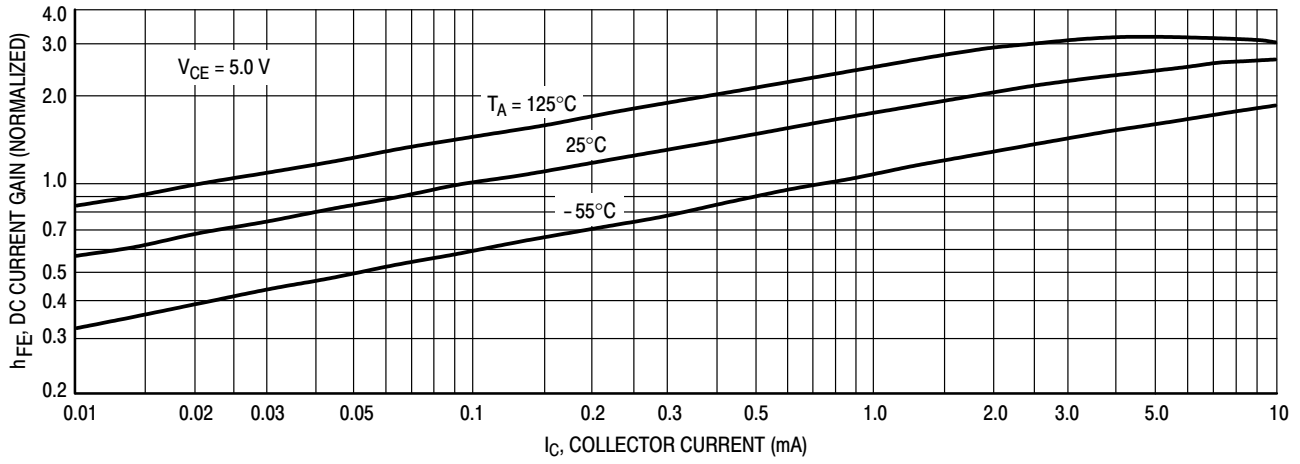


Figure 8. DC Current Gain

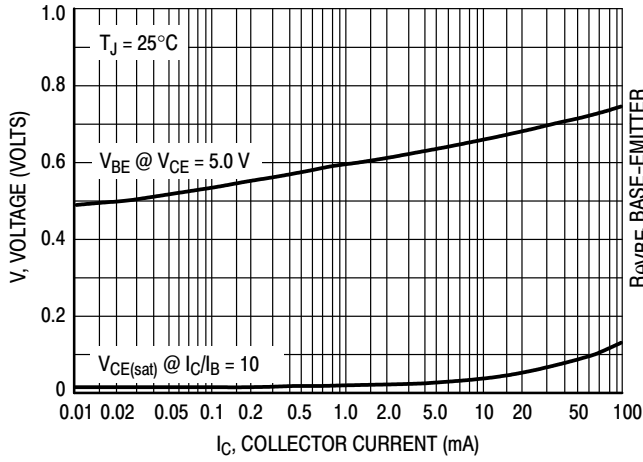


Figure 9. "On" Voltages

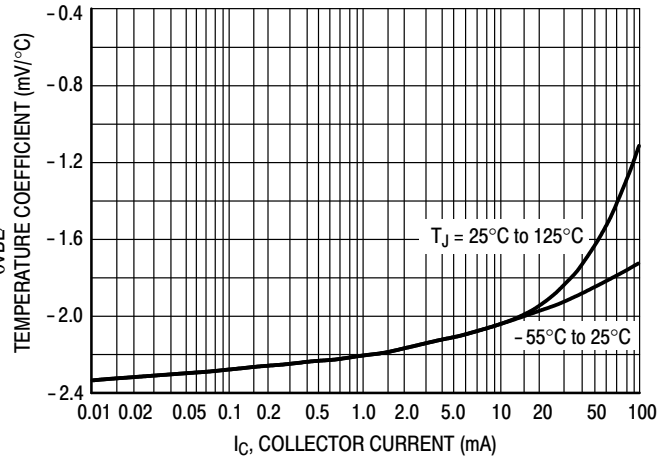


Figure 10. Temperature Coefficients

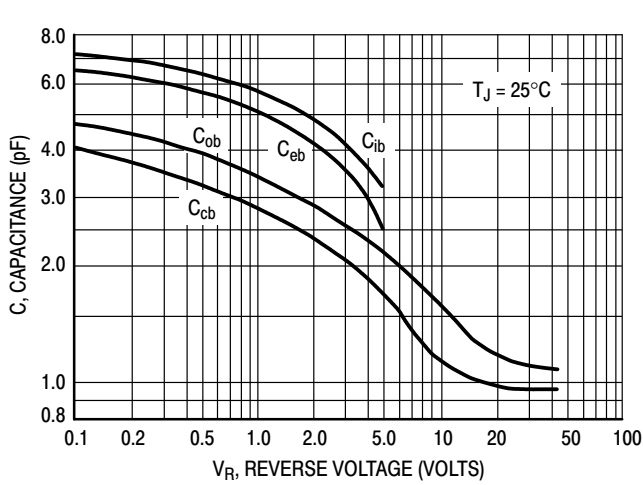


Figure 11. Capacitance

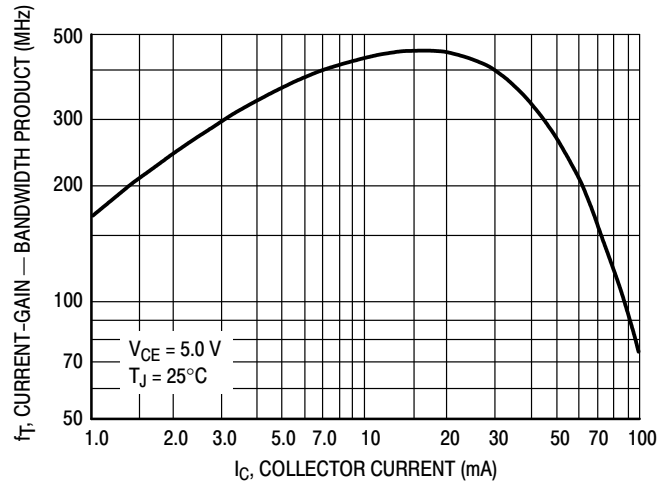


Figure 12. Current-Gain - Bandwidth Product

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

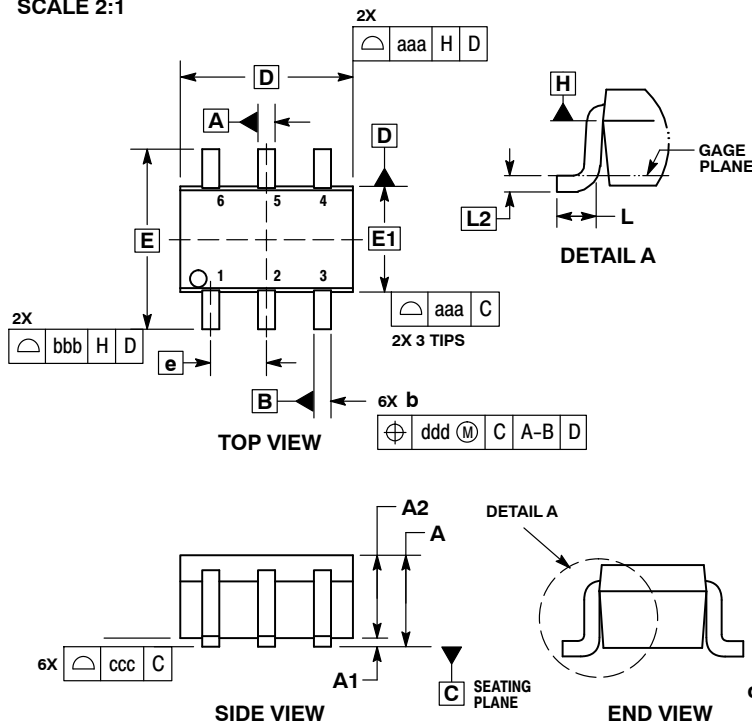
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1  
SCALE 2:1

SC-88/SC70-6/SOT-363  
CASE 419B-02  
ISSUE Y

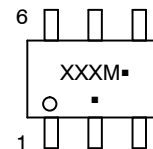
DATE 11 DEC 2012



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
  4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  5. DATUMS A AND B ARE DETERMINED AT DATUM H.
  6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
  7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	---	---	1.10	---	---	0.043
A1	0.00	---	0.10	0.000	---	0.004
A2	0.70	0.90	1.00	0.027	0.035	0.039
b	0.15	0.20	0.25	0.006	0.008	0.010
C	0.08	0.15	0.22	0.003	0.006	0.009
D	1.80	2.00	2.20	0.070	0.078	0.086
E	2.00	2.10	2.20	0.078	0.082	0.086
E1	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65 BSC			0.026 BSC		
L	0.26	0.36	0.46	0.010	0.014	0.018
L2	0.15 BSC			0.006 BSC		
aaa	0.15			0.006		
bbb	0.30			0.012		
ccc	0.10			0.004		
ddd	0.10			0.004		

### GENERIC MARKING DIAGRAM\*



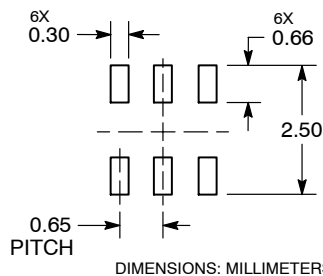
XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

### RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### STYLES ON PAGE 2

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**SC-88/SC70-6/SOT-363**  
**CASE 419B-02**  
**ISSUE Y**

DATE 11 DEC 2012

<b>STYLE 1:</b> PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	<b>STYLE 2:</b> CANCELLED	<b>STYLE 3:</b> CANCELLED	<b>STYLE 4:</b> PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	<b>STYLE 5:</b> PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	<b>STYLE 6:</b> PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
<b>STYLE 7:</b> PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	<b>STYLE 8:</b> CANCELLED	<b>STYLE 9:</b> PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	<b>STYLE 10:</b> PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	<b>STYLE 11:</b> PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	<b>STYLE 12:</b> PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
<b>STYLE 13:</b> PIN 1. ANODE 2. N/C 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	<b>STYLE 14:</b> PIN 1. VREF 2. GND 3. GND 4. IOUT 5. VEN 6. VCC	<b>STYLE 15:</b> PIN 1. ANODE 1 2. ANODE 2 3. ANODE 3 4. CATHODE 3 5. CATHODE 2 6. CATHODE 1	<b>STYLE 16:</b> PIN 1. BASE 1 2. EMITTER 2 3. COLLECTOR 2 4. BASE 2 5. EMITTER 1 6. COLLECTOR 1	<b>STYLE 17:</b> PIN 1. BASE 1 2. EMITTER 1 3. COLLECTOR 2 4. BASE 2 5. EMITTER 2 6. COLLECTOR 1	<b>STYLE 18:</b> PIN 1. VIN1 2. VCC 3. VOUT2 4. VIN2 5. GND 6. VOUT1
<b>STYLE 19:</b> PIN 1. IOUT 2. GND 3. GND 4. V CC 5. V EN 6. V REF	<b>STYLE 20:</b> PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR	<b>STYLE 21:</b> PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. N/C 6. CATHODE 1	<b>STYLE 22:</b> PIN 1. D1 (i) 2. GND 3. D2 (i) 4. D2 (c) 5. VBUS 6. D1 (c)	<b>STYLE 23:</b> PIN 1. Vn 2. CH1 3. Vp 4. N/C 5. CH2 6. N/C	<b>STYLE 24:</b> PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
<b>STYLE 25:</b> PIN 1. BASE 1 2. CATHODE 3. COLLECTOR 2 4. BASE 2 5. EMITTER 6. COLLECTOR 1	<b>STYLE 26:</b> PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	<b>STYLE 27:</b> PIN 1. BASE 2 2. BASE 1 3. COLLECTOR 1 4. EMITTER 1 5. EMITTER 2 6. COLLECTOR 2	<b>STYLE 28:</b> PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN	<b>STYLE 29:</b> PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE/ANODE 6. CATHODE	<b>STYLE 30:</b> PIN 1. SOURCE 1 2. DRAIN 2 3. DRAIN 2 4. SOURCE 2 5. GATE 1 6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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