



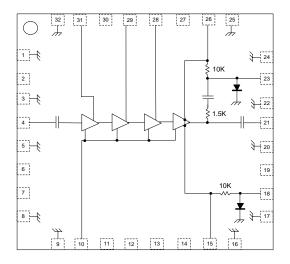
#### **Features:**

- Frequency Range: 21 27 GHz
- Psat: 34 dBm
- IM3 Level -40dBc @Po=20dBm/tone
- Gain: 25 dB
- Vdd =6 V
- Ids = 1500 to 2800 mA
- Input and Output Fully Matched to 50 Ω
- Integrated RF power detector

## **Applications:**

- Communication systems
- Microwave instrumentations
- Point to Point Radios

# **Description:**



#### Functional Block Diagram

The MMA-212734D-M5 is a GaAs MMIC linear power amplifier with 3-Watt output power and high gain of 25dB over full 21 to 27GHz frequency range. This amplifier was optimally designed for high linearity applications at 12dB back-off from P-1 condition.

## Absolute Maximum Ratings: (Ta= 25 °C)\*

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vds	Drain-Source Voltage	V		6.5
Vg	Gate-Source Voltage	V	-2.1	0
lg	First Gate Current	mA	-17	17
Pd	Power Dissipation	W		16.8
Pin max	RF Input Power	dBm		20
Tch	Channel Temperature	°C		+150
Tstg	Storage Temperature	°C		-55 to +150
Tmax	Max. Assembly Temp (20 sec max)	°C		+250

\*Operation of this device above any one of these parameters may cause permanent damage.

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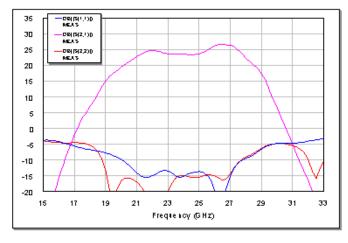
## Electrical Specifications: Vds=6V, Ids=2000mA, Ta=25 °C Z0=50 ohm

Parameter	Units	Typical Data
Frequency Range	GHz	21-27
Gain (Typ/Min)	dB	25 / 24
Gain Flatness (Typ/Max)	+/-dB	1 / 1.5
Input RL(Typ/Max)	dB	15/12
Output RL (Typ/Max)	dB	15/12
Output P1dB (Typ/Min)	dBm	33/32
IM3 Level @Po=20dBm/tone (1)	dBc	-40
Output Psat (Typ/Min)	dBm	34/33
Operating Current at P1dB (Typ/Max)	mA	2000 / 2500
Thermal Resistance	°C/W	3.8
Operating Temperature Range	°C	-40 to +85

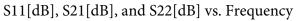
(1) Output IM3 is measured with two tones at output power of 20 dBm/tone separated by 20 MHz.

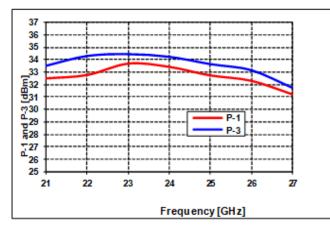




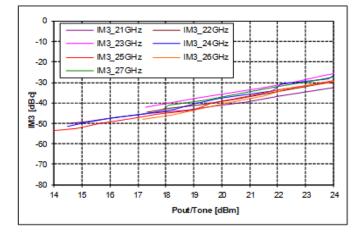


#### Typical RF Performance: Vds=6V, Ids=2000mA, Z0=50 ohm, Ta=25 °C

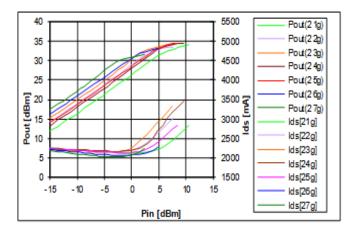




P-1 and Psat vs. Frequency



IM3 Level vs. single tone output power [dBm/tone]

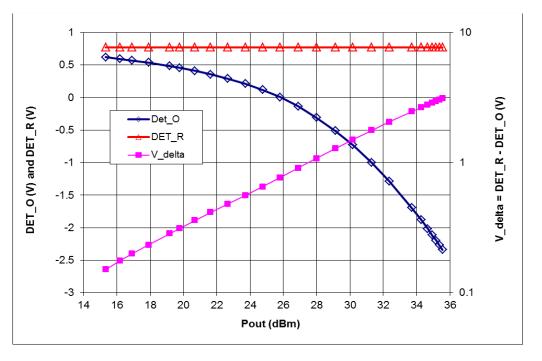


Pout [dBm], and Ids[mA] vs. Input power [dBm]



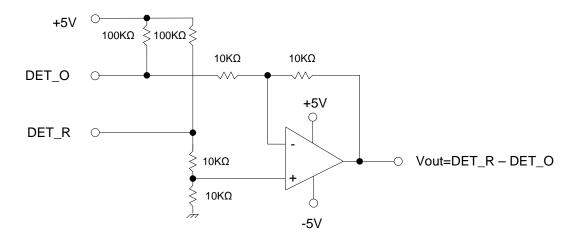


## Typical Power Detector Voltages: Vds=6V, Idsq=2A, Frequency=22GHz





#### Vdelta axis is Log-scale.







### **Applications**

The **MMA-212734D-M5** MMIC power amplifier is designed for use as a power stage amplifier in microwave transmitters. It is ideally suited for 21 to 27GHz band point to point radio applications requiring a flat gain response and excellent linearity performance. This amplifier is provided as a 5x5mm QFN package, and the packaged amplifier is fully compatible with industry standard high volume surface mount PCB assembly processes.

### **Biasing and Operation**

The recommended bias conditions for best performance for the **MMA-212734D-M5** are VDD = 6.0V, Idsq = 2000mA. Performance improvements are possible depending on applications. The drain bias voltage range is 5 to 6V and the quiescent drain current biasing range is 1500mA to 2800mA. A single DC gate supply connected to Vg will bias all the amplifier stages. Muting can be accomplished by setting Vg to the pinch-off voltage (Vp=-2V). The gate voltage (Vg) should be applied prior to the drain voltages (Vd1, Vd2, Vd3, Vd4) during power up and removed after the drain voltages during power down. The RF input and output ports are DC decoupled internally. Typical DC supply connection with bi-passing capacitors for the **MMA-212734D-M5** is shown in following pages.

**MMA-212734D-M5** contains optional temperature compensated output power detectors. Typical detection voltage vs. output power is shown in previous page. To obtain over temperature compensation, a recommended differential amplifier is required.

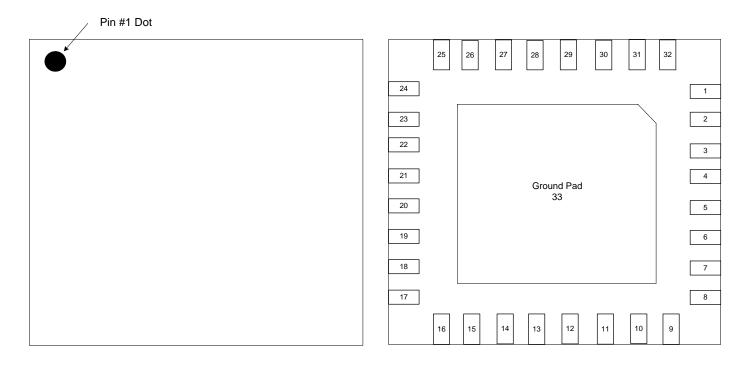
### **Assembly Techniques**

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly. MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.



MMA-212734D-M5 21-27GHz, 3W Power Amplifier Data Sheet

# Package Pin-out:

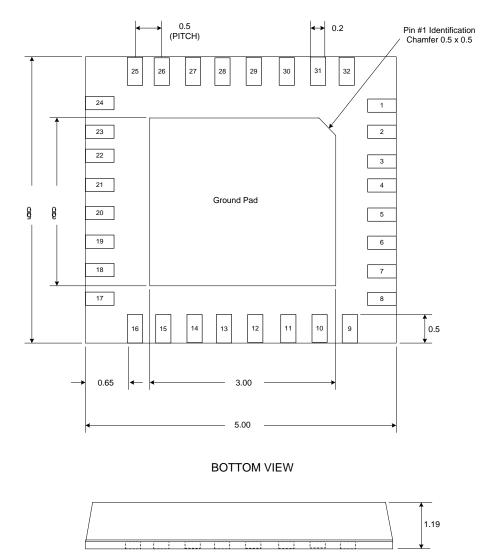


Pin	Description
4	RF Input
21	RF Output
10	Vg
31	Vd1
29	Vd2
28	Vd3
15, 26	Vd4
18	DET_Reference
23	DET_Output
1, 3, 5, 8 ,9, 16, 17, 20, 22,	Ground
24, 25, 32, 33	
2, 6, 7, 11, 12, 13, 14, 19,	N/C
27, 30	





# **Mechanical Information:**



SIDE VIEW

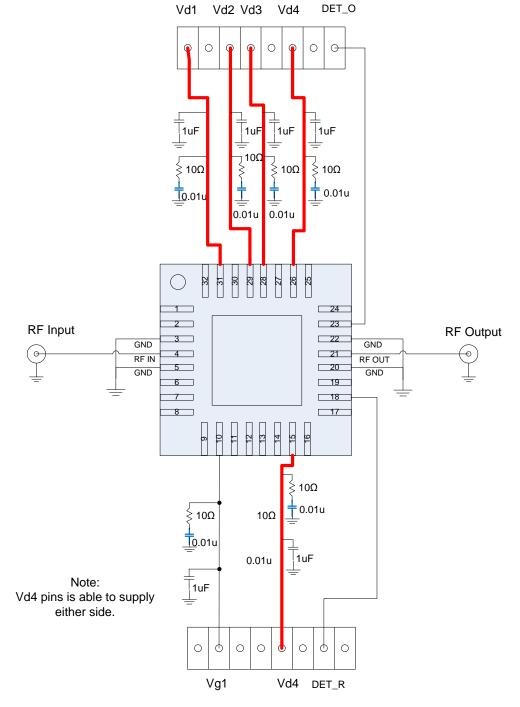
The units are in [mm].

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## **Application Circuit:**



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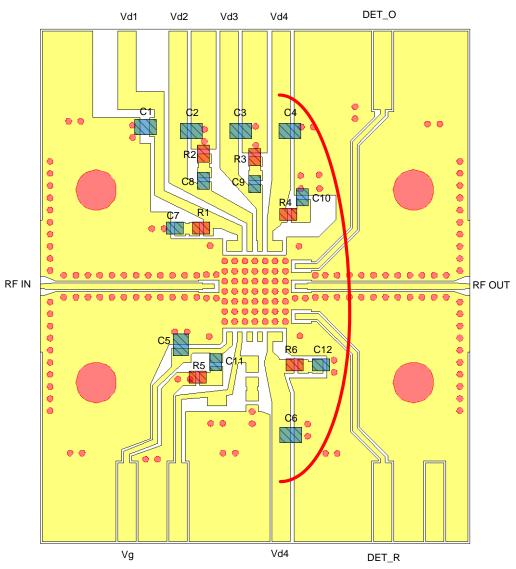
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## **Recommended Application Board Design:**

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. Board is soldered on a gold plated solid cupper block and adequate heat-sinking is required for 16.8W total power dissipation.



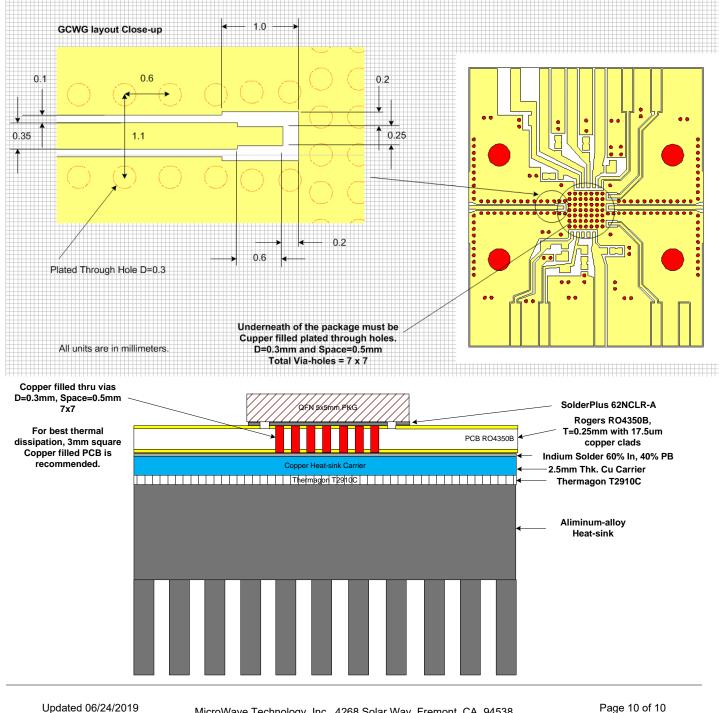
Part	Description
C1, C2, C3, C4, C5, C6	1uF capacitor (0603)
C7, C8, C9, C10, C11, C12	0.01uF Capacitor (0402)
R1, R2, R3, R4, R5, R6	10Ω Resistor (0402)





## **Recommended Application Board Design:**

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. The board material and mounting pattern, as defined in the data sheet, optimizes RF performance and is strongly recommended. An electronic drawing of the land pattern is available upon request from MwT Sales & Application Engineering.



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