Vishay Semiconductors

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PRIMARY CHARACTERISTICS								
I _{F(AV)}	15 A							
V _R	300 V							
V _F at I _F	0.85 V							
t _{rr} (typ.)	40 ns							
T _J max.	175 °C							
Package	D ² PAK (TO-263AB)							
Circuit configuration	Single							

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Meets JESD 201 class 1A whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Vishay Semiconductors 300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Repetitive peak reverse voltage	V _{RRM}		300	V					
Average rectified forward current	I _{F(AV)}	T _C = 142 °C	15	٨					
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	140	А					
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C					

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	300	-	-					
Forward voltage	V _F	I _F = 15 A	-	1.05	1.25	V				
Forward voltage		I _F = 15 A, T _J = 125 °C	-	0.85	1.00					
Reverse leakage current		$V_R = V_R$ rated	-	0.05	40					
neverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	12	400	μΑ				
Junction capacitance	CT	V _R = 300 V	-	45	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH				

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HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, \text{ dI}_F/\text{dt}$	$= 50 \text{ A/}\mu\text{s}, \text{V}_{\text{R}} = 30 \text{ V}$	-	-	40				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	32	-	ns			
		T _J = 125 °C		-	45	-				
Poole recovery ourrent	I _{RRM}	T _J = 25 °C	$I_F = 15 A$	-	2.4	-	A			
Peak recovery current		T _J = 125 °C	dl _F /dt = -200 A/µs V _B = 200 V	-	6.1	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	38	-	20			
neverse recovery charge	Qrr	T _J = 125 °C		-	137	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C				
Thermal resistance, junction to case per leg	R _{thJC}		-	1.02	2.0					
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W				
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.2	-	0,11				
Weight			-	2.0	-	g				
Weight			-	0.07	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style D ² PAK (TO-263AB)	15ETH03SH							

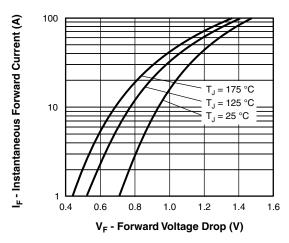


Fig. 1 - Typical Forward Voltage Drop Characteristics

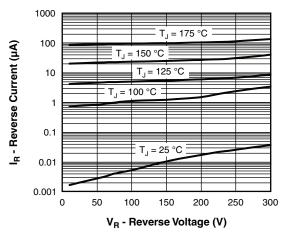


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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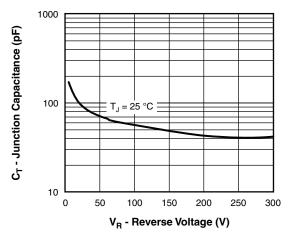


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

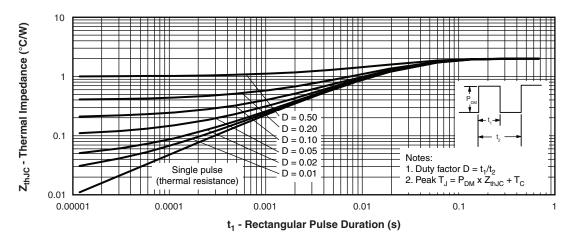


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

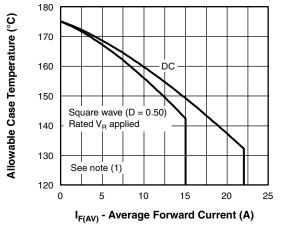


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

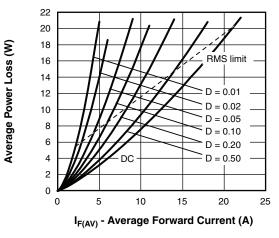


Fig. 6 - Forward Power Loss Characteristics

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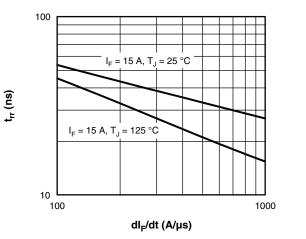
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VS-15ETH03SHM3

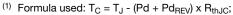
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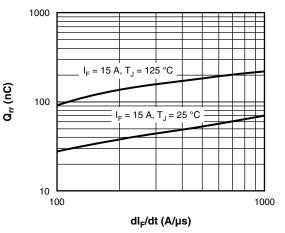
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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

Note



Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R





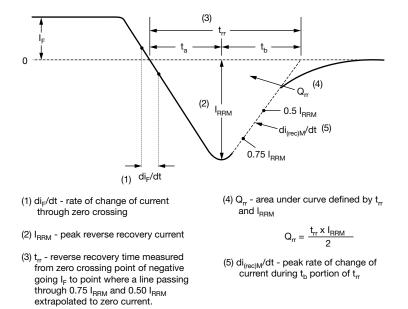


Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	15	Е	т	н	03	S	TRL	н	М3	
		2	3	4	5	6	7	8	9	10	
	1 - Vishay Semiconductors product										
	2 - Current rating (15 A)										
	3 - E = single diode										
	4 -	- $T = D^2 PAK (TO-263AB)$									
	5 -	H =	hyperfa	ist rectifi	ier						
	6 -	Volt	age rati	ng (03 =	= 300 V)						
	7.	S =	D ² PAK								
	8 -	• No	one = tu	be (50 p	pieces)						
		• TF	 TRL = tape and reel (left oriented, for D²PAK package) 								
		• TI	 TRR = tape and reel (right oriented, for D²PAK package) 								
	9 - H = AEC-Q101 qualified										
	10 ·			ntal digit							
		М3	= halog	en-free,	RoHS-	complia	nt, and	termina	tions lea	ad (Pb)-	

ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-15ETH03SHM3	50	1000	Antistatic plastic tube						
VS-15ETH03STRRHM3	800	800	13" diameter reel						
VS-15ETH03STRLHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95046							
Part marking information	www.vishay.com/doc?95444							
Packaging information	www.vishay.com/doc?95032							
SPICE model	www.vishay.com/doc?96567							

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INC	INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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