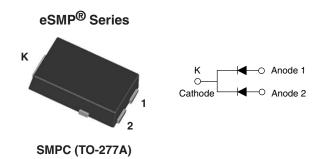
Vishay Semiconductors

Hyperfast Rectifier, 2 x 3 A FRED Pt®



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 3 A				
V _R	100 V				
V _F at I _F	0.75 V				
t _{rr (typ.)}	27 ns				
T _J max.	175 °C				
Package	SMPC (TO-277A)				
Circuit configuration	Common cathode				

FEATURES

- Hyperfast recovery time, reduced $\mathsf{Q}_{\mathrm{rr}},$ and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V _{RRM}		100	V	
Average reatified forward ourrant	per device	1	T 165 °C	6		
Average rectified forward current per d	per diode	I _{F(AV)}	T _{Sp} = 165 °C	3	٨	
Non-repetitive peak surge current	per device		$T = 25 ^{\circ}C$ 6 ma aquara pulsa	150	A	
per di		I _{FSM}	$T_J = 25 \ ^{\circ}C$, 6 ms square pulse	80		
Operating junction and storage terr	nperatures	T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	100	-	-	
Forward voltage, per diode	VF	I _F = 3 A	-	0.87	0.94	V
	۷F	I _F = 3 A, T _J = 125 °C	-	0.75	0.79	
Powere lookage ourrent per diade		$V_{R} = V_{R}$ rated	-	-	2	
Reverse leakage current, per diode	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	0.7	10	μA
Junction capacitance	CT	V _R = 100 V	-	13	-	pF

Revision: 13-Jan-2021

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Document Number: 95701

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HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CC	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t =$	$= 50 \text{ A/}\mu\text{s}, \text{V}_{\text{R}} = 30 \text{ V}$	-	27	-	
Poverse receiver / time	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	20	-	ns
		T _J = 125 °C		-	26	-	
Deals recovery ourrent	I _{RRM}	T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/µs V _R = 160 V	-	2.4	-	А
Peak recovery current		T _J = 125 °C		-	3.8	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	23	-	nC
		T _J = 125 °C		-	50	_	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to mount, per diode	R _{thJM}		-	2.8	4	°C/W	
Annualizate consiste			0.1		g		
Approximate weight				0.0035		oz.	
Marking device		Case style SMPC (TO-277A)		NC	H1		

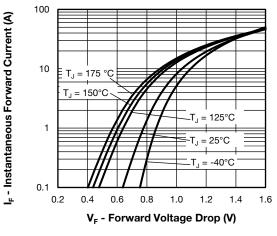


Fig. 1 - Typical Forward Voltage Drop Characteristics

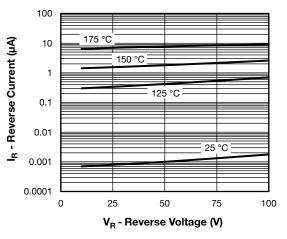
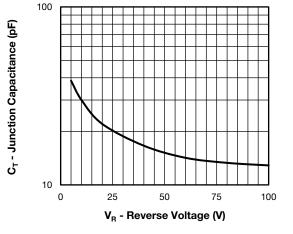


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

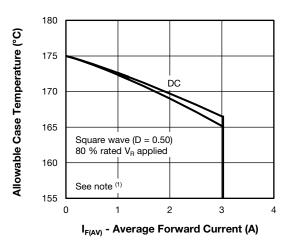


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

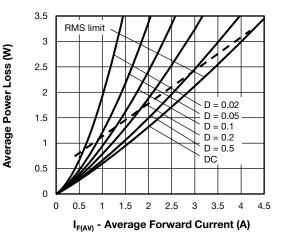


Fig. 5 - Forward Power Loss Characteristics

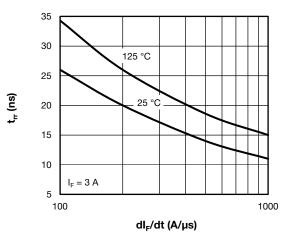


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

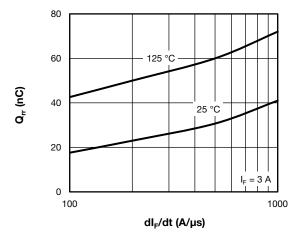


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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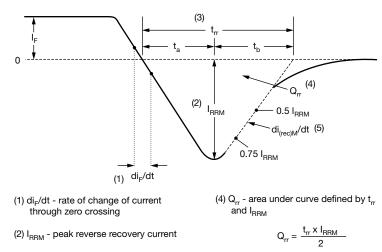
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VS-6CSH01HM3

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(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

SHAY

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Device code	VS-	6	С	S	н	01	н	M3
	1	2	3	4	5	6	7	8
	1	- Visl	nay Sen	niconduc	ctors pro	oduct		
	2	- Cur	rent rati	ng (6 = 0	6 A)			
	3	- Circ	cuit conf	iguratior	ר:			
		C =	commo	on catho	de			
	4	- S=	SMPC	package)			
	5	- Pro	cess typ	be,				
		H =	hyper f	ast reco	very			
	6	- Vol	age coo	de (01 =	100 V)			
	7	- H=	AEC-Q	101 qua	lified			
	8	- M3	= halog	en-free,	RoHS-	complia	nt, and	termina

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6CSH01HM3/86A	1500	1500	7" diameter plastic tape and reel				
VS-6CSH01HM3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95570				
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96378				

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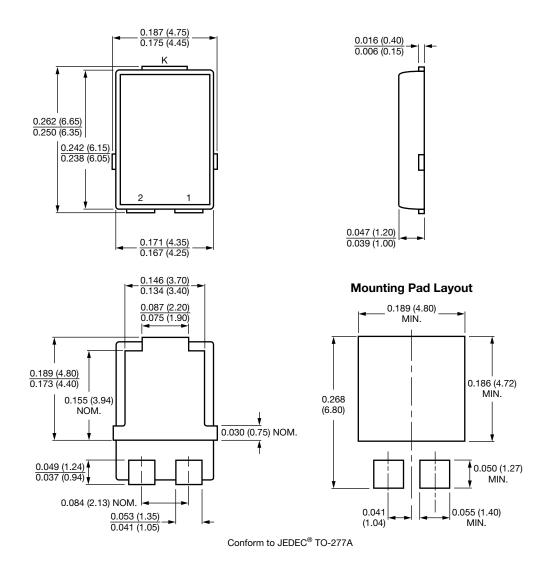
Outline Dimensions





SMPC (TO-277A)

DIMENSIONS in inches (millimeters)





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