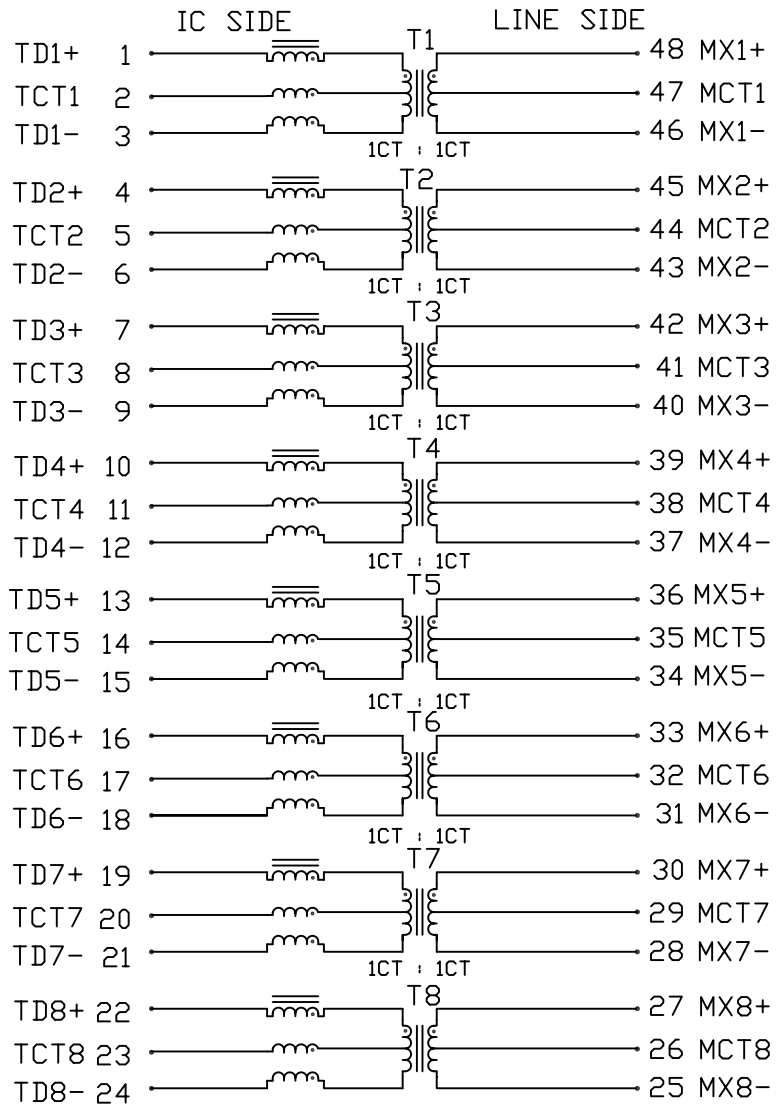


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SCHEMATIC

ELECTRICAL SPECIFICATIONS @25°C



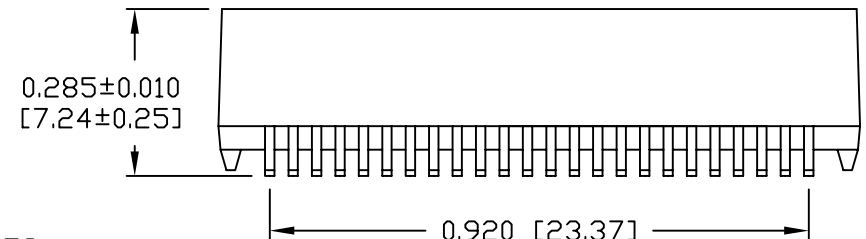
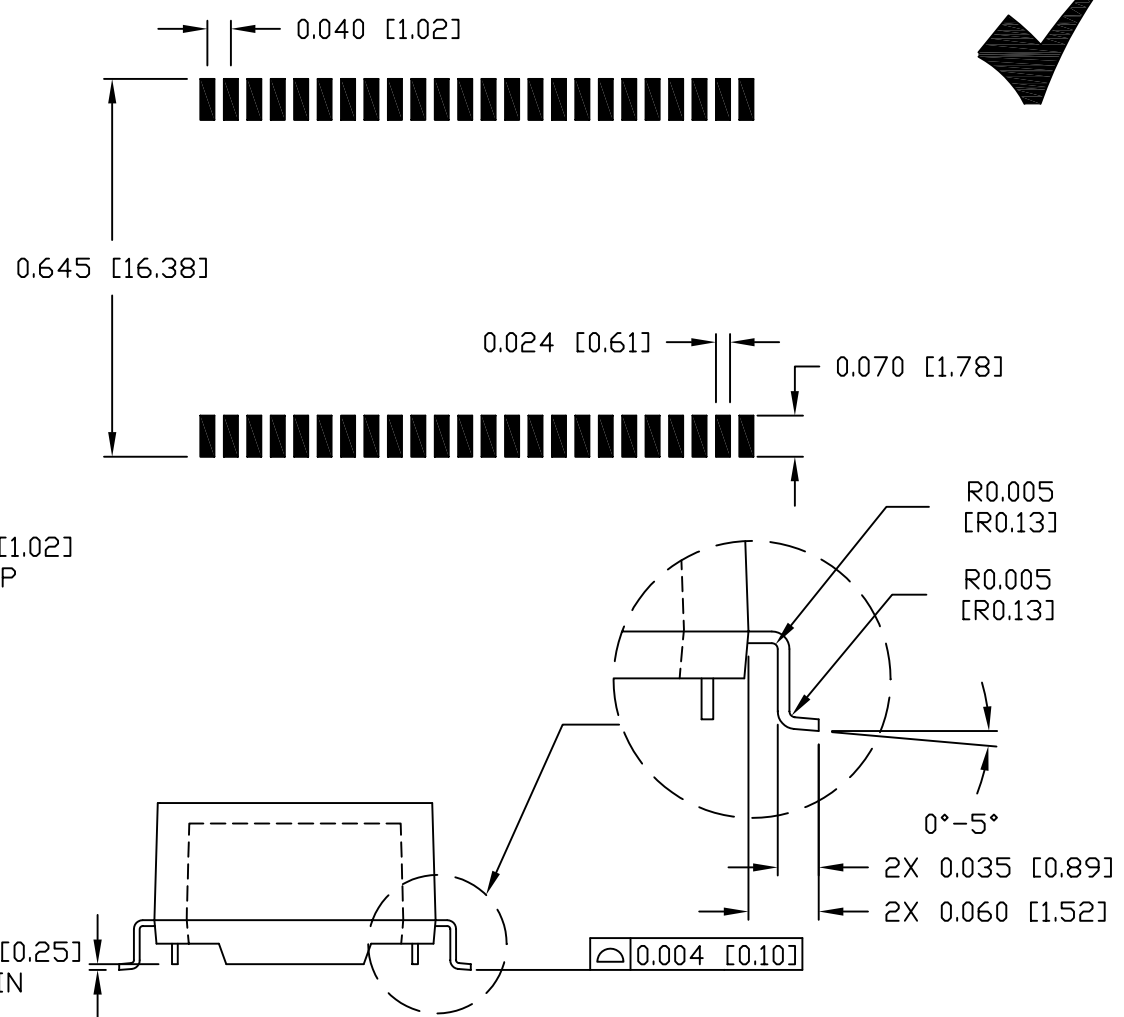
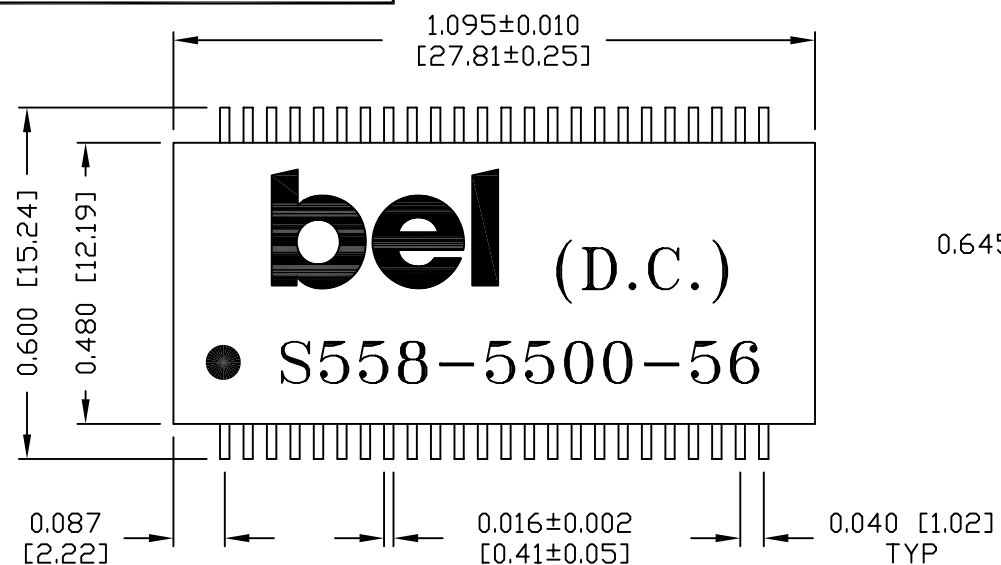
TURNS RATIO IC : LINE POLARITY OCL @100kHz, 100mVrms 8mA DC BIAS INSERTION LOSS 0.3MHz TO 100MHz 100MHz TO 125MHz RETURN LOSS 0.5MHz-40MHz 40MHz-100MHz DC RESISTANCE DCR (START-FINISH) DCR 1/2 WDG DCR IMBALANCE CROSSTALK 300kHz - 100MHz CM TO CM REJ 300kHz - 100MHz CM TO DM REJ 300kHz - 100MHz HIPOT OPERATING TEMPERATURE	1CT : 1CT PER DOT CONVENTION 350µH MIN. 0.8dB MAX 1.2dB MAX 18dB MIN 12-20 LOG (F/80MHz) dB MIN 1.15ΩHMS TYP 1.0ΩHMS MAX ±0.065ΩHMS MAX 33dB MIN 30dB MIN 35dB MIN 1500Vrms 0°C TO +70°C
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ORIGINATED BY DATE Alice Pang 2015-01-26		TITLE ELECTRICAL SPECIFICATION	PART NO. / DRAWING NO. S558-5500-56		STANDARD DIM. [] METRIC DIM. AS REF. TOL. IN INCH		REV. : B PAGE : 2
DRAWN BY DATE ZC Guo 2015-01-26			FILE NAME S558550056B.DWG		UNIT : INCH [mm] SCALE : N/A		
					SIZE : A4		

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SUGGESTED PCB PAD LAYOUT



- NOTES:
1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
 2. PACKAGE CODE: "QVS001".

ORIGINATED BY		DATE	TITLE	PART NO. / DRAWING NO.		STANDARD DIM. [] METRIC DIM. AS REF.		REV. : B	PAGE : 3
Lawrence Tsang		2015-01-26		MECHANICAL OUTLINE	S558-5500-56		TOL. IN INCH		UNIT : INCH [mm]
DRAWN BY		DATE	FILE NAME		.X		SCALE : N/A		
ZC Guo		2015-01-26	S558550056B.DWG		.XX ±0.01		SIZE : A4		
					.XXX ±0.005				 POWER PROTECT CONNECT