

To our customers,

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Phase-out/Discontinued

L, S-BAND SPDT SWITCH

DESCRIPTION

The μPG2151TK is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which was developed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 5.3 V. This device can operate frequency from 0.5 to 3.0 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin lead-less minimold (1511) package. And this package is able to high-density surface mounting.

FEATURES

- Switch control voltage : $V_{cont(H)} = 1.8$ to 5.3 V (2.6 V TYP.)
: $V_{cont(L)} = -0.2$ to $+0.2$ V (0 V TYP.)
- Low insertion loss : $L_{INS1} = 0.30$ dB TYP. @ $f = 0.5$ to 1.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
: $L_{INS2} = 0.35$ dB TYP. @ $f = 1.0$ to 2.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
: $L_{INS3} = 0.50$ dB TYP. @ $f = 2.0$ to 3.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
- High isolation : $ISL1 = 25$ dB TYP. @ $f = 0.5$ to 1.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
: $ISL2 = 18$ dB TYP. @ $f = 1.0$ to 2.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
: $ISL3 = 13$ dB TYP. @ $f = 2.0$ to 3.0 GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
- Handling power : $P_{in(0.1\text{ dB})} = +21.0$ dBm TYP. @ $f = 2.0$ GHz, $V_{cont(H)} = 2.6$ V, $V_{cont(L)} = 0$ V
- High-density surface mounting : 6-pin lead-less minimold package ($1.5 \times 1.1 \times 0.55$ mm)

APPLICATIONS

- L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth™ etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2151TK-E2	μPG2151TK-E2-A	6-pin lead-less minimold (1511) (Pb-Free) ^{Note}	G4N	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 1, 6 face the perforation side of the tape • Qty 5 kpcs/reel

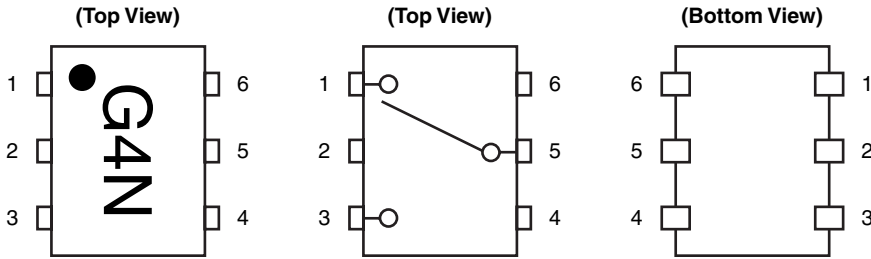
Note With regards to terminal solder (the solder contains lead) plated products (conventionally plated), contact your nearby sales office.

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: μPG2151TK

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	OUTPUT1
2	GND
3	OUTPUT2
4	V _{cont2}
5	INPUT
6	V _{cont1}

TRUTH TABLE

V _{cont1}	V _{cont2}	INPUT-OUTPUT1	INPUT-OUTPUT2
Low	High	OFF	ON
High	Low	ON	OFF

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V _{cont}	+6.0 ^{Note}	V
Input Power	P _{in}	+26	dBm
Operating Ambient Temperature	T _A	-45 to +85	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note |V_{cont1} - V_{cont2}| ≤ 6.0 V

RECOMMENDED OPERATING RANGE (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	V _{cont (H)}	1.8	2.6	5.3	V
Switch Control Voltage (L)	V _{cont (L)}	-0.2	0	+0.2	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{cont} (H) = 2.6 V, V_{cont} (L) = 0 V, DC cut capacitors = 56 pF, unless otherwise specified)

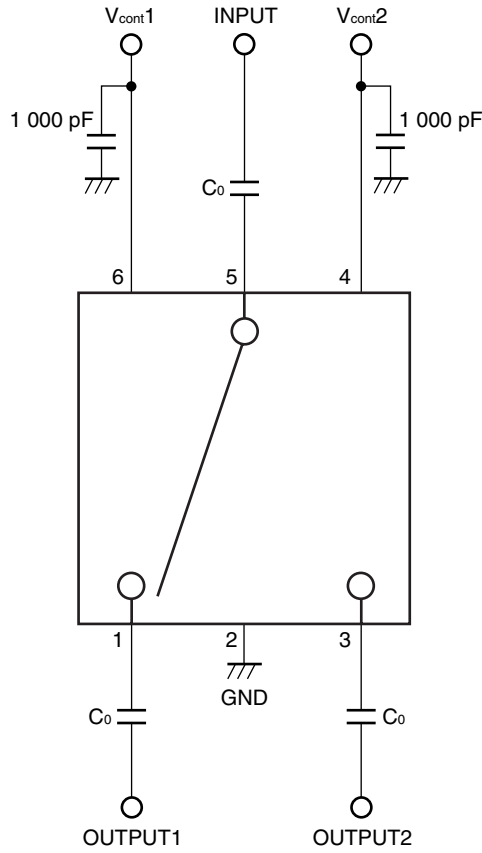
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	L _{INS1}	f = 0.5 to 1.0 GHz	–	0.30	0.42	dB
Insertion Loss 2	L _{INS2}	f = 1.0 to 2.0 GHz	–	0.35	0.47	
Insertion Loss 3	L _{INS3}	f = 2.0 to 3.0 GHz	–	0.50	0.65	
Isolation 1	ISL1	f = 0.5 to 1.0 GHz	22	25	–	dB
Isolation 2	ISL2	f = 1.0 to 2.0 GHz	15	18	–	
Isolation 3	ISL3	f = 2.0 to 3.0 GHz	10	13	–	
Input Return Loss	RL _{in}	f = 0.5 to 3.0 GHz	–	20	–	dB
Output Return Loss	RL _{out}	f = 0.5 to 3.0 GHz	–	20	–	dB
0.1 dB Loss Compression Input Power ^{Note}	P _{in (0.1 dB)}	f = 2.0 GHz	+18.0	+21.0	–	dBm
Switch Control Current	I _{cont}		–	0.2	20	μA
Switch Control Speed	t _{sw}	50% CTL to 90/10% RF	–	1.0	–	μs

Note P_{in (0.1 dB)} is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

Caution This device is used it is necessary to use DC cut capacitors.

The value of DC cut capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC cut capacitor value is less than 100 pF.

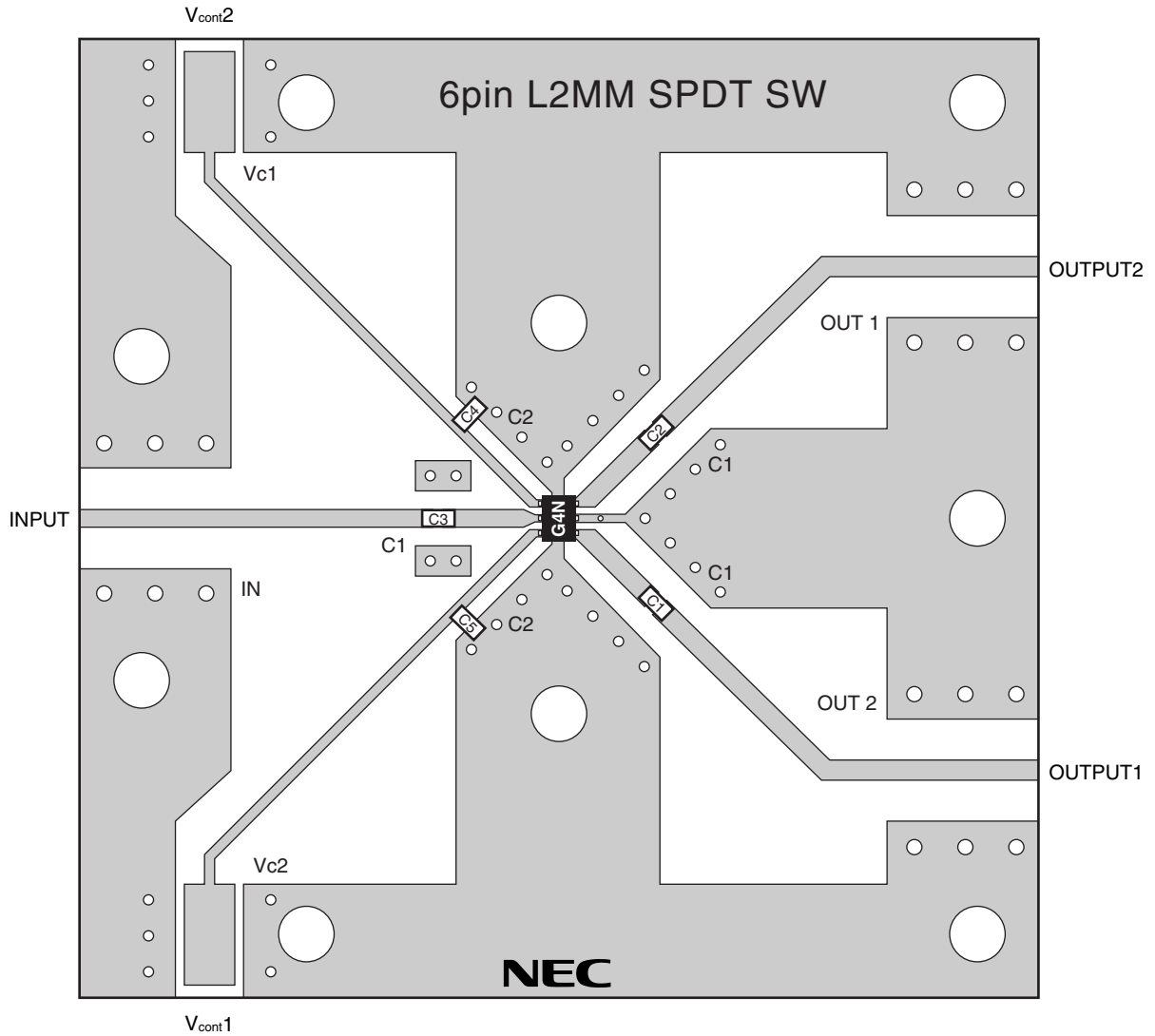
EVALUATION CIRCUIT



Remark C₀ : 56 pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

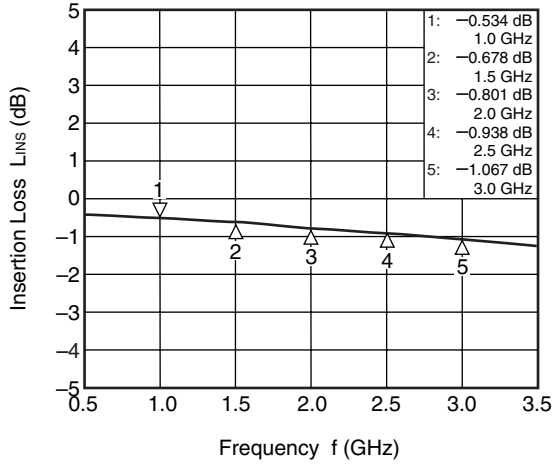


USING THE NEC EVALUATION BOARD

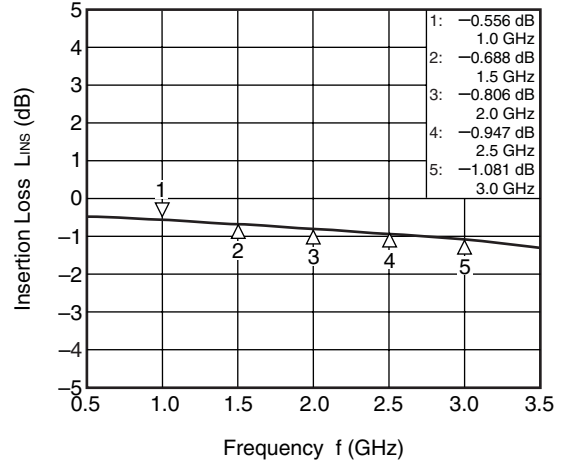
Symbol	Values
C1, C2, C3	56 pF
C4, C5	1 000 pF

TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, $V_{\text{cont}}(\text{H}) = 2.6\text{ V}$, $V_{\text{cont}}(\text{L}) = 0\text{ V}$, DC cut capacitors = 56 pF, using test fixture, unless otherwise specified)

INPUT-OUTPUT1
INSERTION LOSS vs. FREQUENCY



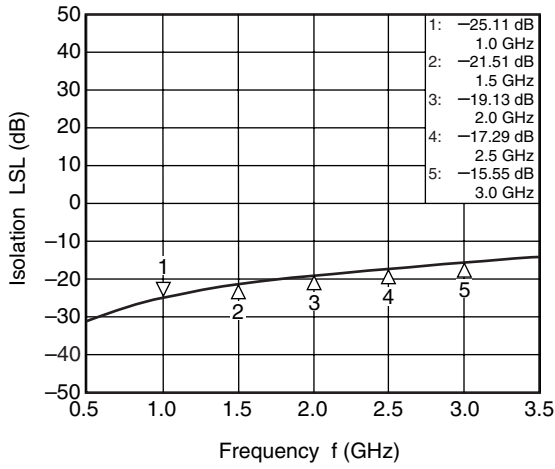
INPUT-OUTPUT2
INSERTION LOSS vs. FREQUENCY



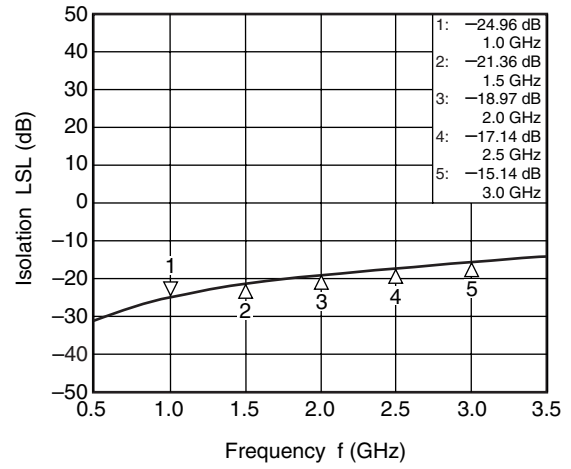
Remark The graphs indicate nominal characteristics.

Caution These characteristics values include the losses of NEC evaluation board.

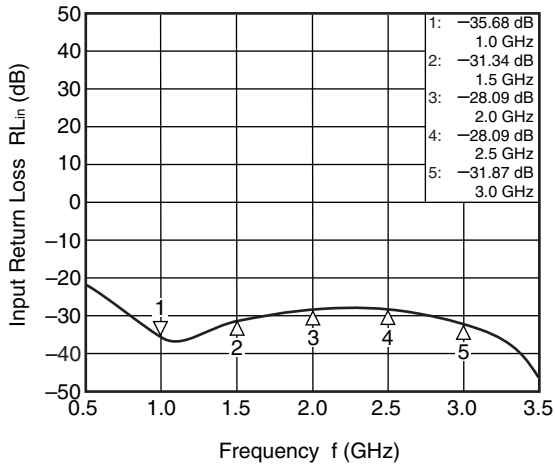
INPUT-OUTPUT1
ISORATION vs. FREQUENCY



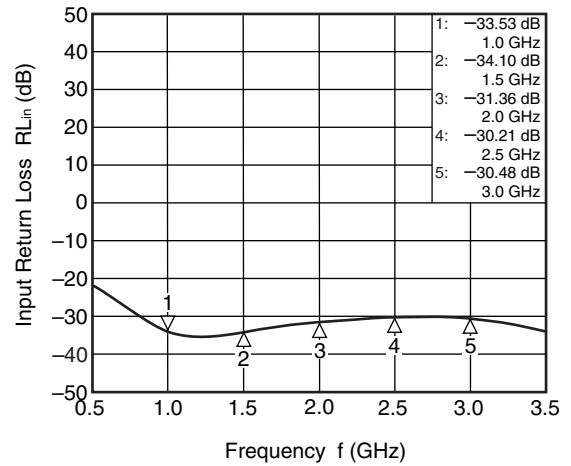
INPUT-OUTPUT2
ISORATION vs. FREQUENCY



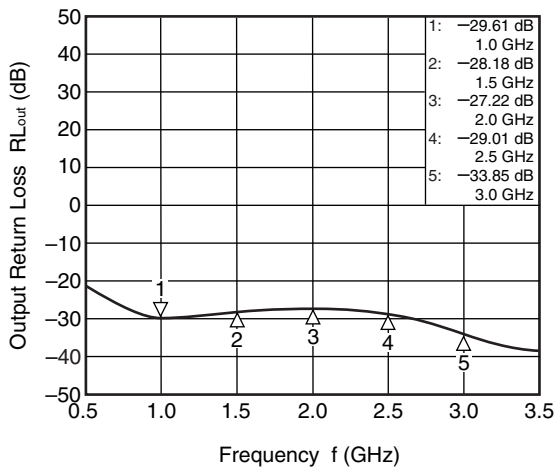
INPUT-OUTPUT1
INPUT RETURN LOSS vs. FREQUENCY



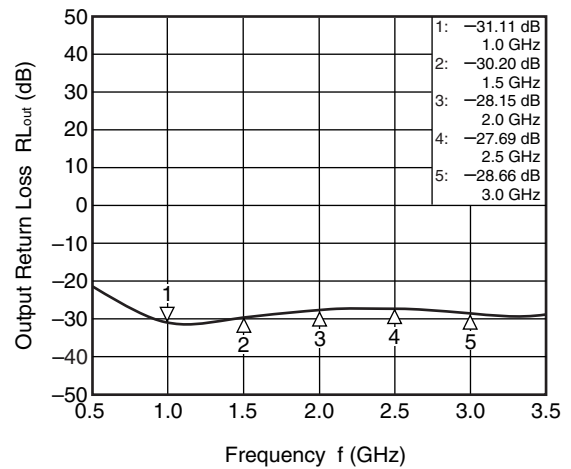
INPUT-OUTPUT2
INPUT RETURN LOSS vs. FREQUENCY



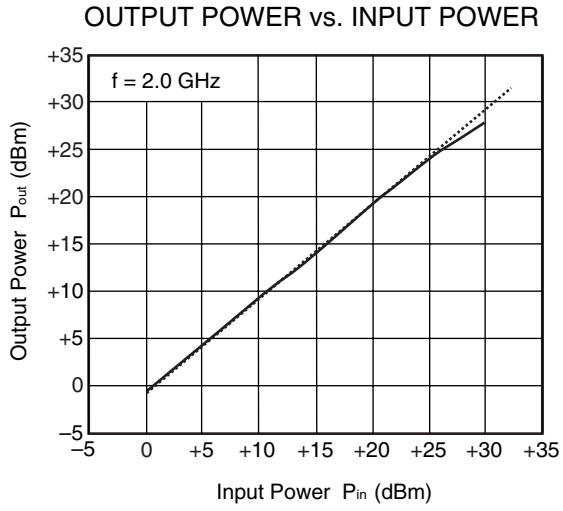
INPUT-OUTPUT1
OUTPUT RETURN LOSS vs. FREQUENCY



INPUT-OUTPUT2
OUTPUT RETURN LOSS vs. FREQUENCY



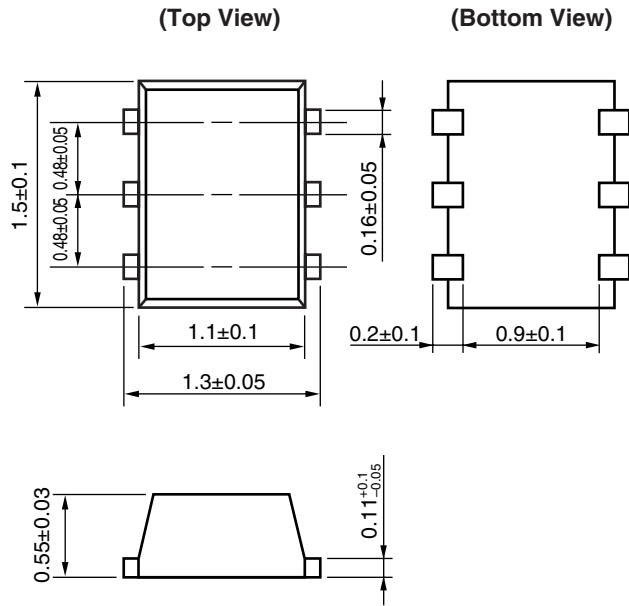
Remark The graphs indicate nominal characteristics.



Remark The graph indicates nominal characteristics.

PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511) (UNIT: mm)



RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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M8E 02.11-1

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