

Ultrafast Rectifier, 5 A FRED Pt®



SMC (DO-214AB)

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	5 A			
V_{R}	600 V			
V _F at I _F	0.92 V			
t _{rr} typ.	45 ns			
T _J max.	175 °C			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

FEATURES

Ultrafast recovery time, reduced Q_{rr} and soft recovery



• 175 °C maximum operating junction temperature

• For PFC CRM/CCM, snubber operation

HALOGEN FREE

Low forward voltage drop

· Low leakage current

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMC (DO-214AB)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _L = 99 °C ⁽¹⁾	5	۸	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 10 ms sine pulse	130	A	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

Note

(1) Mounted on PCB with minimum pad size

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Famuard voltage	V	I _F = 5 A	-	1.1	1.35	V
Forward voltage	V_{F}	I _F = 5 A, T _J = 150 °C	-	0.92	1.15	
Reverse leakage current	1_	V _R = V _R rated	-	-	3	
	I _R	T _J = 150 °C, V _R = V _R rated	-	-	100	μA
Junction capacitance	C _T	V _R = 600 V	-	7.8	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	45	-	
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	50	-	
Reverse recovery time	t _{rr}	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	65	ns
		T _J = 25 °C		-	43	-	
		T _J = 125 °C		-	81	-	
Peak recovery current	I _{RRM}	T _J = 25 °C	I _F = 5 A dI _F /dt = 200 A/μs V _B = 390 V	-	6.1	-	Α
Feak recovery current		T _J = 125 °C		-	8.4	-	^
Poverne receivent charge	0	T _J = 25 °C		-	133	-	nC
Reverse recovery charge	ery charge Q _{rr}	T _J = 125 °C	-	347	-	110	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount	R _{thJM} ⁽¹⁾		-	-	14	°C/W
Thermal resistance, junction to ambient	R _{thJA} (1)		-	-	80	C/VV
Approximate Weight				0.24		g
Approximate weight				0.008		oz.
Marking device		Case style SMC (DO-214AB)		51	J6	

Note

⁽¹⁾ Mounted on PCB with minimum pad size

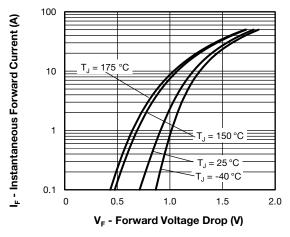


Fig. 1 - Typical Forward Voltage Drop Characteristics

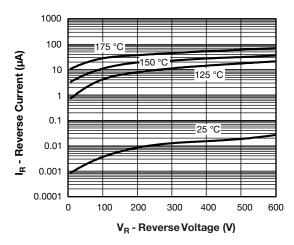


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



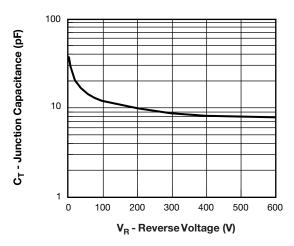


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

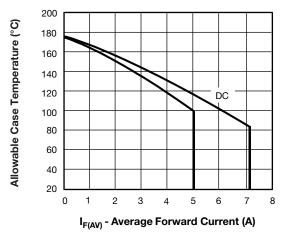


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

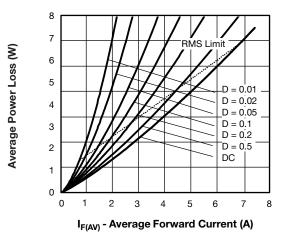


Fig. 5 - Forward Power Loss Characteristics

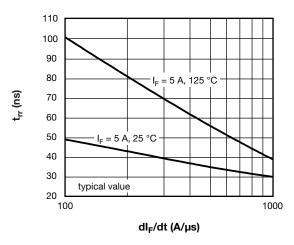


Fig. 6 - Typical Reverse Recovery vs. dl_F/dt

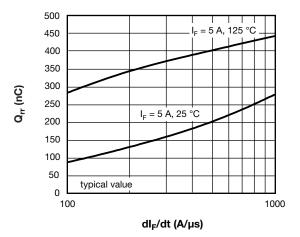
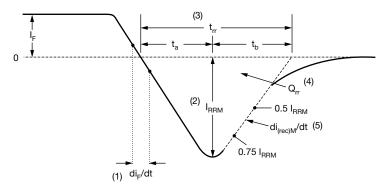


Fig. 7 - Typical Stored Charge vs. dl_F/dt

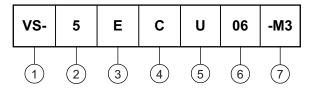


- (1) di_F/dt rate of change of current through zero crossing
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (5) di_{(rec)M}/dt peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- 2 Current rating (5 = 5 A)
- 3 Circuit configuration:

E = single diode

- C = SMC package

5 - Process type

U = ultrafast recovery

Voltage code (06 = 600 V)

7 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-5ECU06-M3/9AT	9AT	3500	13"diameter plastic tape and reel		

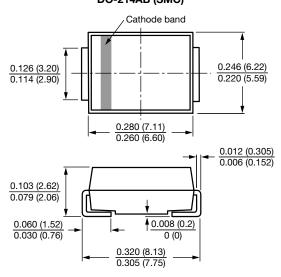
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95402			
Part marking information	www.vishay.com/doc?95472			
Packaging information	www.vishay.com/doc?95404			



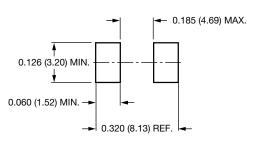
SMC

DIMENSIONS in inches (millimeters)

DO-214AB (SMC)



Mounting Pad Layout





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