

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
80V	26mΩ @ V _{GS} = 10V	28.5A
	45mΩ @ V _{GS} = 4.5V	21A

Description and Applications

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

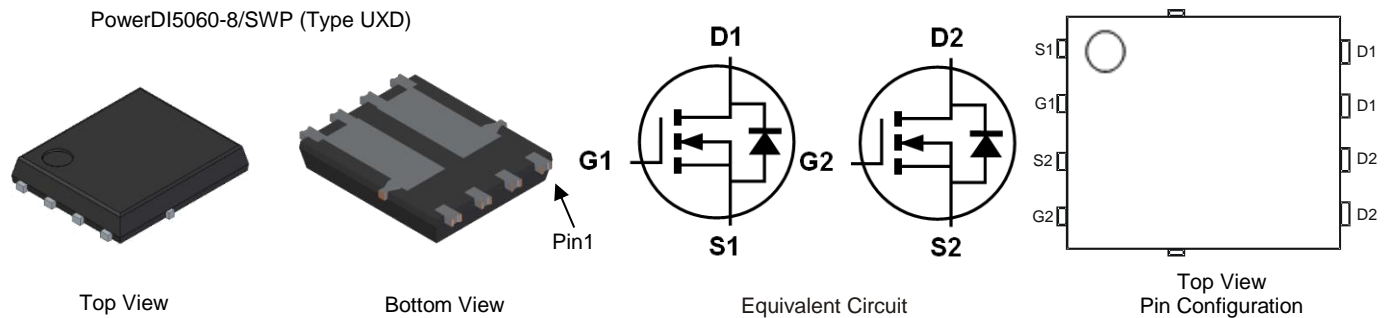
- DC-DC converters
- Motors

Features and Benefits

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Additional Tin-Plated on Sidewall Pads for Optical Solder Inspection
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([DMTH8030LPDWQ](#))**

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 [Ⓔ]
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMTH8030LPDW-13	PowerDI5060-8/SWP (Type UXD)	2500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



DII = Manufacturer's Marking
 H8030LPDW = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 22 = 2022)
 WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	80	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 6)	I _D	T _C = +25°C 28.5	A
		T _C = +100°C 20	
Maximum Body Diode Forward Current	I _S	29	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	113.5	A
Pulsed Body Diode Forward Current (10µs Pulse, T _C = +25°C, Package Limited)	I _{SM}	113.5	A
Avalanche Current, L = 0.3mH	I _{AS}	12.5	A
Avalanche Energy, L = 0.3mH	E _{AS}	23.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	48	°C/W
Total Power Dissipation	P _D	T _A = +25°C 3.1	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	
Total Power Dissipation	P _D	T _C = +25°C 41	W
Operating and Storage Temperature Range		T _J , T _{STG}	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 6. Thermal resistance from junction to solder point (on the exposed drain pin).

Electrical Characteristics (@T_C = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	80	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 64V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.3	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	20	26	mΩ	V _{GS} = 10V, I _D = 10A
		—	29	45		V _{GS} = 4.5V, I _D = 5A
Diode Forward Voltage	V _{SD}	—	0.9	1.2	V	V _{GS} = 0V, I _S = 10A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	631	—	pF	V _{DS} = 40V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	200	—		
Reverse Transfer Capacitance	C _{rss}	—	19.5	—		
Gate Resistance	R _G	—	1.1	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	5.4	—	nC	V _{DS} = 40V, I _D = 7.5A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	10.4	—		
Gate-Source Charge	Q _{gs}	—	1.8	—		
Gate-Drain Charge	Q _{gd}	—	2.4	—		
Turn-On Delay Time	t _{D(ON)}	—	7.1	—	ns	V _{DD} = 40V, V _{GS} = 4.5V, R _G = 2.7Ω, I _D = 10A
Turn-On Rise Time	t _R	—	9.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	18.6	—		
Turn-Off Fall Time	t _F	—	8.6	—		
Body Diode Reverse Recovery Time	t _{RR}	—	28.5	—	ns	I _F = 7.5A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	21.7	—	nC	I _F = 7.5A, di/dt = 100A/μs

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

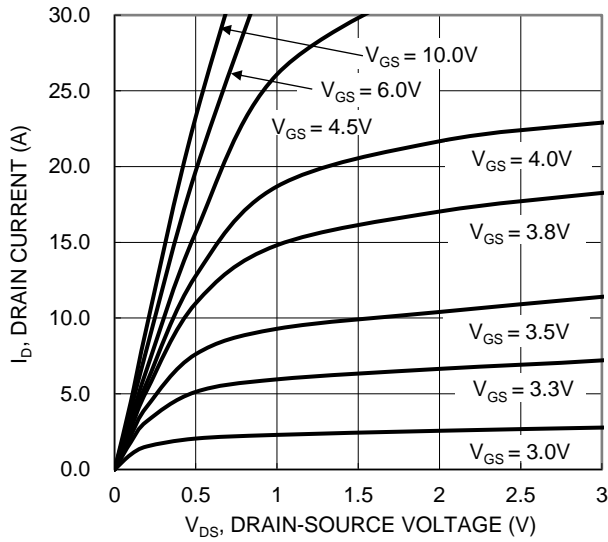


Figure 1. Typical Output Characteristic

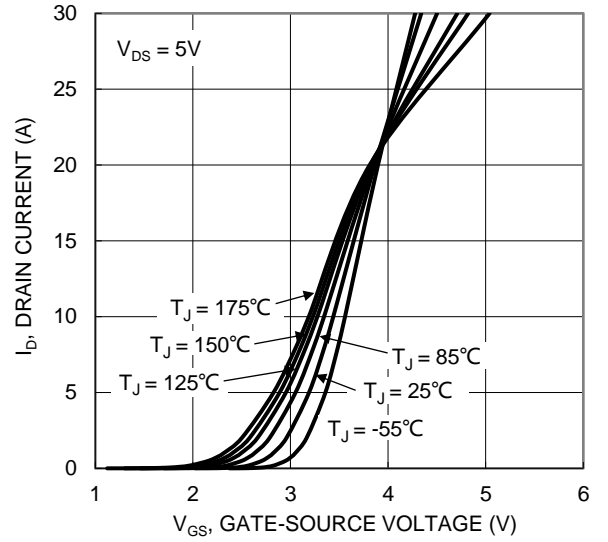


Figure 2. Typical Transfer Characteristic

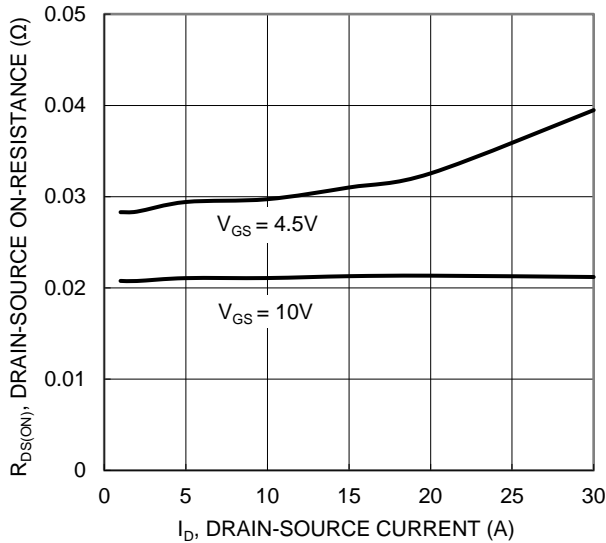


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

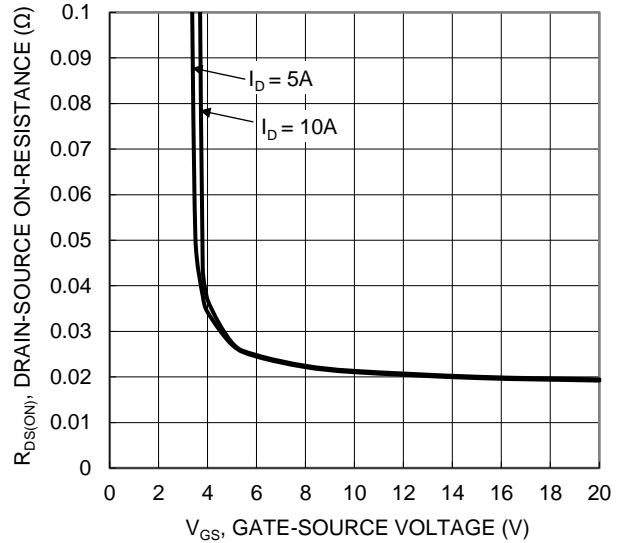


Figure 4. Typical Transfer Characteristic

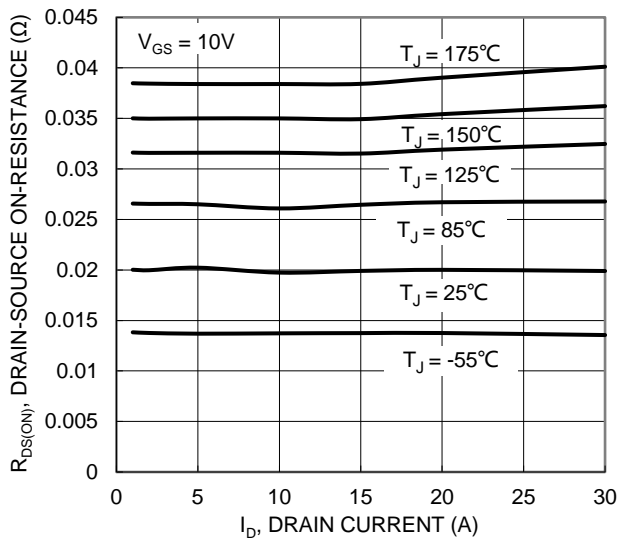


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

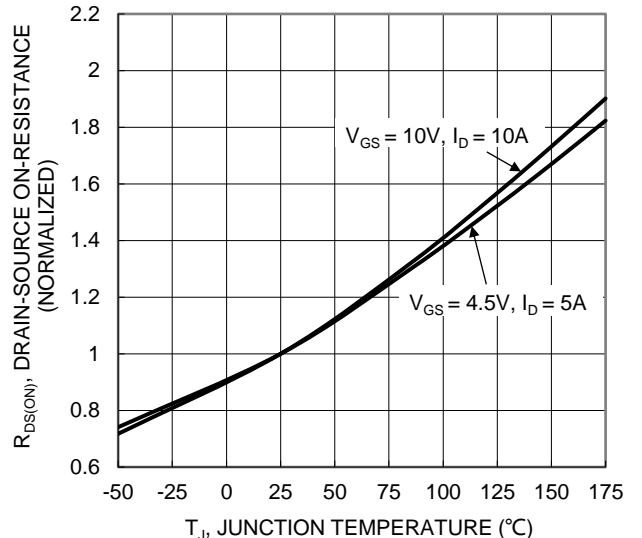


Figure 6. On-Resistance Variation with Junction Temperature

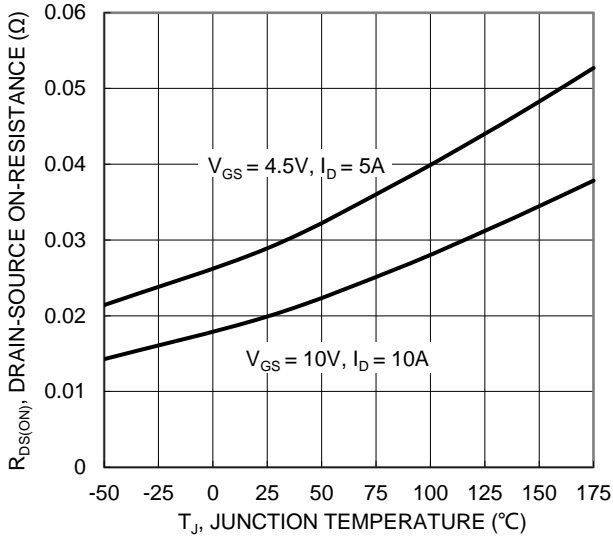


Figure 7. On-Resistance Variation with Junction Temperature

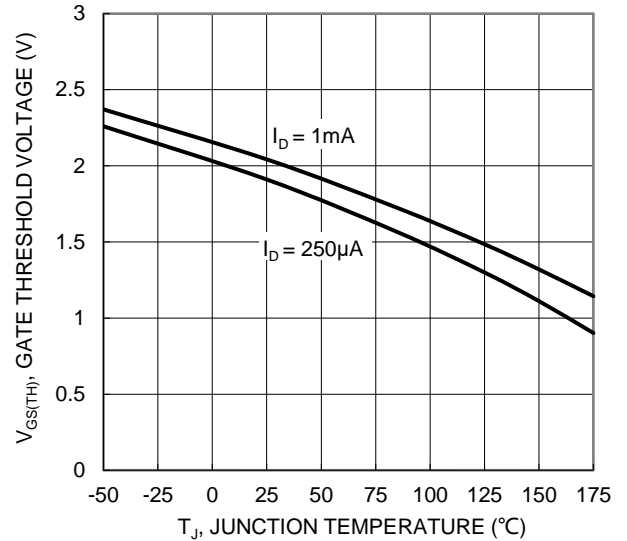


Figure 8. Gate Threshold Variation vs. Junction Temperature

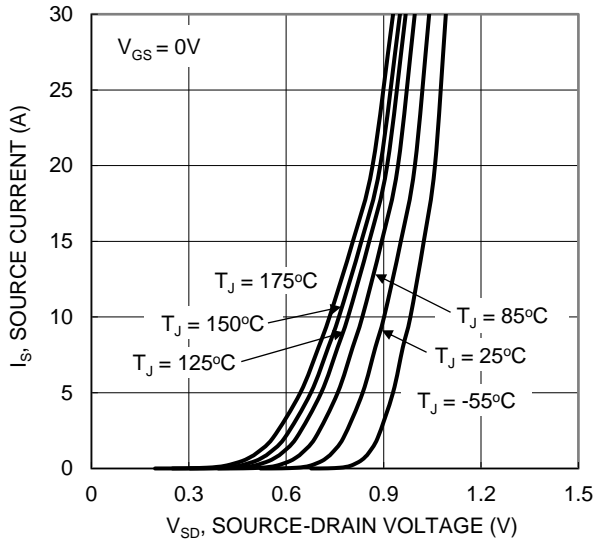


Figure 9. Diode Forward Voltage vs. Current

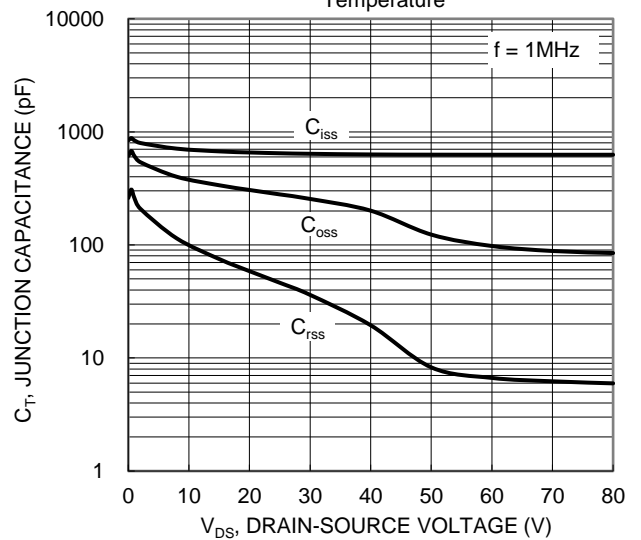


Figure 10. Typical Junction Capacitance

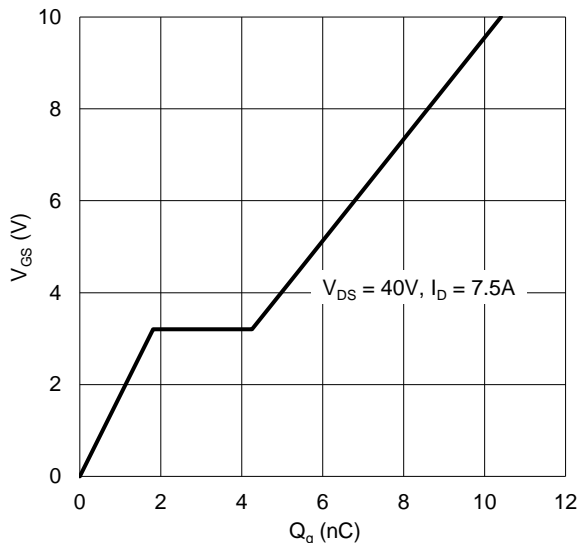


Figure 11. Gate Charge

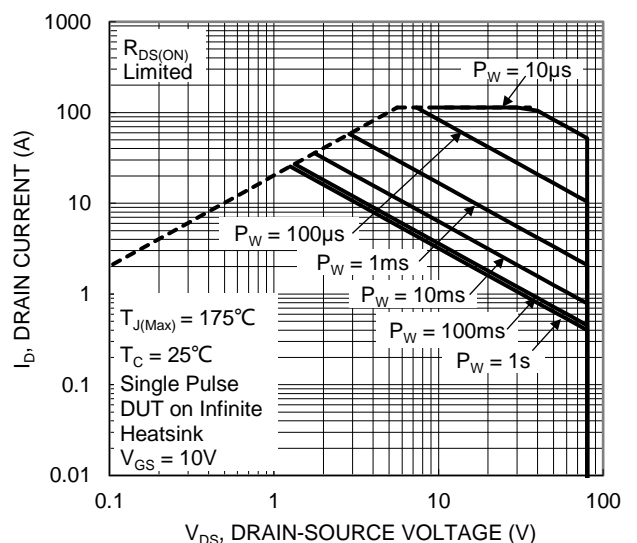


Figure 12. SOA, Safe Operation Area

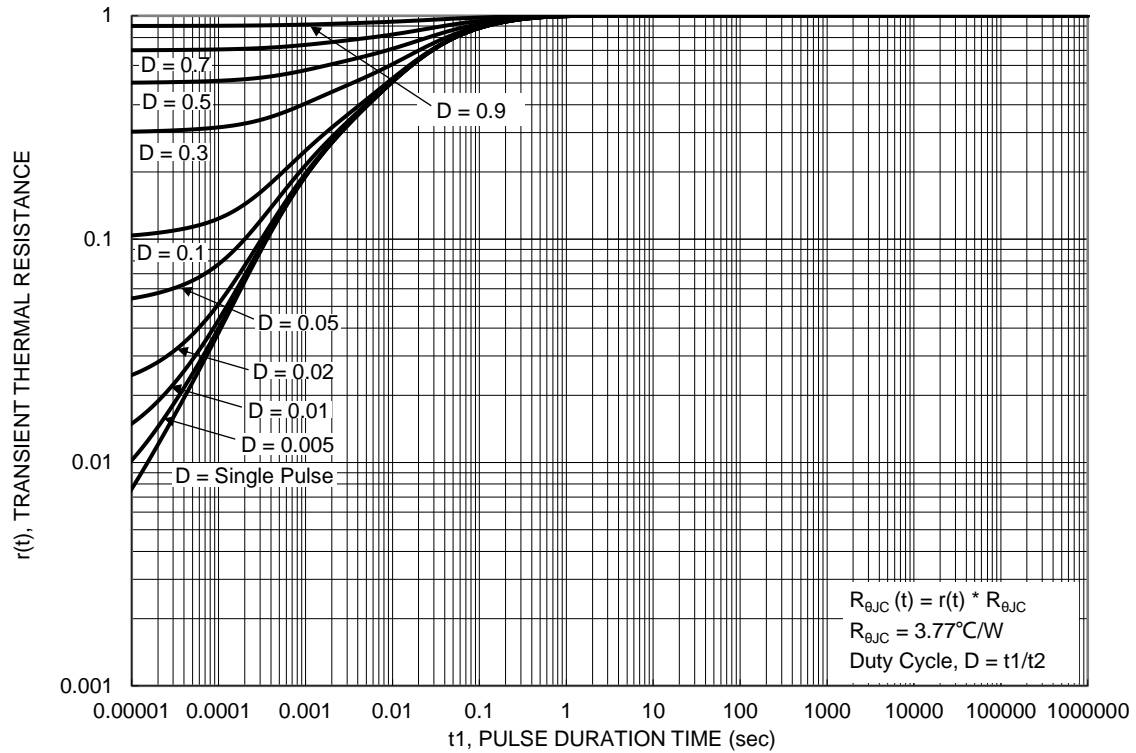
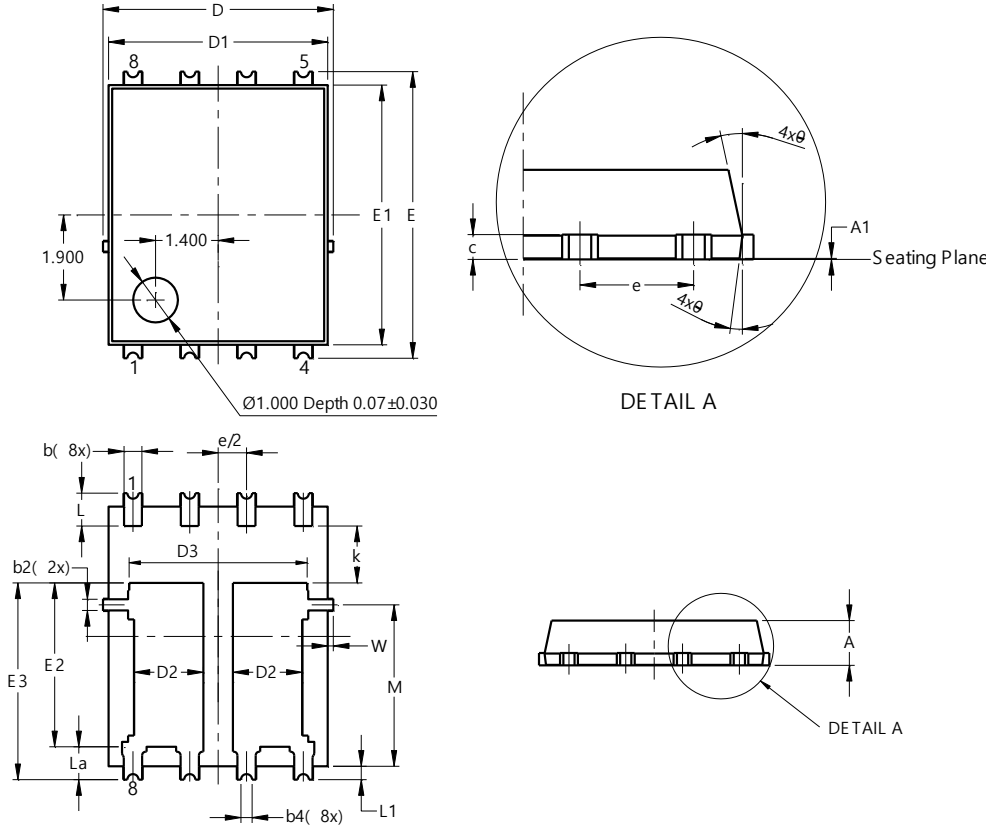


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UXD)

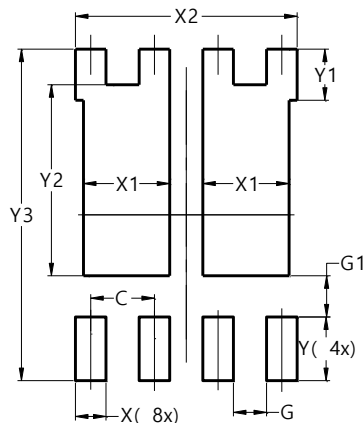


PowerDI5060-8/SWP (Type UXD)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	1.46	1.66	1.55
D3	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
M	3.205	4.005	3.605
W	0.025	0.225	0.125
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UXD)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	1.720
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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