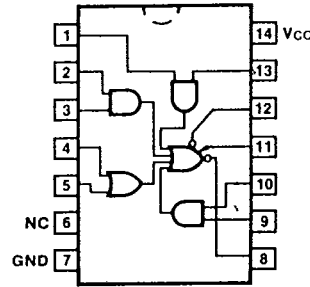


54/7453
54H/74H53

EXPANDABLE 4-WIDE, 2-INPUT AOI GATE ('53)
EXPANDABLE 2-2-2-3-INPUT AOI GATE ('H53)

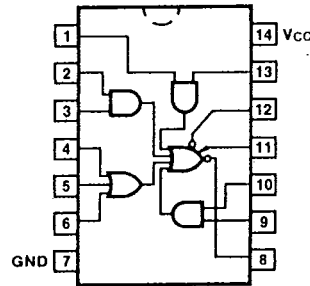
CONNECTION DIAGRAMS
PINOUT A



ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		V _{CC} = +5.0 V ±5%, T _A = 0°C to +70°C	V _{CC} = +5.0 V ±10%, T _A = -55°C to +125°C	
Plastic DIP (P)	A	7453PC		9A
	B	74H53PC		
Ceramic DIP (D)	A	7453DC	5453DM	6A
	B	74H53DC	54H53DM	
Flatpak (F)	C	7453FC	5453FM	3I
	D	74H53FC	54H53FM	

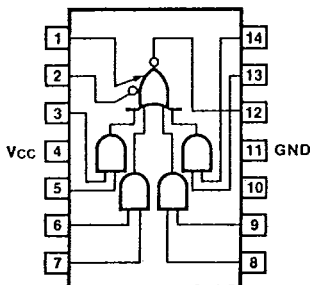
PINOUT B



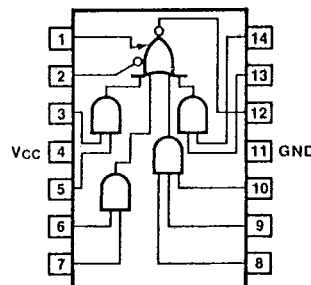
INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.) HIGH/LOW	54/74H (U.L.) HIGH/LOW
Inputs	1.0/1.0	1.25/1.25
Outputs	20/10	12.5/12.5

PINOUT C



PINOUT D



4

53

7-43-15

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE: Using Expander Pins

SYMBOL	PARAMETER	54/74		54/74H		UNITS	CONDITIONS	
		Min	Max	Min	Max			
V _{OH}	Output HIGH Voltage	XM		2.4		V	I ₁ = 320 μA I ₂ = -320 μA I _{OH} = -500 μA	
		XC		2.4				
V _{OH}	Output HIGH Voltage	XM	2.4			V	I ₁ = 0.15 mA I ₂ = -0.15 mA I _{OH} = -400 μA	
		XC	2.4					
V _{OL}	Output LOW Voltage	XM		0.4		V	I ₁ = 470 μA R ₁ = 68 Ω I _{OL} = 20 mA	
		XC		0.4				
V _{OL}	Output LOW Voltage	XM	0.4			V	I ₁ = 0.3 mA R ₁ = 138 Ω I _{OL} = 16 mA	
		XC	0.4					
V _{BE(Q)}	Base-Emitter Voltage of Output Transistor Q	XM		1.0		V	I ₁ = 700 μA I _{OL} = 20 mA R ₁ = 0 Ω	
		XC		1.0				
		XM	1.1					I _{OL} = 16 mA R ₁ = 0 Ω
		XC	1.0					
I _{INX}	Expander-Node Input Current	XM		-5.85		mA	V _X = 1.4 V	
		XC		-6.3				
I _X	Expander Current	XM	2.9			mA	V ₁ = 0.4 V, I _{OL} = 16 mA	
		XC	3.1					
I _{CCH} I _{CCL}	Power Supply Current		8.0 9.5	11 14		mA	V _{CC} = Max	

AC CHARACTERISTICS: V_{CC} = +5.0 V, T_A = +25° C (See Section 3 for waveforms and load configurations)

SYMBOL	PARAMETER	54/74		54/74H		UNITS	CONDITIONS
		Min	Max	Min	Max		
t _{PLH} t _{PHL}	Propagation Delay	22 15		11 11		ns	Expander Pins Open Figs. 3-1, 3-4
t _{PLH} t _{PHL}	Propagation Delay			11.4* 7.4*			

*Typical Value

05453-2X