

39x9 DOTS MATRIX LED DRIVER

DESCRIPTION

The IS31FL3741 is a general purpose 39x9 LED Matrix programmed via an I2C compatible interface. Each LED can be dimmed individually with 8-bit PWM data and 8-bit scaling data which allowing 256 steps of linear PWM dimming and 256 steps of DC current adjustable level.

Additionally each LED open and short state can be detected, IS31FL3741 store the open or short information in Open-Short Registers. The Open-Short Registers allowing MCU to read out via I2C compatible interface. Inform MCU whether there are LEDs open or short and the locations of open or short LEDs.

FEATURES

- Supply voltage range from 2.7V to 5.5V
- 39 Current Sink x 9 SW matrix size: drive up to 351 LEDs or 117 RGBs
- Individual 256 PWM control steps
- Individual 256 DC current steps
- Global 255 current setting
- SDB rising edge reset I2C module
- Interrupt and state lookup registers
- Individual open and short error detect function
- De-ghost
- QFN-60 (7mmx7mm) package

QUICK START



Figure 1: Photo of IS31FL3741 Evaluation Board

RECOMMENDED EQUIPMENT

- 5.0V, 2A power supply

ABSOLUTE MAXIMUM RATINGS

- ≤ 5.5V power supply

Caution: Do not exceed the conditions listed above, otherwise the board will be damaged.

PROCEDURE

The IS31FL3741 evaluation board is fully assembled and tested. Follow the steps listed below to verify board operation.

Caution: Do not turn on the power supply until all connections are completed.

- 1) Connect 3V and P2.
- 2) Connect SDA and P1.
- 3) Connect SCL and P3.
- 4) Connect SDB and P4
- 5) Connect PVCC and SDB.
- 6) Turn on the power supply/Plug in the Micro USB
Pay attention to the supply current. If the current exceeds 1A, please check for circuit fault.

EVALUATION BOARD OPERATION

The IS31FL3741 evaluation board has three animation display modes. Press K1 to switch configurations.

- 1) Rainbow wave#1
- 2) Rainbow wave#2
- 3) Rain
- 4) Visor

Note: IS31FL3741 solely controls the FxLED function on the evaluation board.

ORDERING INFORMATION

Part No.	Temperature Range	Package
IS31FL3741-QFLS4-EB	-40°C to +125°C (Industrial)	QFN-60, Lead-free

Table 1: Ordering Information

For pricing, delivery, and ordering information, please contacts Lumissil's analog marketing team at analog@Lumissil.com or (408) 969-6600.

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SOFTWARE SUPPORT

P2 and 3V default setting is closed (jumper on). If it is open (no jumper), the on-board MCU will configure its own I2C/SDB/ADDR pins to High Impedance status so an external source can driver the I2C/SDB signals to control the IS31FL3741 LED driver.

The ADDR pin is pulled high via R6 setting the device address of IS31FL3741 to 0x60.

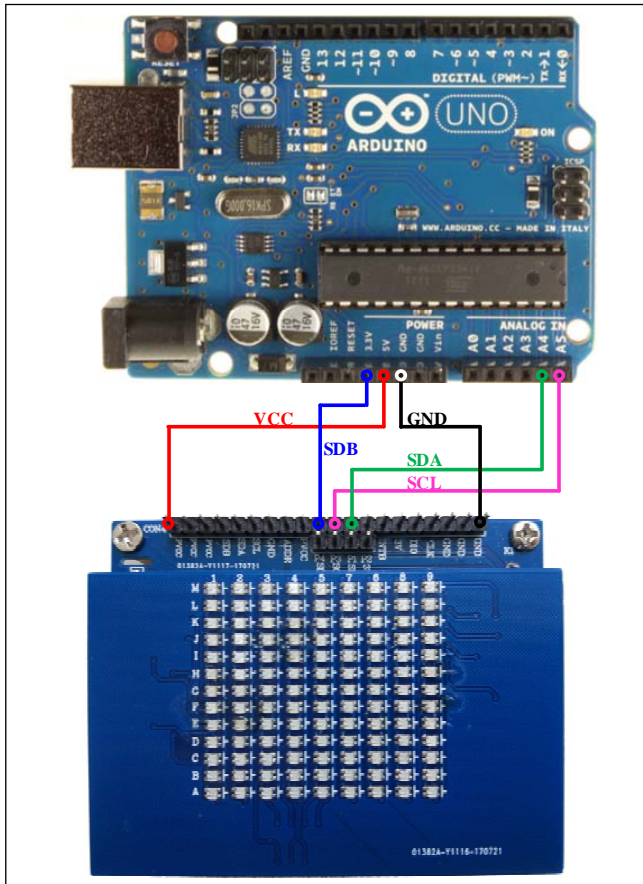


Figure 2: Photo of Arduino UNO connected to Evaluation Board

The steps listed below are an example using the Arduino for external control.

The Arduino hardware consists of an Atmel microcontroller with a bootloader allowing quick

firmware updates. First download the latest Arduino Integrated Development Environment IDE (1.6.12 or greater) from www.arduino.cc/en/Main/Software. Also download the Wire.h library from www.arduino.cc/en/reference/wire and verify that pgmspace.h is in the directory ...program Files(x86)/Arduino/hardware/tools/avr/avr/include/avr/. Then download the latest IS31FL3741 test firmware (sketch) from the Lumissil website <http://www.lumissil.com/products/led-driver/fxled>.

- 1) Open P2 and 3V.
- 2) Open PVCC and SDB.
- 3) Connect the 5 pins from Arduino board to IS31FL3741 EVB:
 - a) Arduino 5V pin to IS31FL3741 EVB PVCC.
 - b) Arduino GND to IS31FL3741 EVB GND.
 - c) Arduino SDA (A4) to IS31FL3741 EVB P1.
 - d) Arduino SCL (A5) to IS31FL3741 EVB P3.
 - e) If Arduino use 3.3V MCU VCC, connect 3.3V to IS31FL3741 EVB SDB, if Arduino use 5.0V MCU VCC, connect 5.0V to EVB SDB.
(Arduino UNO MCU VCC is 5V, so SDB can be 5V or 3.3V)
- 4) Use the test code in appendix I or download the test firmware (sketch) from the Lumissil website, a .txt file and copy the code to Arduino IDE, compile and upload to Arduino.
- 5) Run the Arduino code as appendix I.
- 6) In EVB code, the ADDR pin is pulled low via 100k, so the device address is 0x60. When P1~P4 are disconnect, the ADDR pin can be changed by connecting the ADDR as follows:
 - a) ADDR=VCC, device address=0x66.
 - b) ADDR=SCL, device address=0x62.
 - c) ADDR=SDA, device address=0x64.

Please refer to the datasheet to get more information about IS31FL3741.

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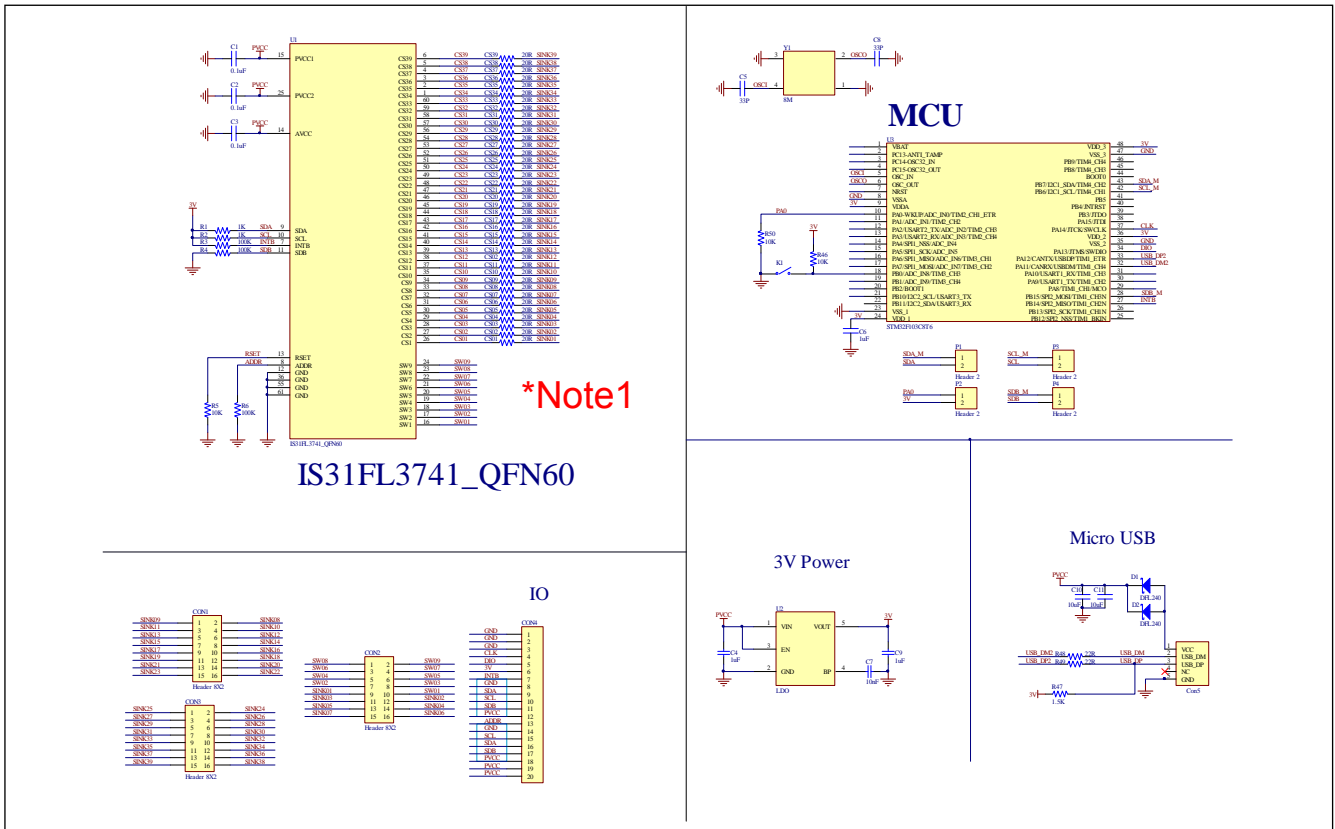
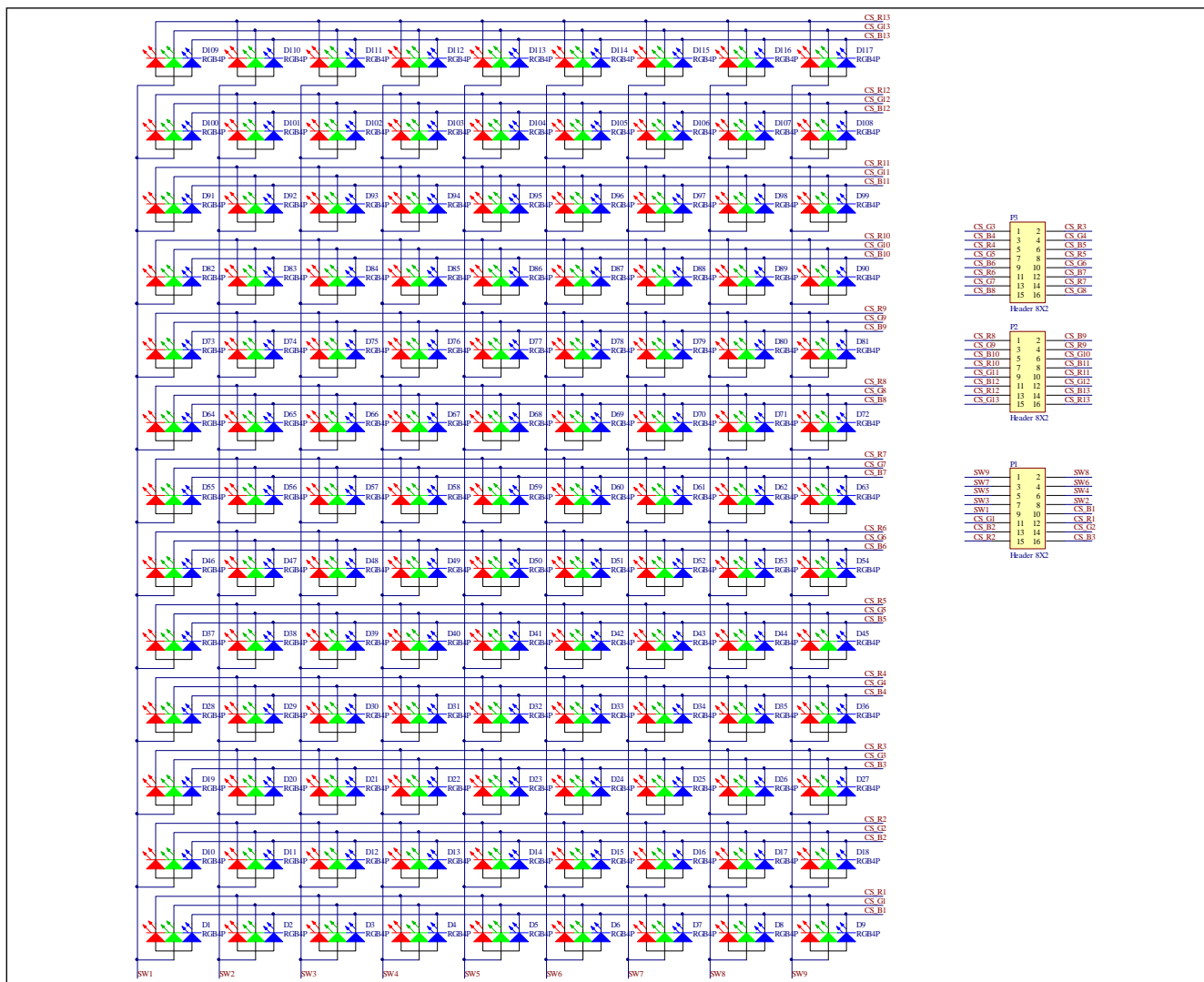


Figure 3: IS31FL3741 Application Schematic

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Header SX2			
CS_G1	P1	1	CS_B3
CS_B4		2	CS_G4
CS_B4		3	CS_B5
CS_B4		4	CS_B5
CS_G5		5	CS_B6
CS_B6		6	CS_B6
CS_B6		7	CS_B7
CS_B6		8	CS_B7
CS_B6		9	CS_B8
CS_B6		10	CS_B8
CS_B6		11	CS_B9
CS_B6		12	CS_B9
CS_B6		13	CS_B10
CS_B6		14	CS_B10
CS_B6		15	CS_B11
CS_B6		16	CS_B11

Header SX2			
CS_B8	P2	1	CS_B9
CS_B10		2	CS_B9
CS_B10		3	CS_B10
CS_B10		4	CS_B10
CS_B10		5	CS_B11
CS_B10		6	CS_B11
CS_B10		7	CS_B11
CS_B10		8	CS_B11
CS_B10		9	CS_B12
CS_B10		10	CS_B12
CS_B10		11	CS_B12
CS_B10		12	CS_B12
CS_B10		13	CS_B13
CS_B10		14	CS_B13
CS_B10		15	CS_B13
CS_B10		16	CS_B13

Header SX2			
SW9	P1	1	SW8
SW7		2	SW6
SW5		3	SW4
SW3		4	SW2
SW1		5	SW1
SW1		6	SW1
SW1		7	SW2
SW1		8	SW2
SW1		9	SW3
SW1		10	SW3
SW1		11	SW4
SW1		12	SW4
SW1		13	SW5
SW1		14	SW5
SW1		15	SW6
SW1		16	SW6

Figure 4: FxLED_9x13_ARRAY Application Schematic

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BILL OF MATERIALS

IS31FL3741

Name	Symbol	Description	Qty	Supplier	Part No.
LED Driver	U1	Matrix LED Driver	1	Lumissil	IS31FL3741
LDO	U2	Reduced voltage	1	SGMICRO	SGM2019-3.3V
MCU	U3	Microcontroller	1	STM	STM32F103C8T6
Diode	D1,D2	Diode, SMD	2	DIODES	DFLS240
Crystal	Y1	Crystal, 8MHz	1	JB	HC-49S
Resistor	R1,R2	RES,4.7k,1/16W,±5%,SMD	2	Yageo	RC0603JR-074K7L
Resistor	R3,R4,R6	RES,100K,1/16W,±5%,SMD	3	Yageo	RC0603JR-07100KL
Resistor	R5,R50,R46	RES,10k,1/16W,±5%,SMD	3	Yageo	RC0603JR-0710KL
Resistor	R47	RES,1.5k,1/16W,±5%,SMD	1	Yageo	RC0603JR-071K5L
Resistor	R48,R49	RES,22R,1/16W,±5%,SMD	2	Yageo	RC0603JR-0722RL
Resistor	CS01,CS02, CS04,CS05, ~ CS34,CS35, CS37,CS38	RES,20R,1/10W,±5%,SMD	26	Yageo	RC0603JR-0720RL
Resistor	CS03,CS06, CS09,CS12, ~ CS36,CS39	RES,20R,1/10W,±5%,SMD (Note 1)	13	Yageo	RC0603JR-0720RL
Capacitor	C1,C2,C3	CAP,0.1µF,16V,±20%,SMD	3	Yageo	CC0603MRX7R7BB104
Capacitor	C4,C6,C9	CAP,1µF,16V,±20%,SMD	3	Yageo	CC0603KRX7R7BB105
Capacitor	C5,C8	CAP,33pF,16V,±20%,SMD	2	Yageo	CQ0603JRNPO9BN330
Capacitor	C7	CAP,10nF,16V,±20%,SMD	1	Yageo	CC0603KRX7R7BB103
Capacitor	C10,C11	CAP,10uF,16V, ±20%,SMD	2	Yageo	CC0805MKX5R7BB106
Button	K1	Button SMD	1		

Bill of Materials, refer to Figure 3 above.

FxLED 9x13 ARRAY

Name	Symbol	Description	Qty	Supplier	Part No.
Diode	D1~D117	RGB LED, SMD	117	Everlight	9-237/R6GHBHC-A01/2T

Bill of Materials, refer to Figure 4 above.

Note 1: The value of these resistors on the evaluation board is 20Ω. For PVCC=5V and red LED application, prefer 51Ω for these resistors as shown in datasheet Figure 1.

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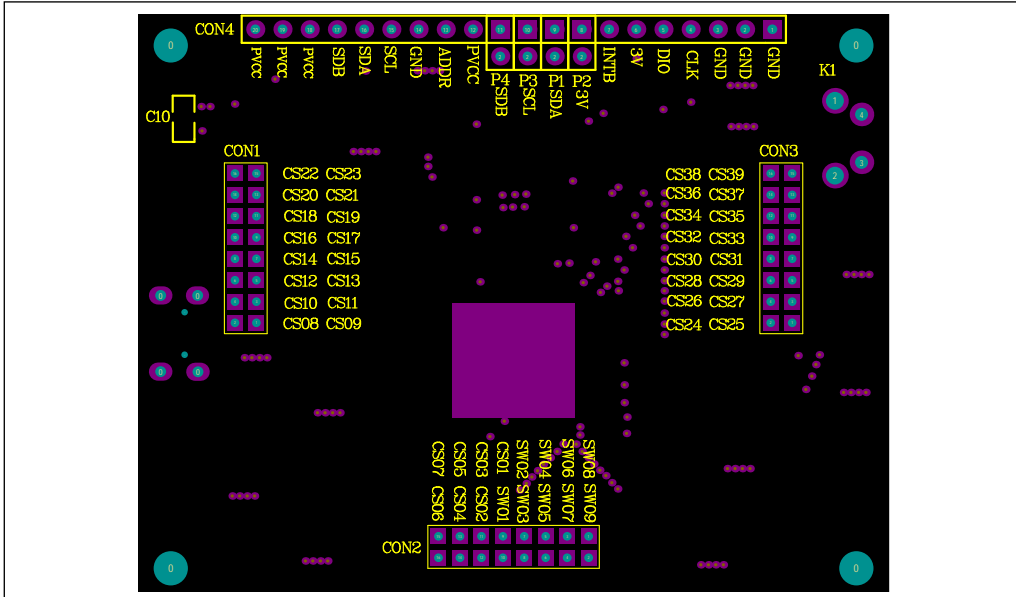


Figure 5: Board Component Placement Guide - Top Layer

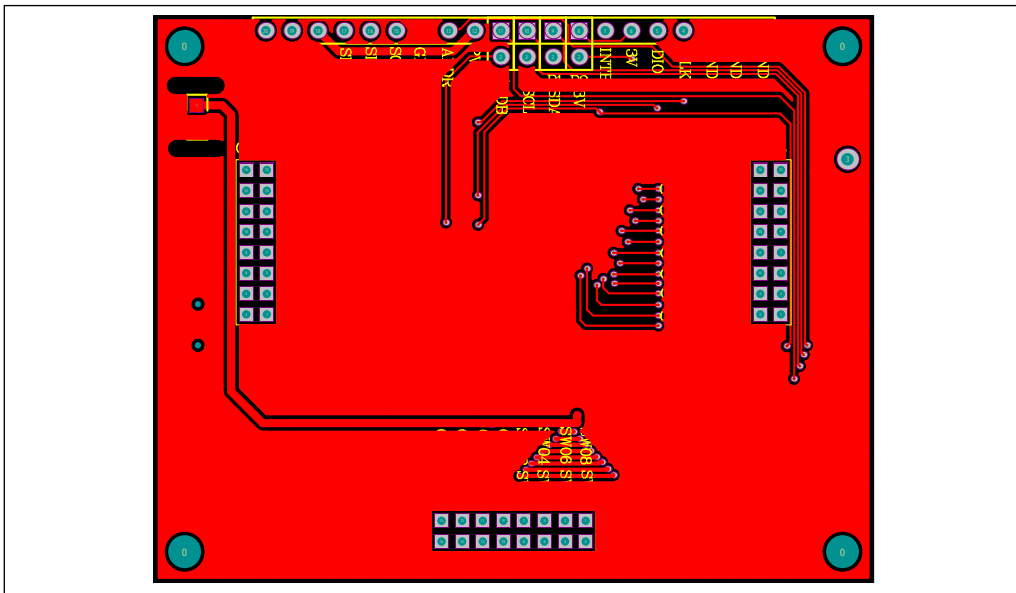


Figure 6: Board PCB Layout - Top Layer

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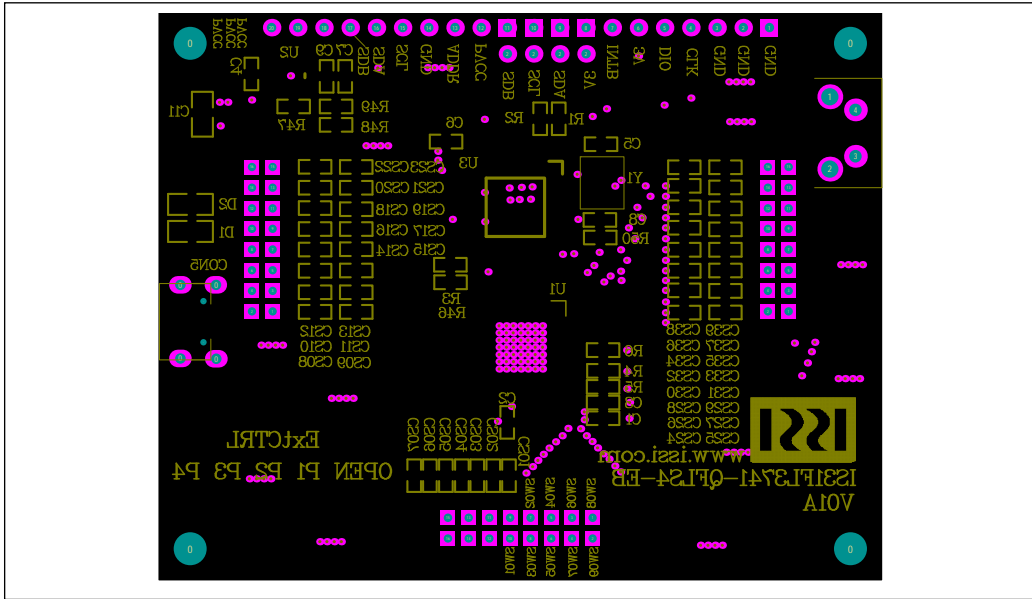


Figure 7: Board Component Placement Guide - Bottom Layer

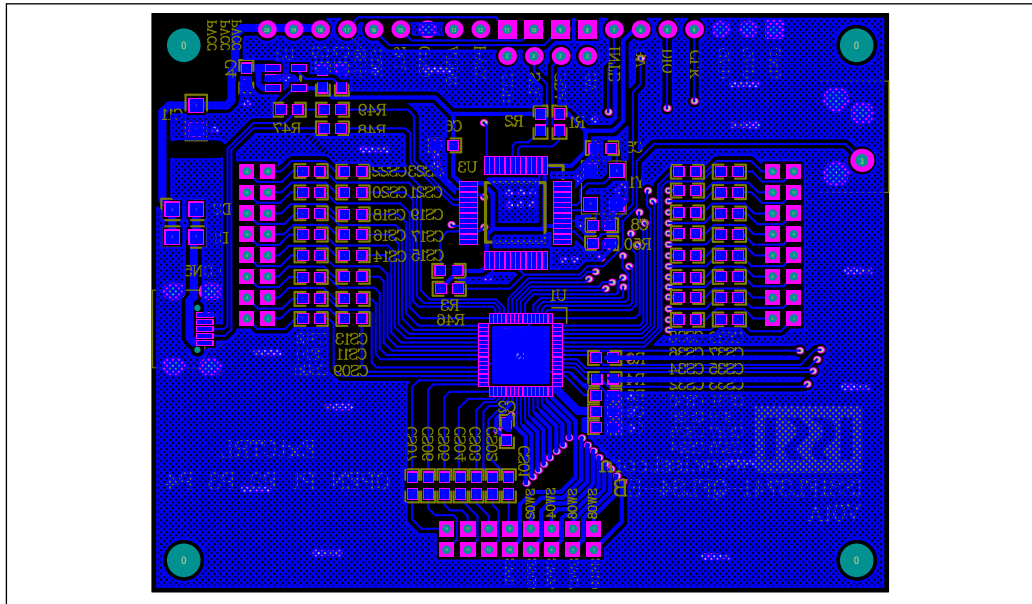


Figure 8: Board PCB Layout - Bottom Layer

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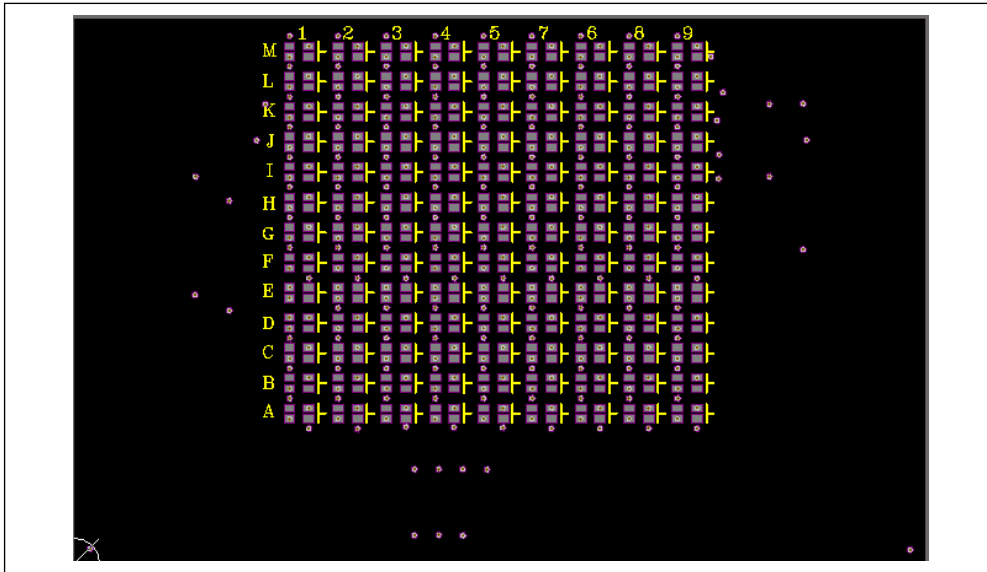


Figure 9: LED Board Component Placement Guide - Top Layer

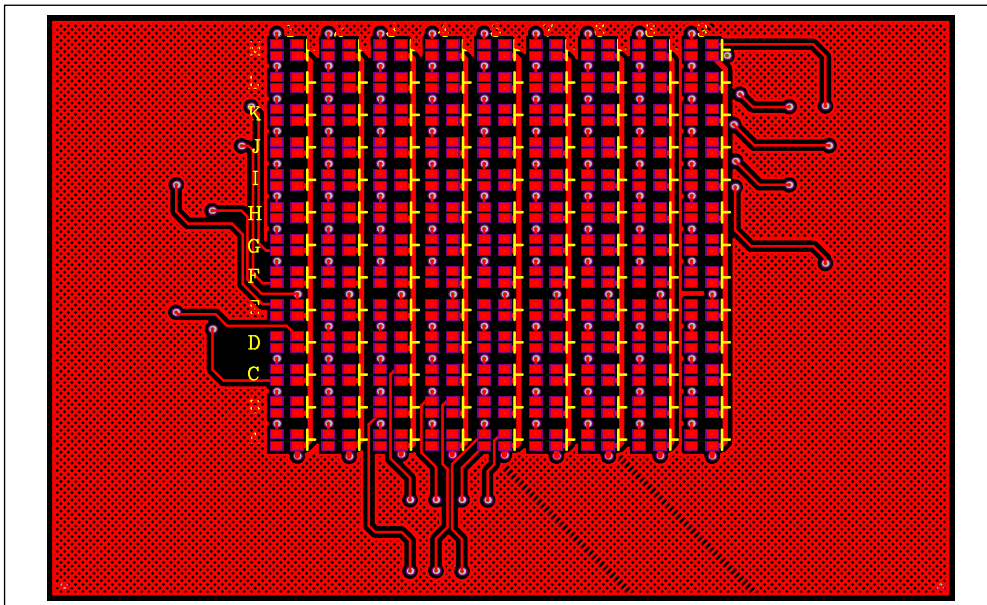


Figure 10: LED Board PCB Layout - Top Layer

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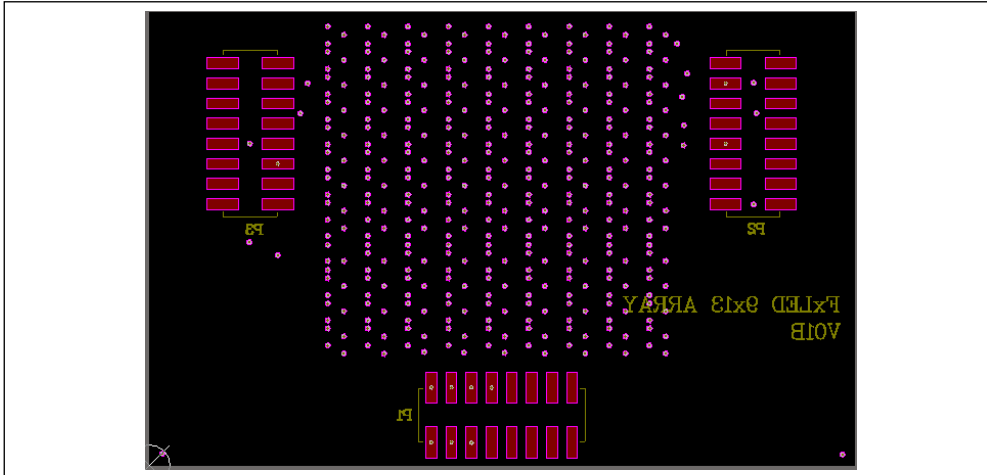


Figure 11: LED Board Component Placement Guide - Bottom Layer

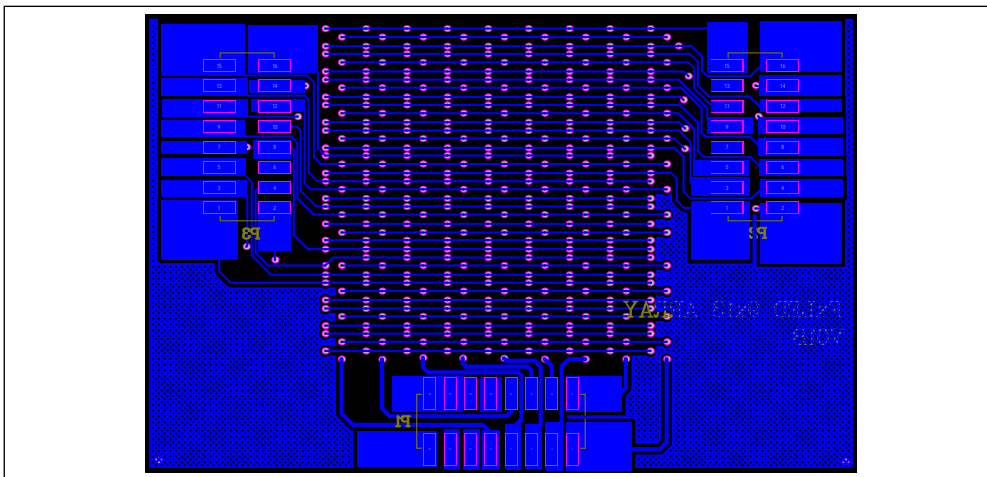


Figure 12: LED Board PCB Layout - Bottom Layer

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REVISION HISTORY

Revision	Detail Information	Date
A	Initial release	2017.09.07
B	Update schematic and bill of materials.	2018.12.19

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APPENDIX I : IS31FL3741 Arduino Test Code V01A

```
#include<Wire.h>
#include<avr/pgmspace.h>

#define Addr_GND 0x60
#define R 0xFF
#define G 0x00
#define B 0xFF
uint8_t i,j;

const PROGMEM byte PWM_Gama64[64]=
{
    0x00,0x01,0x02,0x03,0x04,0x05,0x06,0x07,
    0x08,0x09,0x0b,0x0d,0x0f,0x11,0x13,0x16,
    0x1a,0x1c,0x1d,0x1f,0x22,0x25,0x28,0x2e,
    0x34,0x38,0x3c,0x40,0x44,0x48,0x4b,0x4f,
    0x55,0x5a,0x5f,0x64,0x69,0x6d,0x72,0x77,
    0x7d,0x80,0x88,0x8d,0x94,0x9a,0xa0,0xa7,
    0xac,0xb0,0xb9,0xbf,0xc6,0xcb,0xcf,0xd6,
    0xe1,0xe9,0xed,0xf1,0xf6,0xfa,0xfe,0xff
};

void setup() {
    Wire.begin();
    Wire.setClock(1000000);//I2C 1MHz
    Init_3741(R, G, B);
}

void loop() {
    IS31FL3741_Test_mode1();//breath mode
}

void IS_IIC_WriteByte(uint8_t Dev_Add,uint8_t Reg_Add,uint8_t Reg_Dat)
{
    Wire.beginTransmission(Dev_Add/2); // transmit to device IS31FL373x
    Wire.write(Reg_Add); // sends regaddress
    Wire.write(Reg_Dat); // sends regaddress
    Wire.endTransmission(); // stop transmitting
}

void Init_3741(uint8_t Rdata, uint8_t Gdata, uint8_t Bdata)
{
    IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
    IS_IIC_WriteByte(Addr_GND,0xfd,0x02);//write page 2
    for(i=2;i<0xB4;i+=3)
```

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```

{
    IS_IIC_WriteByte(Addr_GND,i,Rdata);//R LED Scaling
}
for(i=1;i<0xB4;i+=3)
{
    IS_IIC_WriteByte(Addr_GND,i,Gdata);//G LED Scaling
}
for(i=0;i<0xB4;i+=3)
{
    IS_IIC_WriteByte(Addr_GND,i,Bdata);//B LED Scaling
}
IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
IS_IIC_WriteByte(Addr_GND,0xfD,0x03);//write page 3
for(i=2;i<0xAB;i+=3)
{
    IS_IIC_WriteByte(Addr_GND,i,Rdata);//R LED Scaling
}
for(i=1;i<0xAB;i+=3)
{
    IS_IIC_WriteByte(Addr_GND,i,Gdata);//G LED Scaling
}
for(i=0;i<0xAB;i+=3)
{
    IS_IIC_WriteByte(Addr_GND,i,Bdata);//B LED Scaling
}

IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
IS_IIC_WriteByte(Addr_GND,0xfD,0x00);//write page 0
for(i=0;i<0xB4;i++)
{
    IS_IIC_WriteByte(Addr_GND,i,0x00);//write all PWM set 0x00
}
IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
IS_IIC_WriteByte(Addr_GND,0xfD,0x01);//write page 1
for(i=0;i<0xAB;i++)
{
    IS_IIC_WriteByte(Addr_GND,i,0x00);//write all PWM set 0x00
} //init all the PWM data to 0

IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
IS_IIC_WriteByte(Addr_GND,0xfD,0x04);//write page 4
IS_IIC_WriteByte(Addr_GND,0x01,0x7F);//global current
IS_IIC_WriteByte(Addr_GND,0x00,0x01);//normal operation
}

```

```
void IS31FL3741_Test_mode1(void)//
```

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```

{
  IS_IIC_WriteByte(Addr_GND,0xFD,0x00);//write frist frame
  for (j=0;j<64;j++)//all LED ramping up
  {
    IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
    IS_IIC_WriteByte(Addr_GND,0xFD,0x00);//write page 0
    for(i=0;i<0xB4;i++)
    {
      IS_IIC_WriteByte(Addr_GND,i,pgm_read_byte_near(&PWM_Gama64[j]));//set all PWM
    }
    IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
    IS_IIC_WriteByte(Addr_GND,0xFD,0x01);//write page 1
    for(i=0;i<0xAB;i++)
    {
      IS_IIC_WriteByte(Addr_GND,i,pgm_read_byte_near(&PWM_Gama64[j]));//set all PWM
    }
    delay(10);//10ms
  }
  delay(1000); //keep on 1s

  for (j=63;j>0;j--)//all LED ramping down
  {
    IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
    IS_IIC_WriteByte(Addr_GND,0xFD,0x00);//write page 0
    for(i=0;i<0xB4;i++)
    {
      IS_IIC_WriteByte(Addr_GND,i,pgm_read_byte_near(&PWM_Gama64[j-1]));//set all PWM
    }
    IS_IIC_WriteByte(Addr_GND,0xfe,0xc5);//unlock
    IS_IIC_WriteByte(Addr_GND,0xFD,0x01);//write page 1
    for(i=0;i<0xAB;i++)
    {
      IS_IIC_WriteByte(Addr_GND,i,pgm_read_byte_near(&PWM_Gama64[j-1]));//set all PWM
    }
    delay(10);//10ms
  }
  delay(500); //keep off 0.5s
}

```