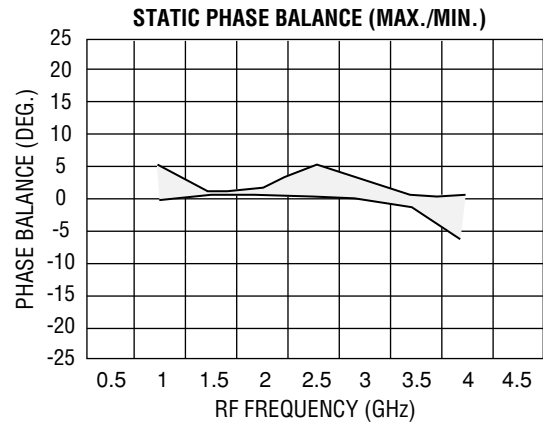
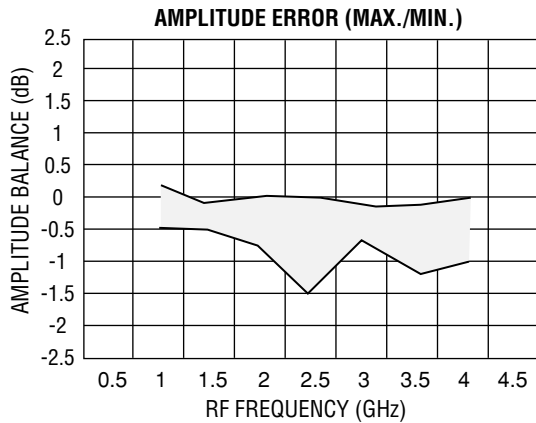


SDM0104LC1MDQ MODULATION DRIVEN TYPICAL TEST DATA

RF Phase (Deg.)	0	+90	-90	+180
I/Q	+/+	-/+	+/-	-/-

I/Q MODE (RF = 0 dBm, I/Q = +10 dBm or ±10 mA)



SDM0104LC1MDC MODULATION DRIVEN OUTPUT SPECTRUM TABLE

SSB UPCONVERTER (RF = 0 dBm, IF = +10 dBm total, IF = 100 MHz)

Frequency (GHz)	$f_0 + IF$ (I.L., dB) Note 1	$f_0 - IF$ (dBc)	f_0 (dBc)	$f_0 - 2 IF$ (dBc)	$f_0 + 2 IF$ (dBc)	$f_0 - 3 IF$ (dBc)	$f_0 + 3 IF$ (dBc)
1	8.8	26	39	50	51	13	29
1.5	6	36	43	52	53	11	30
2	5.7	37	45	48	53	12	26
2.5	7.2	22	48	49	50	9.5	26
3	6.5	25	44	47	52	11	28
3.5	7.3	30	40	51	54	11.5	26
4	9.8	27	46	46	45	10	24

MAXIMUM RATINGS

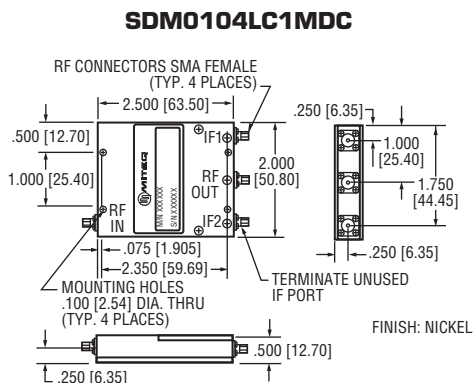
Specification temperature +25°C
 Operating temperature -54 to +85°C
 Storage temperature -65 to +125°C

GENERAL NOTES

1. Insertion loss relative to +3 dBm input. All other outputs, including f_0 , are relative to the desired upper ($f_0 + f_m$) output.
 2. Standard SSB units with IF hybrids are aligned for upper sideband operation. For lower sideband or selectable sideband, contact MITEQ.
 - *3. Available part numbers: SDM0104LC1MD_*
- A = 20–40 MHz
 B = 40–80 MHz
 C = 100–200 MHz
 Q = DC–500 MHz

NOTE: Test data supplied at 25°C; insertion loss, phase and amplitude balance per spectrum table.

OUTLINE DRAWING



NOTE: All dimensions shown in brackets [] are in millimeters.

BLOCK DIAGRAMS

